# **Empirical Small-Signal Modeling of Switching Converters Using Pspice**

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Abstract-- The objective of this paper is to present a new technique for generating small-signal transfer functions of switching converters. The technique requires generation of the input-output data from a switching converter using any simulation software packages, such as Pspice, and applies a simple algorithm on the recorded data to identify an AutoRegressive Moving Average (ARMA) model for the switching converter. A small-signal transfer function can be obtained from the ARMA model thereby.

#### I. INTRODUCTION

Small-signal modeling of switching regulators has always been considered as a key parameter for feedback control implementation. Generation of converter small-signal models prior to hardware implementation is primordial because it can save time and development cost. In this paper, an efficient method for generating small-signal models for switching converters is presented.

Several approaches, such as state-space averaging [4], discrete modeling, and harmonics-balancing technique [3], have been proposed in an attempt to generalize small-signal models for switching converters. However, the state-space averaging method is limited only to PWM converters, and the other two approaches, even though applicable also to resonant converters, are too complicated and difficult to apply. The proposed modeling technique adopts an inputoutput data analysis point of view. It presumably treats any converter as a black box and identifies the unknown smallsignal model of the converter based on its input/output response. The approach is efficient, accurate up to half of the switching frequency, and suitable for identification of various types of converters. As illustrated in Fig. 1, the smallsignal modeling procedure consists of three steps: (1) Any converter can be modeled using Pspice for data generation, (2) a simple algorithm is performed on the data to approximate the equivalent AutoRegressive Moving Average (ARMA) model, and (3) conversion to transfer function is performed.

The paper is divided into two main sections. The first section addresses the theoretical underlying of the modeling technique, which includes data generation and development 0-7803-2730-6/95 \$4.00 © 1995 IEEE

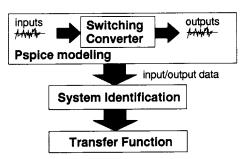


Fig. 1: Identification Process

of the identification algorithm. The subsequent section illustrates applicability of the modeling technique through small-signal transfer function identification of a buck converter, a series resonant converter, and a forward multi-resonant converter.

#### II. METHODOLOGY

#### A. Data Generation

Any switching converter of unknown structure can be modeled using Pspice; a disturbance of small amplitude is injected around a nominal equilibrium point of the input; and both the input perturbation and the corresponding output responses are sampled for identification. Consequently, selection of the sampling frequency and the perturbation signal is primordial, since the sampled data must represent adequately the linear dynamic response of the converter around the steady-state operating point.

Selection of the input perturbation signal is important because in order to identify a converter accurately, the inputoutput data must contain all the different modes of the converter. To achieve this, the spectrum of the input signal
must span the entire range of frequencies of interest. A
pseudo-random binay signal (PRBS) inherents such a property [2]. It is a binary signal that swiches randomly to either A or -A at every  $\Delta t$  and the random sequence repeats every
period T, as shown in Fig. 2. Figure 3 illustrates the spectrum of a PRBS, which consists of a train of diract deltas
following the envelop of the square of a Sa(t) function  $(\sin(t)/t)$ .

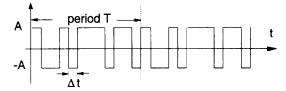


Fig. 2: Pseudorandom Binary Signal (PRBS)

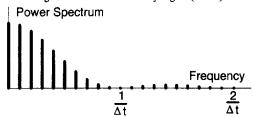


Fig. 3: Spectrum of PRBS

Since the information collected from the unknown system is in the form of measured input-output data,  $\{u,y\}$ , which are recorded at a specific sampling period,  $T_d$ , the spectra of the measured signals u and y consist of replication of the true spectra of the signals u and y at multiple of the sampling frequency,  $1/T_d$  a phenomenon known as sampling effect. If the sampling frequency is selected to be smaller than half of the bandwidths of the signals u and y, the replicated spectra will overlap, and aliasing will occur, resulting in erroneous characterization of the unknown system. Therefore, selection of the perturbation frequency for the PRBSs is important. If the perturbation period,  $\Delta t$ , is the same as the sampling period,  $T_d$ , Fig. 4(a) illustrates the spectrum of the input signal,  $\mathcal{P}_{\mu}(\mathbf{f})$ , and indicates that there is still considerable power within the input signal above the Nyquist frequency  $(1/2\Delta t)$  and aliasing errors will occur. However, setting  $\Delta t =$  $2T_d$  would move the replicated spectrum toward higher frequency, and the amount of overlap between the original spectrum and the replicated spectrum would be significantly reduced, as illustrated in Fig. 4(b). Thus, the illustration in Fig. 4(b) suggests that the perturbation period,  $\Delta t$ , should be at least two times larger than  $T_d$  to avoid the problem of aliasing when sampling the input data.

A method for generating a PRBS is to use a shift register circuit (Fig. 5) which can be modeled in Pspice as shown in Appendix C. For a shift register circuit of n registers, a maximum random binary sequence period of  $2^n$  - 1 can be achieved by performing an XOR operation between the nth register and a specific mth register. For a 7-shift-register circuit, the XOR operation must be performed between the 7th register and the 3rd register to achieve a maximum random binary sequence of 127 bits. On the other hand, for a 9-shift-register circuit, the XOR operation is performed between the 9th and the 4th registers and a random binary sequence of 511 bits is generated thereby. These maximum random binary sequences are adequate since only about 500 sampled data points are needed for the identification process.

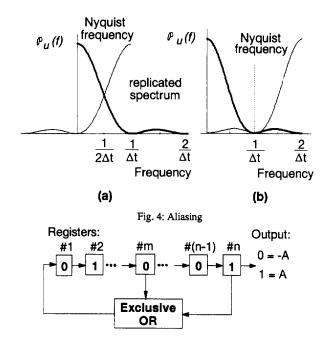


Fig. 5: Shift Register Circuit

The next issue concerns selection of the sampling frequency,  $f_d$ . For a switching converter, the outputs are usually an output voltage or an input current, and often, they can be periodic waveforms with a fundamental switching period,  $T_s$ . When dealing with such periodic signals, selection of the sampling frequency must be made carefully. These waveforms can be represented by a Fourier series expansion,

$$y(t) = K + \sum_{n=1}^{n=\infty} a_n \cos nw_s t + \sum_{n=1}^{n=\infty} b_n \sin nw_s t,$$
 (1)

where y(t) represents the converter output, and  $w_s = 2\pi f_s$  is the switching frequency of the converter, and K is a constant. When the input is selected as the input voltage or output current of the switching regulator and is perturbed with a signal of bandwidth B, the Fourier coefficients of the output y(t),  $a_n$ ,  $b_n$ , and K, becomes time-varying, and

$$y(t) = K(t) + \sum_{n=1}^{n=\infty} a_n(t) \cos nw_s t + \sum_{n=1}^{n=\infty} b_n(t) \sin nw_s t.$$
 (2)

According to the amplitude modulation theory (Appendix A), the spectrum of y(t) becomes

$$Y(f) = K(f) + \frac{1}{2} \sum_{n=1}^{n=\infty} A_n (f - nf_s) + A_n (f + nf_s) + \frac{1}{2} \sum_{n=1}^{n=\infty} B_n (f - nf_s) + B_n (f + nf_s) , \qquad (3)$$

which contains the basedband spectrum, K(f), and other centered at multiple of the switching frequency, as illustrated in Fig. 6. Therefore, if the bandwidth of the input perturbation signal, B, is larger than  $f_s/2$ , it will excite the output

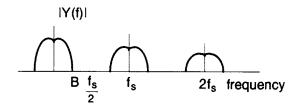


Fig. 6: Spectrum of Output y(t)

signal to have a bandwidth larger than  $f_s/2$  and the spectra will overlap, resulting in a phenomenon known as aliasing.

If the perturbed input is the duty cycle, the same phenonmenon occurs. Indeed, when the input signal v(t) is perturbed (Fig. 7), the duty cycle, d(t) may be expressed in the form of a Fourier series as

$$d(t) = \frac{A}{T_s} [D + v(t)] , \qquad (4)$$

$$+ \sum_{s} \frac{2A}{n\pi} \sin \left[ \frac{n\pi}{T_s} [D + v(t)] \right] \cos(nw_s t)$$

where A is a constant and D is the steady-state duty cycle. A quick inspection of (4) indicates that PWMing is characterized by both amplitude modulation and angle modulation. Under the assumption that v(t) is a small perturbation, |v(t)| << 1,  $\sin[Kv(t)] \approx Kv(t)$  and  $\cos[Kv(t)] \approx 1$ , where K is an arbitrary small constant. Therefore, (4) becomes

$$d(t) \approx \frac{A}{T_s} [D + v(t)] + \sum_{s} [K_1 + K_2 v(t)] \cos(n2\pi f_s t) , \qquad (5)$$

where 
$$K_1 = \frac{2A}{n\pi} \sin\left(\frac{n\pi D}{T_s}\right)$$
 and  $K_2 = \frac{2A}{T_s} \cos\left(\frac{n\pi D}{T_s}\right)$ .

Consequently, the spectrum of d(t) is given by

$$D(f) = \frac{A}{T_s} [D\delta(f) + V(f)] + \sum_{n} \frac{K_1}{2} [\delta(f - f_s) + \delta(f + f_s)] + \sum_{n} \frac{K_2}{2} [V(f - f_s) + V(f + f_s)].$$
 (6)

Equation (6) indicates that PWMing results in replicated spectra of V(f) at multiples of the switching frequency,  $f_s$ ; and therefore, D(f) has an infinite bandwidth. It follows from (3) that the replicated spectra of Y(f) overlap.

Fortunately, switching converters inherent lowpass properties, which means that the system bandwidth is usually smaller than the switching frequency, and therefore, aliasing is not too severe. For PWM converters, whose bandwidths are much smaller than the switching frequencies, it is expected that aliasing has negligible effect on the small-signal identification. However, resonant converters are characterized by resonant frequencies which are near the switching

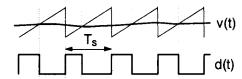


Fig. 7: Pulse Width Modulation

frequencies. It follows that aliasing may result in erroneous identification, especially in the high frequency range.

The above analysis suggests that the linear response beyond  $f_s/2$  cannot be recovered by sampling the system faster than  $f_s$  since the aliasing problem is inherent in the converter. Consequently, a sampling frequency equal to or smaller than the switching frequency of the converter should be used to determine the small-signal model of the converter.

#### B. System Identification

The sampled data are used for the identification process in the subsequent stage. One of the approaches is based on the least-square fit algorithm [1]. Qualitatively, the identification algorithm approximates the order of the unknown converter and put the measured input/output data in an AutoRegressive Moving Average (ARMA) model form, such as the following nth-order ARMA model

$$y(k) = a_0 + a_1 y(k-1) + \dots + a_{n-1} y(k-n+1) + b_0 u(k) + b_1 u(k-1) + \dots + b_n u(k-n) + e(k)$$
(7)

where e(k) represents the error between the exact response and the approximated response of the ARMA model. It is desirable to estimate the parameters  $[a_0, a_1, ..., a_{n-1}, b_0, b_1, ..., b_n]$  in such a way that the identified ARMA model will mimic as close as possible the linear response of the unknown system based on the measured sequence  $\{u,y\}$ . Substituting N sampled data points of the set  $\{u,y\}$  in (7) yields

$$Y = U\theta + E, (8)$$

where

$$\mathbf{Y} = \begin{bmatrix} y(n) \\ y(n+1) \\ \vdots \\ y(N) \end{bmatrix},$$

$$\mathbf{U} = \begin{bmatrix} 1 & y(n-1) & \cdots & y(1) & u(n) & \cdots & u(0) \\ 1 & y(n) & \cdots & y(2) & u(n+1) & \cdots & u(1) \\ \vdots & \vdots & \vdots & \vdots & \vdots & \vdots & \vdots \\ 1 & y(N-1) & \cdots & y(N-n+1) & U(N) & \cdots & U(N-n) \end{bmatrix},$$

 $\theta = [a_0, a_1, ..., a_{n-1}, b_0, b_1, ..., b_n]^T$ , and E is the error vector. Let S denote the sum square error,

$$S = E^{T}E = (Y^{T} - \theta^{T}U^{T})(Y - U\theta), \qquad (9)$$

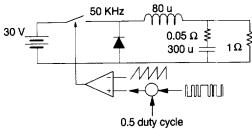


Fig. 8: Open-Loop Buck Converter Gain [dB] Phase 40 0 -45 20 -90 0 Identification -135 -20 Measurement -180 -40 10 100 1,000 10,000 100,000 Frequency

it is desirable to estimate  $\theta$  such that S is minimized. This can be accomplished by taking the derivative of S with respect to  $\theta$  and setting it to zero; namely

Fig. 9: Control-to-Output TF

$$\frac{\delta S}{\delta \theta} = -2\mathbf{U}^{\mathsf{T}} \mathbf{Y} + 2\mathbf{U}^{\mathsf{T}} \mathbf{U} \theta = 0.$$
 (10)

Then, one obtains

$$\left[\mathbf{U}^{\mathsf{T}}\mathbf{U}\right]\mathbf{\hat{\theta}} = \mathbf{U}^{\mathsf{T}}\mathbf{Y} \ . \tag{11}$$

Equation (11) represents a set of algebraic equations from which  $\theta$  can be solved using the Gaussian elimination technique with partial pivoting or better techniques that can be found in [1]. Thus, the identification process consists of applying (11), which can be implemented in MATLAB, to the sampled data obtained from the Pspice simulations. Estimation of the order of the unknown converter can be performed based on trying various ARMA models of different orders and selecting the one that yields the lowest sumsquare error, S. Once the ARMA model is obtained, transform to the S-domain can be applied, and the converter small-signal transfer function is obtained thereby.

#### III. RESULT VERIFICATION

To validate the method, a buck converter, a series resonant converter (SRC), and a forward multiresonant converter (FMRC) are selected for the small-signal transfer function identification, as illustrated in Figs. 8, 10, and 12. PBRSs of small amplitudes (about 2% - 5% of the dc value) are injected into the converters; the sampling frequencies are set at the switching frequencies; and 500 input-output data points are collected for all three cases. The MATLAB program in Appendix B is used to identify the small-signal transfer functions. Since the system orders are unknown, various

TABLE I Log (RMS Error)

200 (11/12 214/01)			
System Order	Buck	SRC	FMRC
1	-0.97	-2.06	-1.34
2	-2.54	-2.33	-1.94
3	-2.57	-2.96	-2.12
4	-2.58	-3.00	-2.16
5		-3.02	-2.18
6			-2.32
7			-2.35
8			-2.36

system orders are selected, and the identification results are illustrated in Table 1. Observe that as the system order is increased, the identification result becomes more accurate for each converter (smaller RMS error). However, beyond a certain order, the RMS error starts to level down, indicating that increasing the system order further does not produce much more accuracy. Thus, according to Table 1, the identified ARMA model for the buck converter is of second order, the one for the SRC is of third order, and the one for the FMRC is of sixth order. Notice that the identification results are in good agreement with the measurement results, as illustrated in Figs. 9, 11, and 13. However, the disagreement in the phase of the FMRC case could result from the aliasing problem as predicted.

#### IV. CONCLUSIONS

A new empirical data modeling method for determining small-signal transfer functions of switching regulators is proposed. The method requires generation of the input-output data of a switching converter using a simulation software such as Pspice, and utilizes the least-square fit algorithm to approximate an ARMA model for the converter. The result verification shows that the method is accurate up to half of the switching frequency and is efficient, simple, and suitable for all classes of switching converters.

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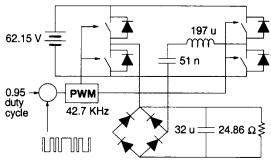


Fig. 10: Open-Loop Series Resonant Converter Gain [dB] Phase 30 20 45 10 0 -90 -10 measurement -135 -20 identification -30 -180 100,000 1,000 100 10.000 Frequency [Hz]

Fig. 11: Control-to-Output TF

#### APPENDIX A: MODULATION THEORY

In this appendix, basic modulation theory, which is needed for the empirical data modeling of switching regulators, is presented. By definition, modulation is the process of imparting the source information onto a bandpass signal with a carrier frequency  $f_c$  by the introduction of amplitude and/or phase perturbation.

#### A. Amplitude modulation

Given the baseband source signal, s(t), the amplitude-modulated signal, m(t), with the carrier frequency  $f_c$  is given by

$$m(t) = s(t)\cos(w_c t) \tag{12}$$

where  $w_c = 2\pi f_c$ . Consequently, if S(f) and M(f) denote the frequency domain representation of the signals s(t) and m(t), respectively, then M(f) is given by

respectively, then 
$$M(f)$$
 is given by
$$M(f) = \frac{1}{2} [S(f + f_c) + S(f - f_c)], \tag{13}$$

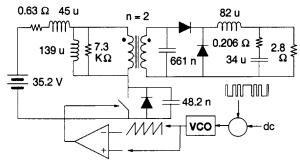
which means that the modulated signal m(t) contains the shifted-spectra of the baseband signal s(t) onto the carrier frequency  $\pm f_c$ , as illustrated in Fig. 14

## B. Narrowband Angle Modulation

In angle modulation, the modulated signal, m(t), is given by

$$m(t) = A\cos[w_c t + s(t)], \qquad (14)$$

which is a nonlinear function of the modulation. However, when s(t) is restricted to small perturbation, m(t) can be approximated by a linear function of the modulation. Indeed, (14) can be expanded to



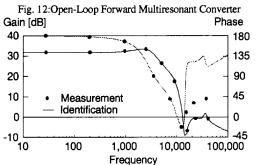


Fig. 13: Control-to-Output TF

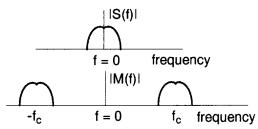


Fig. 14: Amplitude modulation

 $m(t) = A\cos(w_c t)\cos[s(t)] - A\sin(w_c t)\sin[s(t)]$ . (15) Since |s(t)| << 1,  $\cos[s(t)] \approx 1$  and  $\sin[s(t)] \approx s(t)$ ; and therefore

$$m(t) \approx A\cos(w_c t) - As(t)\sin(w_c t),$$
 (16)

which, in the frequency domain, becomes

$$M(f) = \frac{A}{2} [\delta(f - f_c) + \delta(f + f_c)] + j \frac{A}{2} [S(f - f_c) - S(f + f_c)].$$
 (17)

The spectrum of M(f) from (17) is similar to the one in Fig. 14, except for the presense of two additional delta functions centered at  $\pm f_c$ .

# APPENDIX B: MATLAB PROGRAM FOR SMALL-SIGNAL IDENTIFICATION

load fmrc.dat % Load data
U = fmrc(:,1); % U = input
Y = fmrc(:,2); % Y = output
Fs = 88496; % Sampling frequency

%..... Compute ARMA model.....
order = 6;

```
RXor (0, XOR) 1
n = length(Y) - order;
                                                             * Init 1st bit = 1 by tieing it to Vini
UU = U(1:n);
                                                            Sout (01,1) (c,0) SWITCH

Eout (out,0) POLY(1) 07 0 -1 2

Rout (out,0) 1
YY = Y(1:n);
for i = 2:order+1;
   UU = [U(i:n+i-1) UU];
                                                             .ends
   YY = [Y(i:n+i-1) YY];
end;
                                                             .MODEL SWITCH VSWITCH (RON=.01, ROFF=1e6)
Ye = YY(:,1);
Ue = [ones(n,1) UU YY(:,2:order+1)];
W = (Ue'*Ue) \ (Ue' * Ye);
                                                            * OPEN-LOOP BUCK CONVERTER
%>>>> W = ARMA model coefficients <<<
                                                            Vg (1,0)
SW (1.2)
                                                                              3.0
%..... transfer function in Z-domain...
numd = W(2:order+2)';
                                                            SW
                                                                    (1,2)
                                                                              (d,0)
                                                                                          SW1
                                                            Diode (0,2)
                                                                                          DIODE
dend = [1 -W(order+3:length(W))'];
                                                                              80u
                                                                                          IC = 13.58
                                                            L
                                                                    (2,3)
                                                                              0.05
                                                            RC
                                                                    (3,4)
%...... Z-domain -> S-domain ......
                                                            C
                                                                    (4,0)
                                                                              300u
                                                                                          IC = 14.50
[num,den] = d2cm (numd,dend,1/Fs,'zoh');
                                                            Rload (3,0)
                                                             *....PWM....
      ... Plot tranfer function ......
                                                            XRSC 99 RSC9 PARAMS: Ts=40u, td=10u
w = logspace (1,6,300);
[mag,ph] = bode (num,den,w);
db = 20 * log10 (mag);
semilogx (w/2/pi,db);
title ('Transfer Function Gain [dB]');
                                                            Vd (vc,0)
                                                                             0.5
                                                            RVd (vc,0)
                                                                             1
                                                            Vr (r,0) PULSE 0 1 0 19.99u 0.01u 0 20u
Rr (r,0) 1
                                                            Ec (d,0) VALUE=[1/(1+exp(-
1e5*(.05*V(99)+V(vc)-V(r))))}
figure
semilogx (w/2/pi,ph);
                                                            Rd (d,0) 1
.MODEL SW1 VSWITCH (RON=.0001,ROFF=1e6)
title ('Transfer Function Phase')
                                                            .MODEL DIODE D ()
.tran 20u 10e-3 0
                                                                                          0.5u
                                                             .end
               APPENDIX C: PSPICE CODES
                                                            *FULL BRIDGE SERIES RESONANT CONVERTER *
* SAMPLE & HOLD CIRCUIT
* node <sample> -> sample when 1
* node <hold> -> hold when 1
                                                                           62.15
                                                            Vg (1,0)
                                                            SW1 (1,2)
                                                                           (d1,0)
                                                                                       SW
                                                            D1 (2,1)
                                                                                       DIODE
                                                            SW2 (1,3)
                                                                           (0, d2)
.subckt SHl in out s h PARAMS:cap=5e-7
                                                            D2 (3,1)
SW3 (2,0)
                                                                                       DIODE
                    (in,0)
E1
     (1,0)
                               1
                                                                           (0, d1)
SW1
      (1,2)
                     (s,0)
                                SWITCH
                                                                                       SW
                                                            D3 (0,2)
SW4 (3,0)
                                                                                       DIODE
      (2,0)
Cl
                    {cap}
                                IC = 0
                    (2,0)
      (3,0)
                                                                            (d2,0)
E2
                                                                                       SW
                                                            D4 (0,3)
                    (h,0)
                                SWITCH
                                                                                       DIODE
SW2 (3,4)
                                                             *...Switch control..
                               IC = 0
                                                            XPRBS p RSC7 PARAMS: Ts=46.88u, td=0 Vc (dc,0) 0.05 Rv (dc,0) 1
C2
      (4,0)
                    {cap}
      (vcc,0)
Vcc
                    (4,0)
                                SWITCH
SW3
      (vcc,out)
Rout (out,0)
                    ìoò
                                                            Ec (99,0) POLY(2) (p,0) (dc,0) 0 0.03 1
Rc (99,0) 1
V1 (t,0)PULSE (-1 1 0 .01u .01u 11.70u
. ends
                                                            23.44u)
                                                            Rd1 (t,d1)
Cd1 (d1,0)
                                                                              5n
* REGISTER SHIFT CIRCUIT: 7 Stages
                                                            Vr r,0 PULSE 0 1 0 11.71u .01u 0 11.72u
Rr r,0 1
   Ts = Period
   td = time delay before perturbation *
   Output = zero mean, amplitude {-1,1}*
                                                            E1 c,0 VALUE = [2*(1/(1+exp(-1e5*(v(99)-
                                                            V(r))) - 0.5)
R1 c,0 1
.subckt RSC7 out PARAMS: Ts=10u, td=10u
Vclk0 (s,0) PULSE 0 [Ts/10] [Ts/10] [Ts/
                            1 [td]
                                                            E2 0,s POLY(2) (c,0) (d1,0) 0 0 0 0 1
                                            [Ts/10]
                                                            Rd2 (s,d2) 1
Cd2 (d2,0) 5n
Vclkl (h,0) PULSE 0 {Ts/10} {Ts/10}
                             1 {td+Ts/2} {Ts/10}
                                                            L (3,5)
C (5,6)
                                                                         197u
                                                                                      ic = -1.3169485
Vctrl (c,0) PULSE 1
                             0 {td+Ts/2} {Ts/le3}
                                                                         51n
                                                                                      ic = 1.7245076E2
                                                            Db1 (2,7)
Db2 (6,7)
Db3 (8,2)
Db4 (8,6)
Cf (7,8)
{Ts/le3} le6 2e6
Vini (i,0) PULSE 0.5 1 {td}
                                                                           DIODE
                                                                           DIODE
                                            {Ts/le3}
{Ts/le3} le6
                   2e6
                                                                           DIODE
XSH1 XOR O1 s h SH1 PARAMS: cap={Ts/10}
                                                                           DIODE
                                                            Cf (7,8) 32u ic
Rload (7,8) 24.86
Ecf (21,0) (7,8) 1
REcf (21,0) 1
XSH2 01 02 s h SH1 PARAMS: cap={Ts/10}
                                                                                      ic = 4.5954197E1
XSH3 O2 O3 s h SH1 PARAMS: cap=[Ts/10]
XSH4 O3 O4 s h SH1 PARAMS: cap=[Ts/10]
         O5 s h SH1 PARAMS: cap=[Ts/10]
O6 s h SH1 PARAMS: cap=[Ts/10]
XSH5 04
XSH6 05
XSH7 06 07 s h SH1 PARAMS: cap={Ts/10}
                                                             .model DIODE D()
EXOr (0, XOR) VALUE = \{2*(v(O3) - 0.5)*(v(O7))\}
                                                             .model SW VSWITCH (Ron=1m, Roff=1MEG, Von=1,
- 0.5) - 0.5} ; XOR circuit
                                                            Voff=-1)
```

```
.TRAN 11.72u 5.86m 0 0.05u UIC
                                                                             (8,6)
                                                       EVo
                                                                   (21,0)
                                                                   (21,0)
35.2
                                                       RVo
.end
                                                       Vg (1,0)
Rs (1.2)
                                                            (1,2)(2,3)
                                                                     0.63
                                                       Rs
                                                                               ic = -3.8805144
                                                                     45u
* Voltage-controlled-variable-frequency*
                                                       Lm (3,4)
Rm (3,4)
                                                                               ic = 1.0163970
                                                                     139u
* Ramp generator
* Ramp voltage amplitude (-5 V,5 V)
                                                                     7.3K
                                                       Cs (4,0) 48.2n
Diode (0,4)
Sd (4,0) (d,0)
                                                                               ic = 7.8406708E1
* Rise time = v(in)/le-6
* Fall time = -100*v(in)/le-6
                                                                     48.2n
                                                                               DIODE
   ********
                                                                              SW1
                                                       .subckt RGEN in out
Gic (0,out) VALUE = [v(in)/(1+exp(-
Gic (0,out)
Rn(40,0) 1
                                                       *..Ramp generator
Es (50,0) POLY(1) (p,0) .905 .01
                                                       Xr (50,r) RGEN
Ed (d,0) VALUE = [1/(1+exp(-1e5*(v(40)-
.ends
**********
                                                       v(r))))}
* SCHMITT TRIGGER CIRCUIT:
                                                       Rd (d,0)
                                                       XPRBS p RSC7 PARAMS: Ts = 22.6u, td = 0
* Output [-10.4 V, 10.4 V]

* If input voltage > 5, output = -10.4V*

* If input voltage < -5, output = 10.4V*
                                                       *..... Ideal Tranformer.....
                                                       RXFORM (4 , 6) 1MEG
FXFORM (3 , 4) VXFORM
VXFORM (5x, 5) 0
EXFORM (5x, 6) (3 , 4)
.subckt STRIG 1 3
Rin (1,2) 1MEG
Ein (7,0) (2,1) 1000
Rc (7,3) 1
                                                                                           0.5
      (7,3)
     (3,0) \bar{1}00n
                           ic = 0
                                                       Cd (5,6)
D1 (5,7)
                                                                   661n
                                                                               ic = 9.5960226
Dz1 (3,4) ZENER
Dz2 (0,4) ZENER
                                                                   DIODE
                                                       D2 (6,7)
Lf (7,8)
Rc (8,9)
Cf (9,6)
                                                                   DIODE
     (3,2) 10K
                                                                               ic = 3.3184993
                                                                   82u
R1
                                                                   206m
             10K
R2
      (2,0)
                                                                               ic = 8.3736696
                                                                  34u
.MODEL ZENER D (BV = 8.47)
                                                                   (8,6)
                                                                                2.8152
.ends
                                                       Rload
*******
                                                       .MODEL SW1 VSWITCH (Ron=0.1m Roff=1Meg)
                                                       .MODEL DIODE D ()
.tran 11.3u 5650u 0 0.02u UIC
* Forward Multi-Resonant Converter
                                                       .end
```