



PS10/WRD SPECIFICATIONS

Last update: 25/12/03

REVISION HISTORY				
ECO NO.	REV.	DESCRIPTION	DATE	APPR.
	0.0	Initial specifications	25.12/03	Shai Pe'er

1. Electrical requirements:

1.1 Input requirements

1.1.1 Voltage:

- 85±264VAC rms, 47±63Hz (Universal Voltage)
- 40±370VDC

1.1.2 Efficiency: 70% min at max load.

1.1.3 Inrush current: 30A peak at 264VAC rms

1.1.4 Leakage current: 1mA max. @ 100-240VAC rms

1.2 Output requirements

1.2.1 Output power: Max 10W

1.2.2 Nominal output Voltages & currents:

- +5V 0±1.5A
- -5V 0±0.5A

1.2.3 Accuracy, Line/Load & cross regulation:

- +5V ±1%
- -5V ±2%

1.2.4 Over shoot at start up: less than 1%

1.2.5 Ripple Voltage & noise: 1%

1.2.6 Hold-up time: 30 msec At 115VAC and full load.

1.2.7 Protection

- Short circuit and overload protected for unlimited time.
- Over Voltage protection for typical +6.2V(Zener)

2. Mechanical and Thermal requirements(see Mechanical drawing below)

2.1 Dimensions: 105 x 45 x 22 mm

2.2 Connectors:

- Input: 3 wires with length of 15cm approx. AWG18 per UL1007
L-Brown, N-Blue, E-Green/Yellow

• Output:

J1 Molex p/n 5414-03A-102, pin 1: +5V, pin 2: RTN, pin 3: -5V

Or

J2 Samtec p/n BCS-105-L-S-PE, pin 1: +5V, pin 2 & 3 & 4: RTN, pin5: -5V

2.3 Temp

- Storage -40°C ÷ +80°C
- Operating -10°C ÷ +65°C at max. output power and free convection.

3. Standards

3.1 The power supply shall bear a CE mark. The manufacturer/vendor shall provide a Declaration of conformity stating that the Power supply confirms to:

- The EMC Directive – per EN 55022 Class B
- The Low Voltage Directive – per EN 60950



data communications

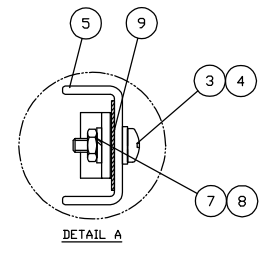
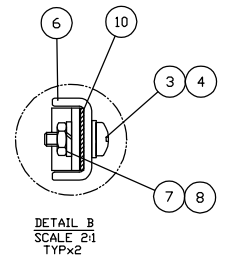
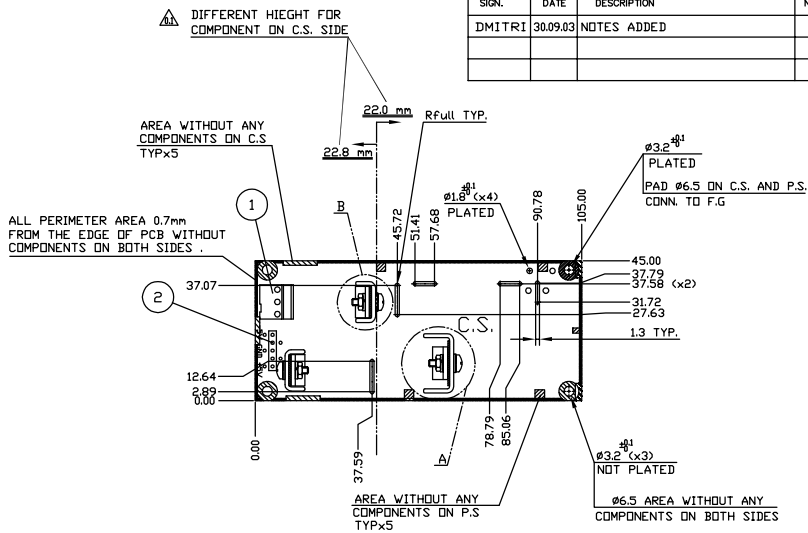
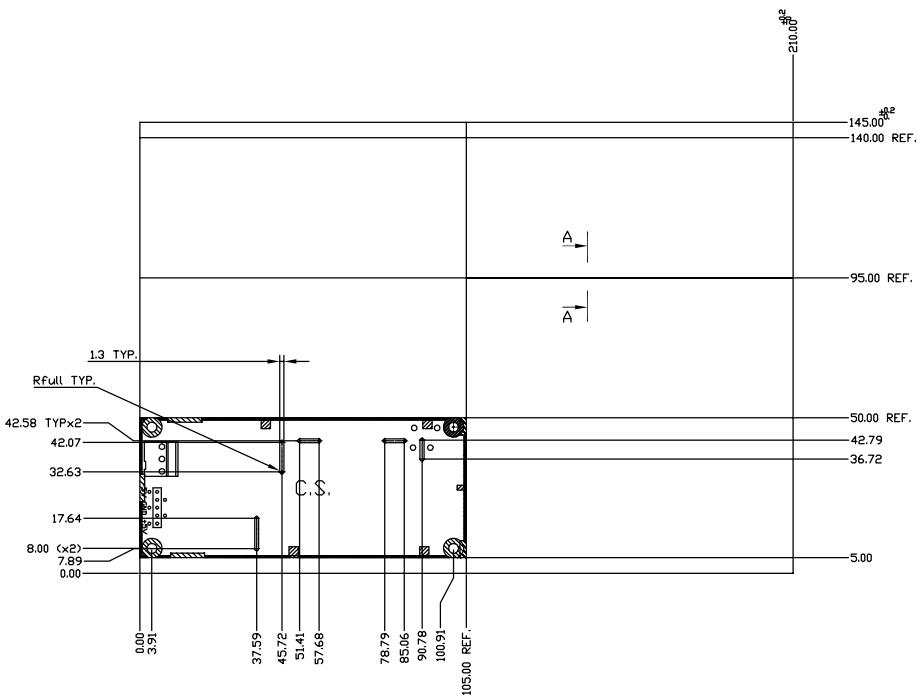
- 3.2 The power supply shall be UL recognized and CSA certified (or UL for Canada recognized).
- 3.3 The manufacturer/vendor shall provide Rad test reports and certificates of meeting Safety and EMC requirements.
- 3.4 Leakage current: Less than 0.75 mA @ max. input Line Voltage.
- 3.5 Surge: According to IEC-1000-4-5
 - 1KV line to line
 - 2KV line to earth

- 3.6 Calculated MTBF: 3M hours
Part count according to Telcordia SR-332, Issue 1, GB, at 35°C and delta T 10°, 50% stress on Electrolytic capacitors and semiconductors.

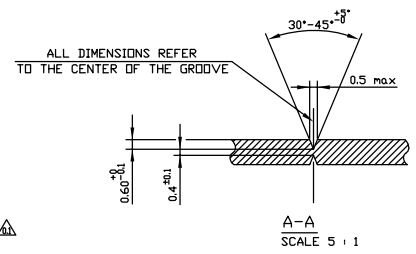
4. Mechanical & Electrical drawings

SIGN.	DATE	DESCRIPTION	NO.
DMITRI	30.09.03	NOTES ADDED	01

WAVE DIRECTION →



- NOTES :**
- ALL DIMENSIONS AND BORES DIAMETERS ARE FINAL, AFTER COATING AND OTHER MANUFACTURING PROCESSES.
 - BOARD THICKNESS: 1.5mm.
 - ALL COMPONENTS DIMENSIONS SHOULD BE TAKEN FROM MANUFACTURER'S DATA-SHEETS.
 - MAXIMUM HEIGHT OF COMPONENTS ON C.S. SEE NOTES IN DRAWING
 - MAXIMUM HEIGHT OF COMPONENTS ON P.S. 3.0mm

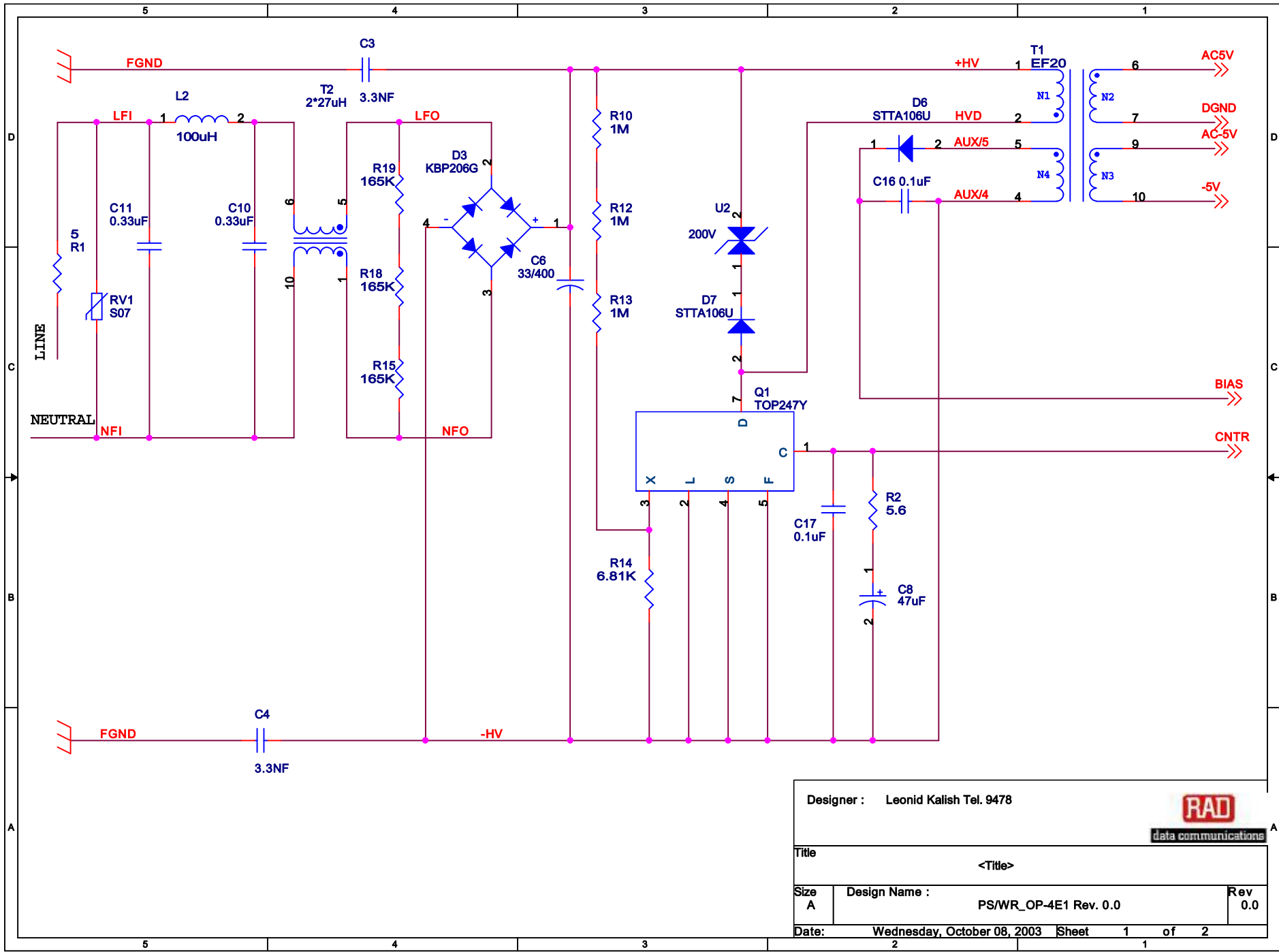


ITEM NO.	CATALOG NO.	DRAWING NO.	QTY.	DESCRIPTION	NOTES
10	60-11-0393-1674	2	MECH. HARDWARE, INSULATOR, SILICON, (T0-220)		
9	60-11-5791-1674	1	MECH. HARDWARE, INSULATOR, SILICON, (T0-220)		
8	D-2	3	MECH.DISC,SPRING,2.9*5.3*0.635,ST,ZINC		
7	A-2	3	MECH,NUT,HEX,440UNC*2.381,ST,ZINC,56.35		
6	5970B	2	HEAT SINK, VOLTAGE REGULATOR WITH PIN STAND		
5	5073B	1	HEAT SINK, VOLTAGE REGULATOR		
4	D-9	3	MECH,DISC,FLAT,3.2*7.0*0.5,ST,ZINC		
3	B-20	3	MECH,BOLT,PAN-H,PH,440UNC*9.53,ST,ZINC,HDS,38		
2	BCS-105-L-S-PE	1	CONN.BOARD,TH,ST,SPIN(1X5),(F),H=7.4MM,PASS THROUGH		
1	2630-3	1	CONNECTOR, 3 STRAIGHT PIN FRICTION LOCK HEADER		

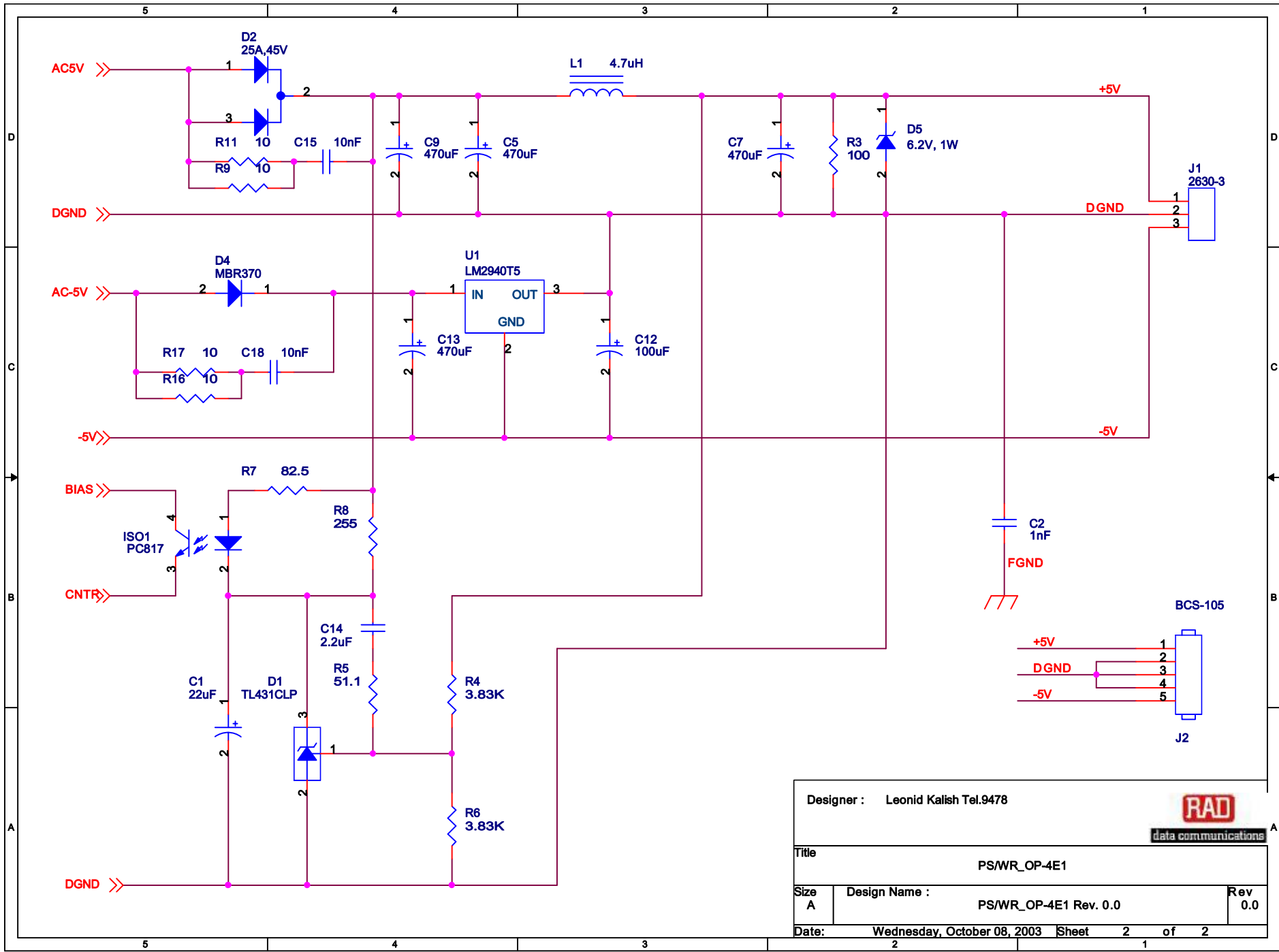
DATE	NAME	SCALE	REV.
06.03.03	DIMA_G	1:1	01
06.01.03	N. Maslov	1:1	01
06.01.03	F. Frolov	1:1	01


DO NOT SCALE IN THE DRAWING. ALL DIMENSIONS BEFORE COATING. UNLESS OTHERWISE SPECIFIED, ALL DIMENSIONS ARE IN MM. LINEAR: ± 0.15, ANGLES: ± 30', ROUND: SHARP, PRODUCTION APPROVED: PRODUCTION APPROVED: FINISH PROTECTION: SHEET: 1 OF: 1. SCALE: 1:1. DRAWING NO.: 02.11.22-04-01. REV. 01.

TITLE:
 OPTIMUX-4E1T1/NEW LOOK
 PCB-MECHANICAL LAYOUT



Designer : Leonid Kalish Tel. 9478		RAD data communications	
Title		<Title>	
Size A	Design Name :	PS/WR_OP-4E1 Rev. 0.0	Rev 0.0
Date:	Wednesday, October 08, 2003	Sheet	1 of 2



Designer : Leonid Kalish Tel.9478		
		
Title PSWR_OP-4E1		
Size A	Design Name : PSWR_OP-4E1 Rev. 0.0	Rev 0.0
Date: Wednesday, October 08, 2003	Sheet 2	of 2