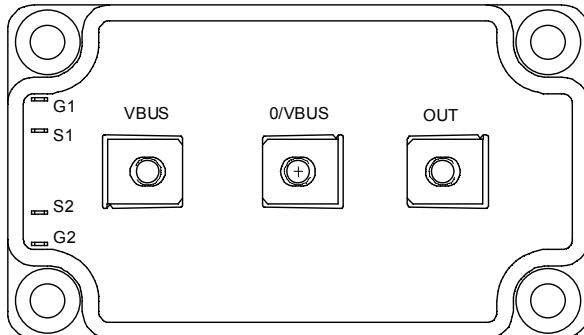
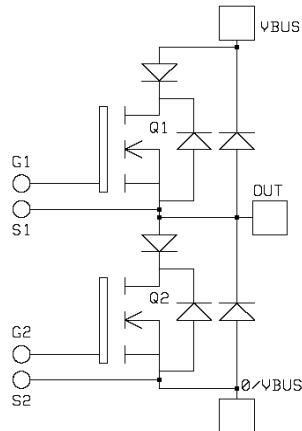


**Phase leg
Series & parallel diodes
MOSFET Power Module**

**V_{DSS} = 1000V
R_{DSon} = 130mΩ max @ T_j = 25°C
I_D = 65A @ T_c = 25°C**



Absolute maximum ratings

Symbol	Parameter	Max ratings	Unit
V _{DSS}	Drain - Source Breakdown Voltage	1000	V
I _D	Continuous Drain Current	T _c = 25°C T _c = 80°C	65 49
I _{DM}	Pulsed Drain current		
V _{GS}	Gate - Source Voltage	±30	V
R _{DSon}	Drain - Source ON Resistance	130	mΩ
P _D	Maximum Power Dissipation	T _c = 25°C	1250
I _{AR}	Avalanche current (repetitive and non repetitive)	24	A
E _{AR}	Repetitive Avalanche Energy	30	mJ
E _{AS}	Single Pulse Avalanche Energy	1300	

 **CAUTION:** These Devices are sensitive to Electrostatic Discharge. Proper Handing Procedures Should Be Followed.

All ratings @ $T_j = 25^\circ\text{C}$ unless otherwise specified

Electrical Characteristics

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
BV_{DSS}	Drain - Source Breakdown Voltage	$\text{V}_{\text{GS}} = 0\text{V}$, $\text{I}_D = 1.5\text{mA}$	1000			V
I_{DSS}	Zero Gate Voltage Drain Current	$\text{V}_{\text{GS}} = 0\text{V}$, $\text{V}_{\text{DS}} = 1000\text{V}$	$\text{T}_j = 25^\circ\text{C}$		600	μA
		$\text{V}_{\text{GS}} = 0\text{V}$, $\text{V}_{\text{DS}} = 800\text{V}$	$\text{T}_j = 125^\circ\text{C}$		2	mA
$\text{R}_{\text{DS(on)}}$	Drain – Source on Resistance	$\text{V}_{\text{GS}} = 10\text{V}$, $\text{I}_D = 32.5\text{A}$			130	$\text{m}\Omega$
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	$\text{V}_{\text{GS}} = \text{V}_{\text{DS}}$, $\text{I}_D = 6\text{mA}$	3		5	V
I_{GSS}	Gate – Source Leakage Current	$\text{V}_{\text{GS}} = \pm 30\text{ V}$, $\text{V}_{\text{DS}} = 0\text{V}$			± 450	nA

Dynamic Characteristics

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
C_{iss}	Input Capacitance	$\text{V}_{\text{GS}} = 0\text{V}$ $\text{V}_{\text{DS}} = 25\text{V}$ $f = 1\text{MHz}$		15.2		nF
C_{oss}	Output Capacitance			2.6		
C_{rss}	Reverse Transfer Capacitance			0.42		
Q_g	Total gate Charge	$\text{V}_{\text{GS}} = 10\text{V}$ $\text{V}_{\text{Bus}} = 500\text{V}$ $\text{I}_D = 65\text{A}$		562		nC
Q_{gs}	Gate – Source Charge			75		
Q_{gd}	Gate – Drain Charge			363		
$\text{T}_{\text{d(on)}}$	Turn-on Delay Time	Inductive switching @ 125°C $\text{V}_{\text{GS}} = 15\text{V}$ $\text{V}_{\text{Bus}} = 667\text{V}$ $\text{I}_D = 65\text{A}$ $\text{R}_G = 0.5\Omega$		9		ns
T_r	Rise Time			9		
$\text{T}_{\text{d(off)}}$	Turn-off Delay Time			50		
T_f	Fall Time			24		
E_{on}	Turn-on Switching Energy ①	Inductive switching @ 25°C $\text{V}_{\text{GS}} = 15\text{V}$, $\text{V}_{\text{Bus}} = 667\text{V}$ $\text{I}_D = 65\text{A}$, $\text{R}_G = 0.5\Omega$		2.13		mJ
E_{off}	Turn-off Switching Energy ②			0.46		
E_{on}	Turn-on Switching Energy ①	Inductive switching @ 125°C $\text{V}_{\text{GS}} = 15\text{V}$, $\text{V}_{\text{Bus}} = 667\text{V}$ $\text{I}_D = 65\text{A}$, $\text{R}_G = 0.5\Omega$		4.4		mJ
E_{off}	Turn-off Switching Energy ②			0.57		

Series diode ratings and characteristics

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
$\text{I}_{\text{F(AV)}}$	Maximum Average Forward Current	50% duty cycle	$\text{T}_c = 85^\circ\text{C}$		60	A
V_F	Diode Forward Voltage	$\text{I}_F = 60\text{A}$		1.1	1.15	V
		$\text{I}_F = 120\text{A}$		1.4		
		$\text{I}_F = 60\text{A}$	$\text{T}_j = 125^\circ\text{C}$	0.9		
t_{rr}	Reverse Recovery Time	$\text{I}_F = 60\text{A}$	$\text{T}_j = 25^\circ\text{C}$		24	ns
		$\text{V}_R = 133\text{V}$ $\text{di}/\text{dt} = 400\text{A}/\mu\text{s}$	$\text{T}_j = 125^\circ\text{C}$		48	
Q_{rr}	Reverse Recovery Charge	$\text{I}_F = 60\text{A}$	$\text{T}_j = 25^\circ\text{C}$		66	nC
		$\text{V}_R = 133\text{V}$ $\text{di}/\text{dt} = 400\text{A}/\mu\text{s}$	$\text{T}_j = 125^\circ\text{C}$		300	

① E_{on} includes diode reverse recovery.

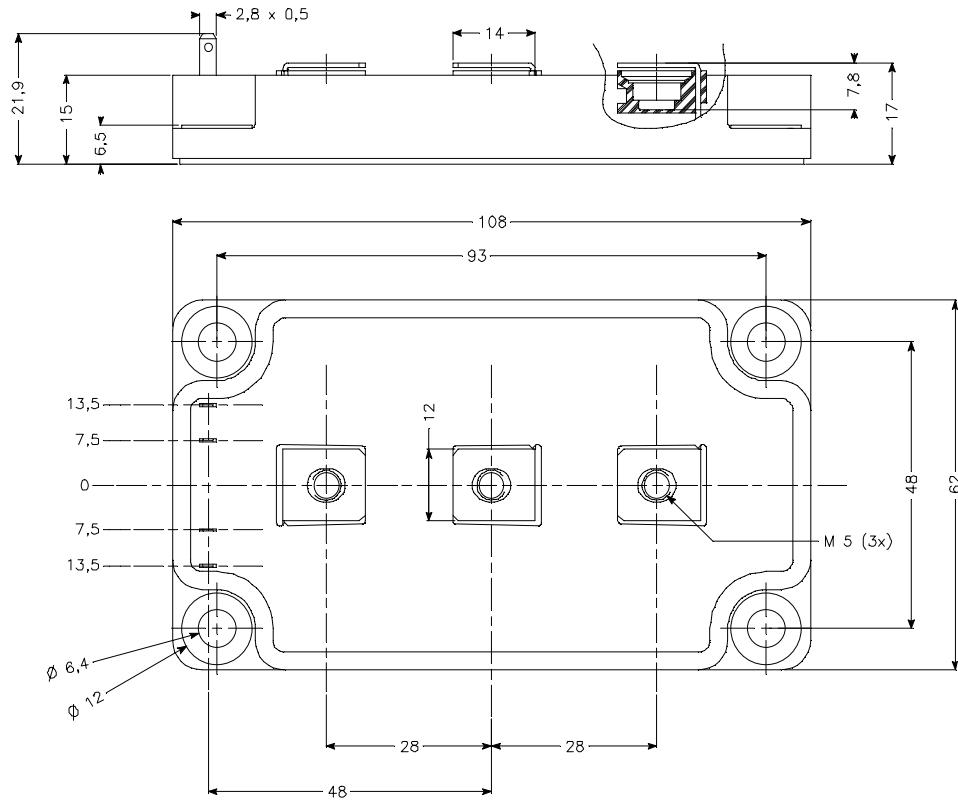
② In accordance with JEDEC standard JESD24-1.

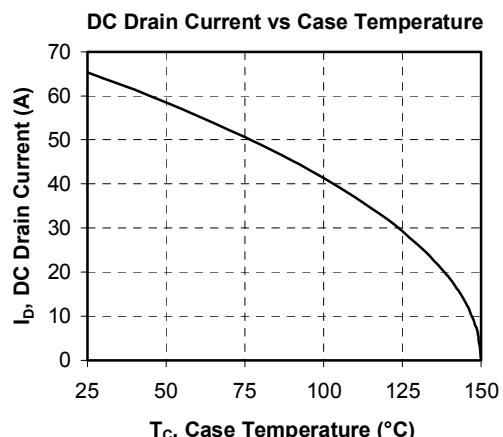
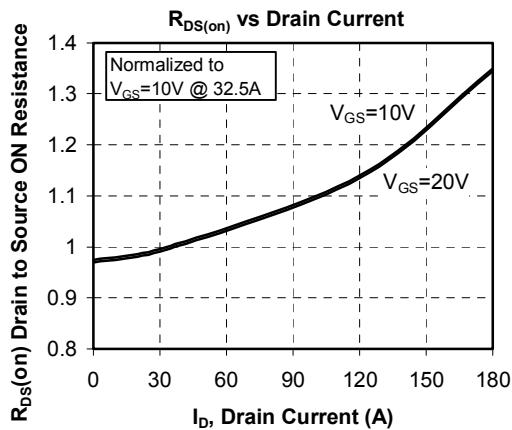
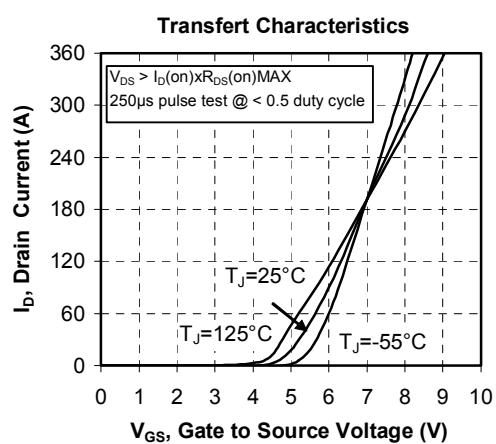
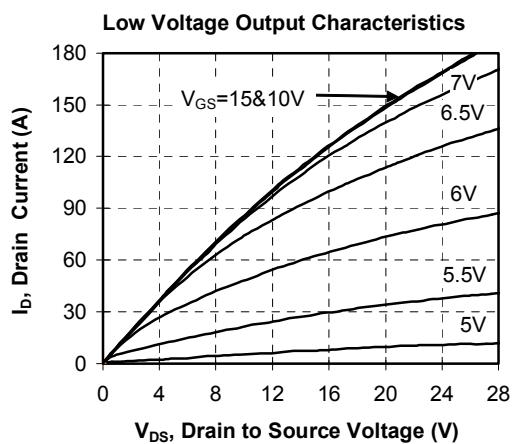
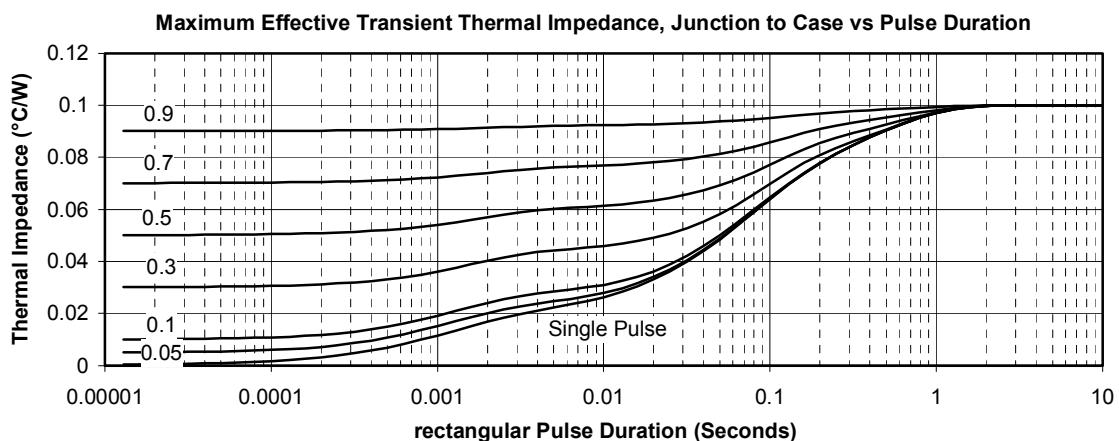
Parallel diode ratings and characteristics

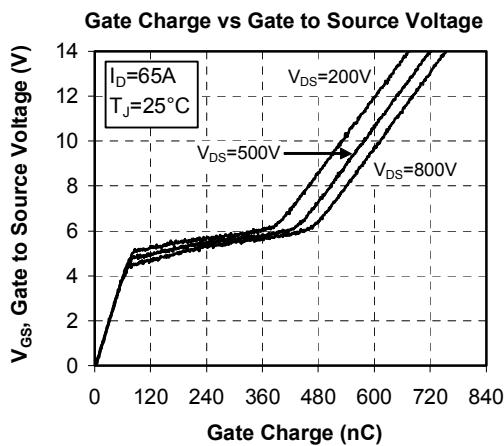
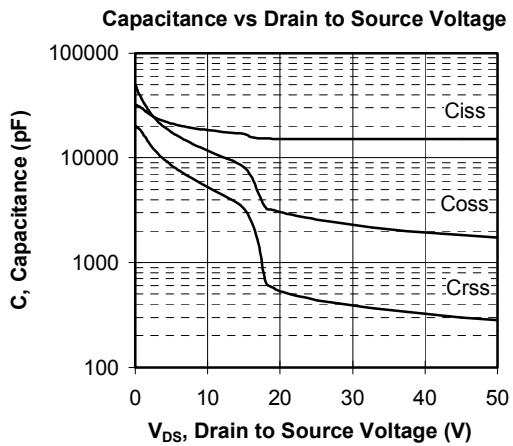
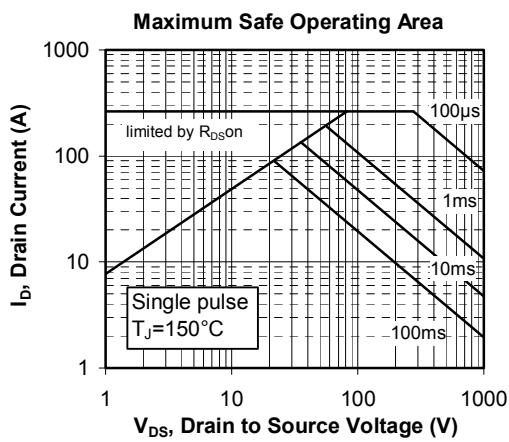
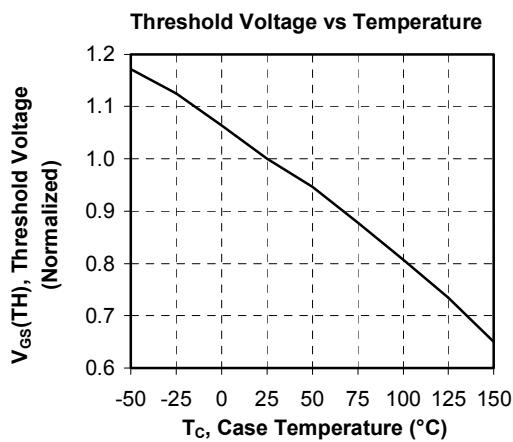
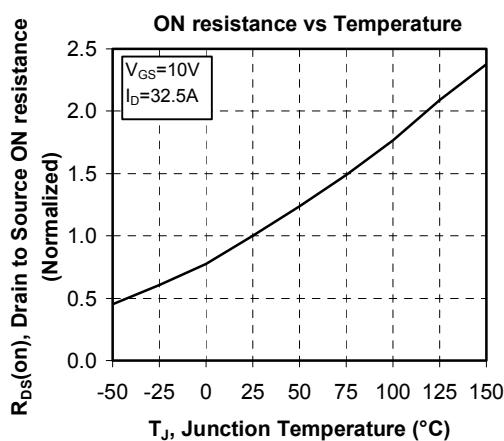
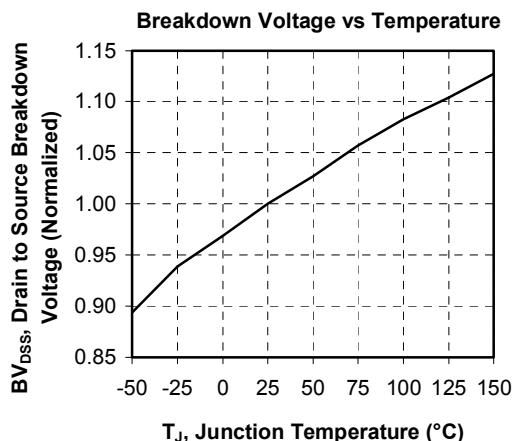
<i>Symbol</i>	<i>Characteristic</i>	<i>Test Conditions</i>		<i>Min</i>	<i>Typ</i>	<i>Max</i>	<i>Unit</i>
I _{F(AV)}	Maximum Average Forward Current	50% duty cycle	T _c = 100°C		120		A
V _F	Diode Forward Voltage	I _F = 120A			1.9	2.5	V
		I _F = 240A			2.2		
		I _F = 120A	T _j = 125°C		1.7		
t _{rr}	Reverse Recovery Time	I _F = 120A	T _j = 25°C		280		ns
		V _R = 667V di/dt = 400A/μs	T _j = 125°C		350		
Q _{rr}	Reverse Recovery Charge	I _F = 120A	T _j = 25°C		1520		nC
		V _R = 667V di/dt = 400A/μs	T _j = 125°C		7200		

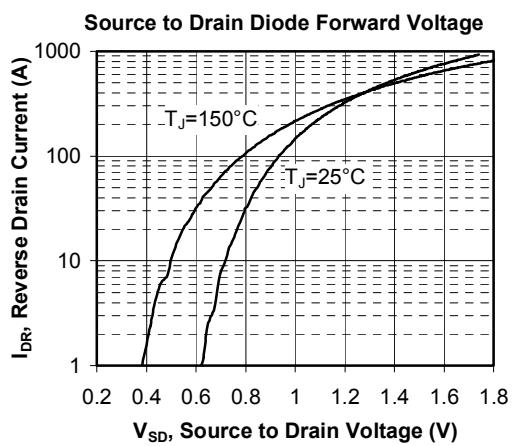
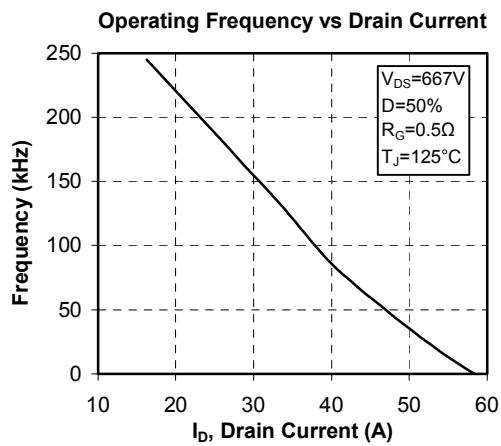
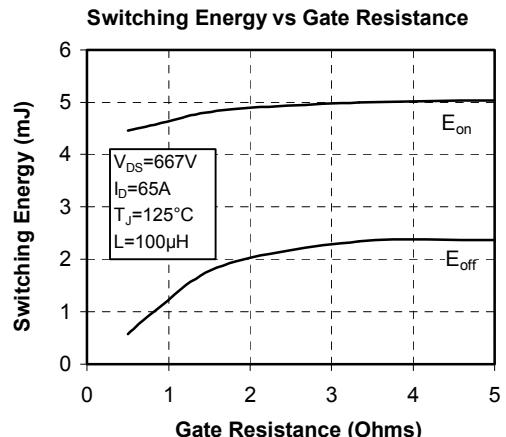
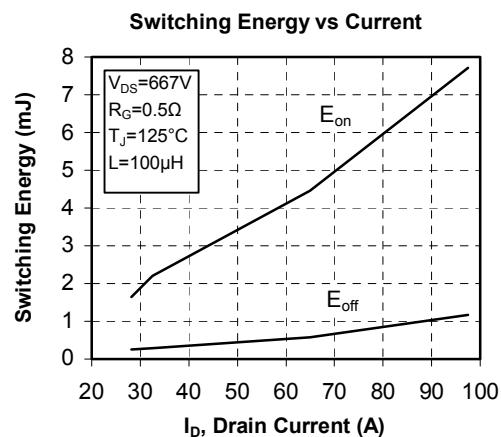
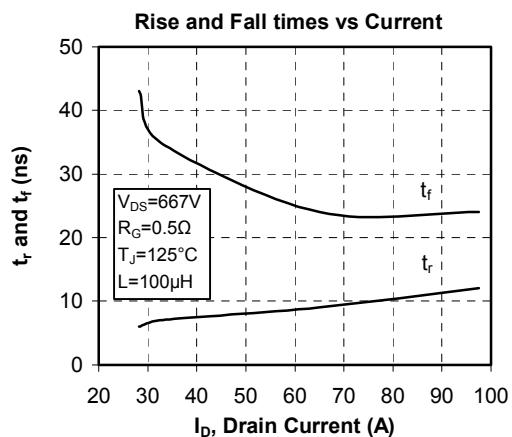
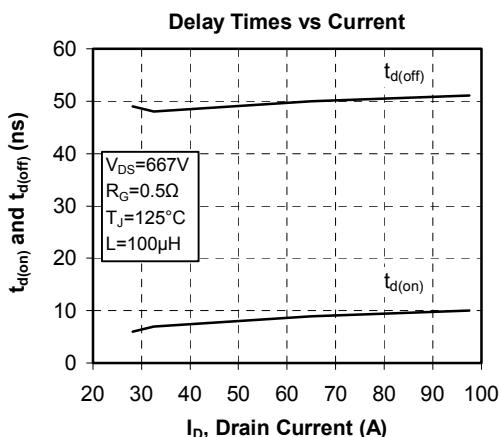
Thermal and package characteristics

<i>Symbol</i>	<i>Characteristic</i>		<i>Min</i>	<i>Typ</i>	<i>Max</i>	<i>Unit</i>
R _{thJC}	Junction to Case	Transistor			0.10	°C/W
		Diode serie			0.65	
		Diode parallel			0.46	
V _{ISOL}	RMS Isolation Voltage, any terminal to case t = 1 min, I _{isol} <1mA, 50/60Hz	2500				V
T _J	Operating junction temperature range	-40		150		°C
T _{STG}	Storage Temperature Range	-40		125		
T _C	Operating Case Temperature	-40		100		
Torque	Mounting torque	To heatsink	M6	3	5	N.m
		For terminals	M5	2	3.5	
Wt	Package Weight				280	g

Package outline








APT reserves the right to change, without notice, the specifications and information contained herein

APT's products are covered by one or more of U.S patents 4,895,810 5,045,903 5,089,434 5,182,234 5,019,522 5,262,336 6,503,786 5,256,583 4,748,103 5,283,202 5,231,474 5,434,095 5,528,058 and foreign patents. U.S and Foreign patents pending. All Rights Reserved.