

Li-Ion and NiMH Battery Charge IC

The PJ3963 is designed to optimize charging of Li-Ion and NiMH batteries. A flexible pulse-width modulation regulator allows the PJ3963 to control voltage and current during charging. The regulator frequency is set by an external capacitor for design flexibility. The switch-mode design keeps power dissipation to a minimum.

The PJ3963 measures battery temperature using an external thermistor for charge qualification. Charging begins when power is applied or on battery insertion.

For safety, the PJ3963 inhibits charging until the battery voltage and temperature are within configured limits. If the

battery voltage is less than the low-voltage threshold, the PJ3963 provides low-current conditioning of the battery.

A constant current-charging phase replenishes up to 70% of the charge capacity, and a voltage-regulated phase returns the battery to full. The charge cycle terminates when the charging current falls below a user-selectable current limit. For safety, charging is suspended if the temperature is outside the preconfigured limits.

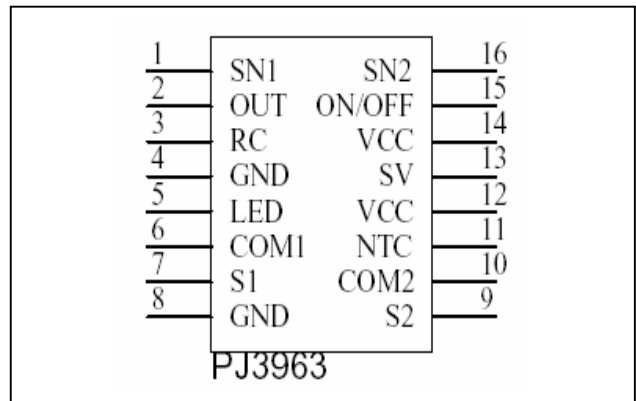
The PJ3963 provides status indications of charger states and faults for accurate determination of the battery and charge system conditions.

FEATURES

- n 3V to 40V input voltage range
- n Programmable charge current
- n Output voltage adjustable (1% accurate reference).
 - For Li-ion battery: 4.2V, 8.4V, 12.6V, 16.8V
 - For NiMH battery: 1.4V, 2.8V, 4.2V
- n Output switch current to 1A
- n LED charge status output
- n Vmax, Vmin, and Temp. detection and protection

APPLICATIONS

- n Wireless Devices
 - : Wireless Mouse, Wireless Telephone, Remote Control Aircraft
- n Desktop Chargers: Cellular, PDA, DSC Charger
- n USB Chargers
- n Electric Tools, Toys



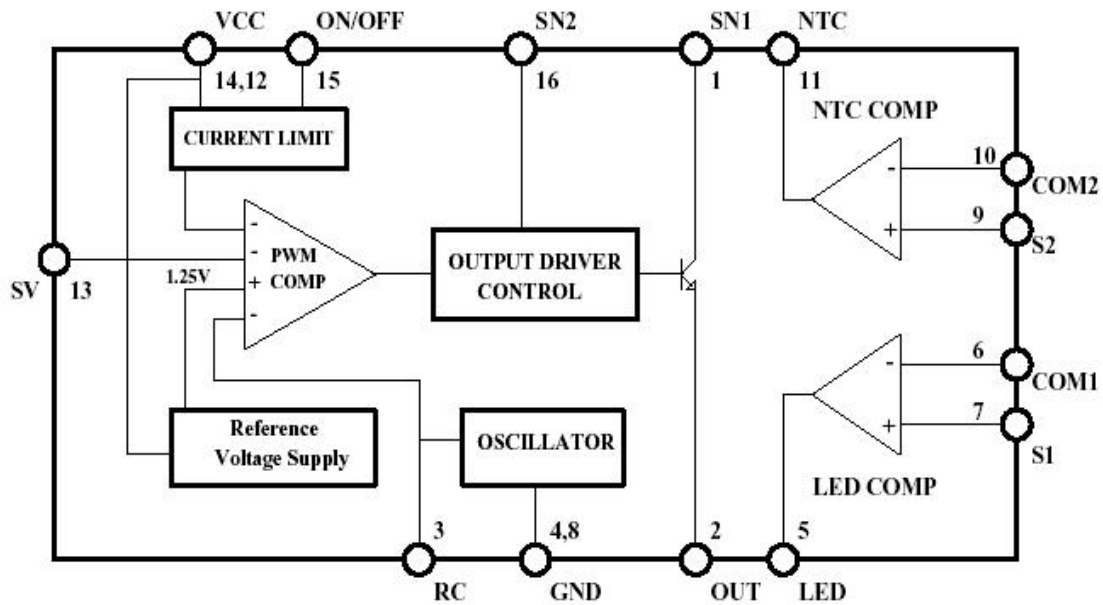
ORDERING INFORMATION

Device	Operating Temperature	Package
PJ3963CD	-20°C ~ +85°C	DIP-16
PJ3963CS		SOP-16

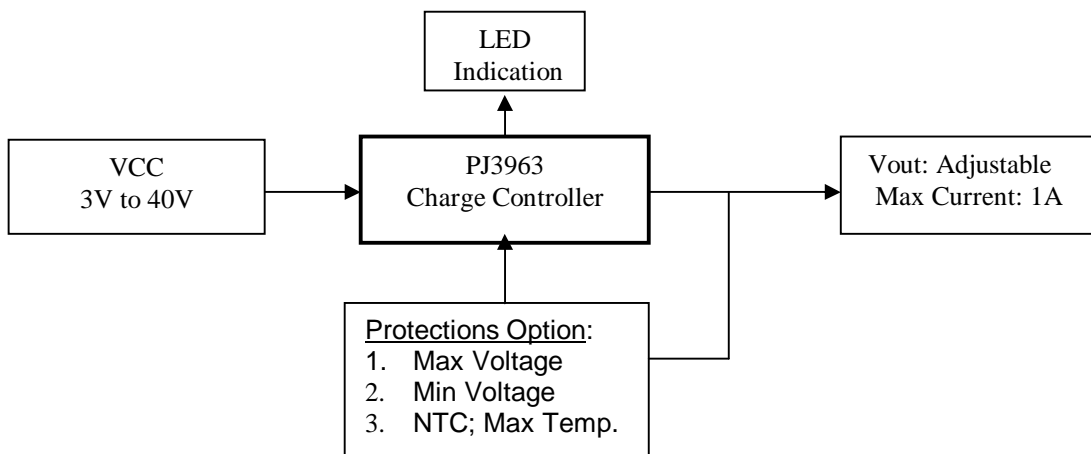
PIN DEFINE

PIN	Name	Description	PIN	Name	Description
1	SN1	SENSE Net 1	16	SN2	SENSE Net 2
2	OUT	OUTPUT	15	ON/OFF	Switching On/Off
3	RC	Oscillator Capacitor	14	VCC	Power Supply
4	GND	Power ground	13	SV	Sense Voltage
5	LED	LED charger indicator	12	VCC	Power Supply
6	COM1	Common 1	11	NTC	NTC input
7	S1	Send output 1	10	COM2	Common 2
8	GND	Power ground	9	S2	Send output 2

BLOCK DIAGRAM



FUNCTION DIAGRAM



ABSOLUTE MAXIMUM RATINGS

Characteristic	Symbol	Value	Unit
Power Supply Voltage	V_{CC}	40	Vdc
Comparator Input Voltage Range	V_{FB}	-0.3 ~ VCC	Vdc
PWM Out Collector Voltage	$V_{C(SW)}$	40	Vdc
PWM Out Collector to Emitter Voltage	$V_{CE(SW)}$	40	Vdc
PWM Out Switch Current	I_{SW}	1	A
Operating Junction Temperature	T_J	+125	°C
Operating Ambient Temperature Range	T_A	-20 to +85	°C

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Storage Temperature Range	Tstg	-65 to +150	°C
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ELECTRICAL CHARACTERISTICS

(V_{CC} = 5.0V. Ta= Tlow to Thigh. unless otherwise specified)

Characteristic	Symbol	Test Condition	Min	Typ	Max	Unit
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OSCILLATOR

Oscillator Frequency	fosc	V _{PIN5} =0V, C _T =1.0nF, T _A =25°C	24	33	42	KHz
Charging Current	I _{chg}	V _{CC} =5 to 40V, T _A =25°C	24	35	42	μ A
Discharge Current	I _{dischg}	V _{CC} =5 to 40V, T _A =25°C	140	220	260	μ A
Discharge To Charge Current Ratio	I _{dischg} /I _{chg}	Pin7 to V _{CC} , T _A =25°C	5.2	6.5	7.5	--
Current Limit Sense Voltage	V _{IPK(sense)}	I _{chg} = I _{dischg} , T _A =25°C	250	300	350	mV

OUTPUT SWITCH

Saturation Voltage , Darlington Connection	V _{CE(sat)}	I _{sw} =0.5A, Pins1 connected	--	1.0	1.3	V
DC Current Gain	h _{FE}	I _{sw} =1.0A, V _{CE} = 5.0V, T _A =25°C	50	75	--	--
Collector Off- State Current	I _{C(off)}	V _{CE} =40V	--	40	100	μ A

COMPARATOR

Threshold Voltage	V _{TH}	T _A =25°C	1.23	1.25	1.27	V
Threshold Voltage Line Regulation	Regline	V _{CC} = 3 to 40V	--	1.4	5.0	mV
Input Bias Current	I _{IB}	V _{IN} =0V	--	-20	-400	nA

ON/OFF DRIVER CONTROL

ON Driver Voltage	V _{ON}	V _{CC} = 3 to 40V	4	--	5	V
OFF Driver Voltage	V _{OFF}	V _{CC} = 3 to 40V	--	--	1	V
ON Driver Current	I _{on}	V _{CC} = 3 to 40V			100	mA

TOTAL DEVICE

Supply Current	I _{CC}	V _{CC} =5 to 40V, C _T =1.0nF, Pin7=V _{cc} , V _{PIN 5} >V _{th} Pin 2 =GND, remaining pins open	--	--	4.0	mA
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- Notes :** 1.Maximum package power dissipation limits must be observed.
 2.Low duty cycle pulse techniques are used during test to maintain junction temperature as close to ambient temperature as possible.

Table : Design Formula

Calculation	Step-Up	Step-Down	Voltage-Inverting
$\frac{ton}{toff}$	$\frac{V_{out}+V_F-V_{in}(min)}{V_{CC}(min)-V_{sat}}$	$\frac{V_{OUT}+V_F}{V_{CC}-V_{sat}-V_{OUT}}$	$\frac{ V_{out} + V_F}{V_{CC} + V_{sat}}$
(ton+toff) max	$\frac{1}{f \text{ min}}$	$\frac{1}{f \text{ min}}$	$\frac{1}{f \text{ min}}$
C _T	4.0×10 ⁻⁵ ton	4.0×10 ⁻⁵ ton	4.0×10 ⁻⁵ ton
I _{pk} (switch)	2I _{out} (max) $\left(\frac{ton}{toff} + 1 \right)$	2I _{out} (max)	2I _{out} (max) $\left(\frac{ton}{toff} + 1 \right)$
R _{sc} (R5)	0.3/I _{PK} (switch)	0.3/I _{PK} (switch)	0.3/I _{PK} (switch)
L (min)	$\left(\frac{V_{in}(min) - V_{sat}}{I_{pk}(switch)} \right) \cdot ton(max)$	$\left(\frac{V_{in}(min) - V_{sat} - V_{out}}{I_{pk}(switch)} \right) ton(max)$	$\left(\frac{V_{in}(min) - V_{sat}}{I_{pk}(switch)} \right) ton(max)$

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Co	$\left(9 \frac{I_{outton}}{V_{ripple(pp)}}\right)$	$\left(\frac{I_{pk}(switch)(ton + toff)}{8V_{ripple(pp)}}\right)$	$\left(9 \frac{I_{outton}}{V_{ripple(pp)}}\right)$
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TERMS AND DEFINITIONS

- ◆ V_{sat} = Saturation Voltage of the output switch.
- ◆ V_F = Forward Voltage drop of the rectifier.

The following power supply characteristics must be chosen:

- ◆ V_{in}= Normal input voltage
- ◆ V_{out}: Desied Output voltage, $|V_{out}|=1.25 \left(1 + \frac{R2}{R1}\right)$
- ◆ I_{out} : Desired output current.
- ◆ f_{min} : Minimum desired output switching frequency at the selected values for V_{in} and I_o.
- ◆ V_{ripple(p-p)}: Desired peak-to-peak output ripple voltage. in practice, the calculated capaitor value will need to be increased due to its equivalent series resistance and board layout. The ripple voltage should be kept to a low value since it will directly affect the line and load regulation.

CHARGE ALGORITHM

The PJ3963 uses a two-phase fast charge algorithm. In phase 1, the PJ3963 regulates constant current (I_{max}) until V_{battery} rises to V_{max}

nLi-ion battery: 1 cell = 4.2~4.3V, 2 cell = 8.4~8.5V

nNiMH battery: 1 cell = 1.35~1.5V, 2 cell = 2.8~3.0V

PJ3963 then transitions to phase 2 and regulates constant voltage (V_{battery} =V_{max}) until the charging current falls below the programmed I_{min} threshold (around I_{max}/10). Fast charge then terminates, and the PJ3963 enters the Charge Complete state.

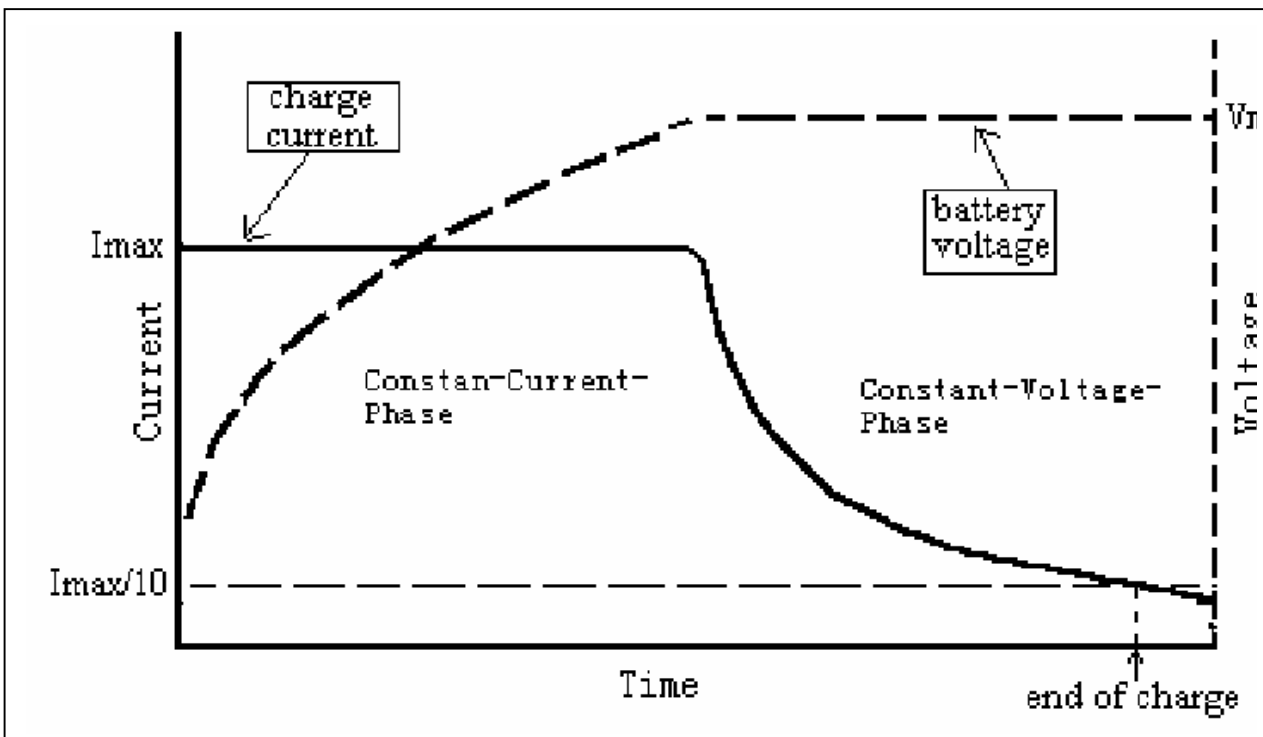


FIGURE 1. OUTPUT SWITCH ON-OFF TIME versus OSCILLATOR TIMING CAPACITOR

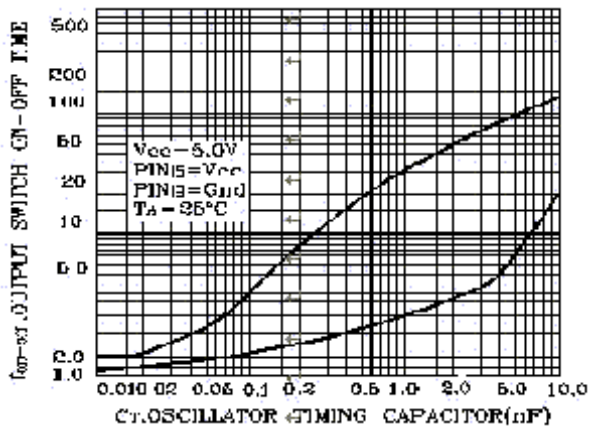


FIGURE 2. TIMING CAPACITOR WAVEFORM

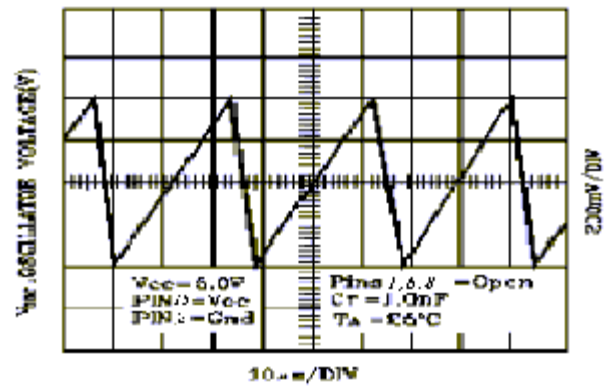


FIGURE 3. EMITTER FOLLOWER CONFIGURATION OUTPUT SATURATION VOLTAGE versus EMITTER CURRENT

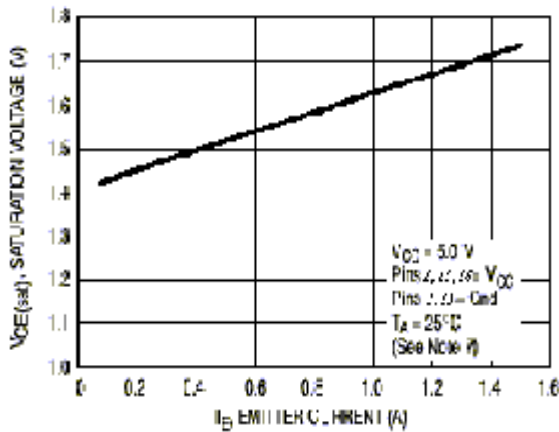


FIGURE 4. COMMON EMITTER CONFIGURATION OUTPUT SWITCH SATURATION VOLTAGE versus COLLECTOR CURRENT

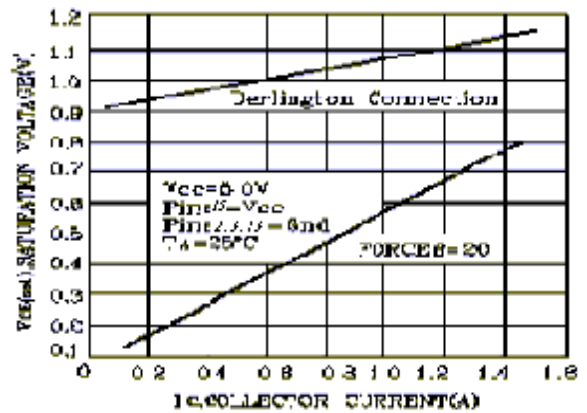


FIGURE 5. CURRENT LIMIT SENSE VOLTAGE versus TEMPERATURE

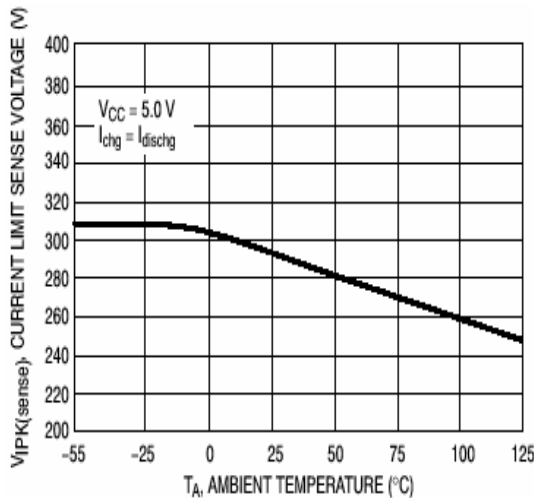
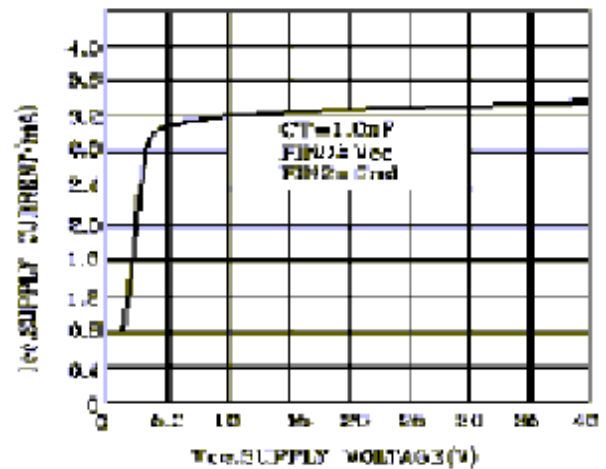


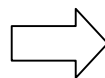
FIGURE 6. STANDBY SUPPLY CURRENT versus SUPPLY VOLTAGE



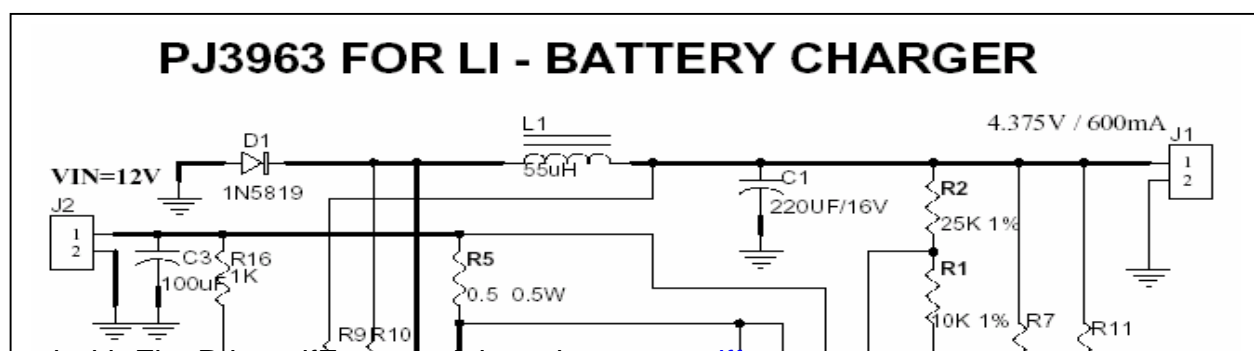
APPLICATION EXAMPLES

1. To design a Charger suitable for charging 3.6V/1000mAH Li-ion Battery

- n Power Supply: 12V / 290mA
- n Fast Charging Current: I_{max}=600mA
- n Maximum Charging Voltage: V_{max}=4.375V



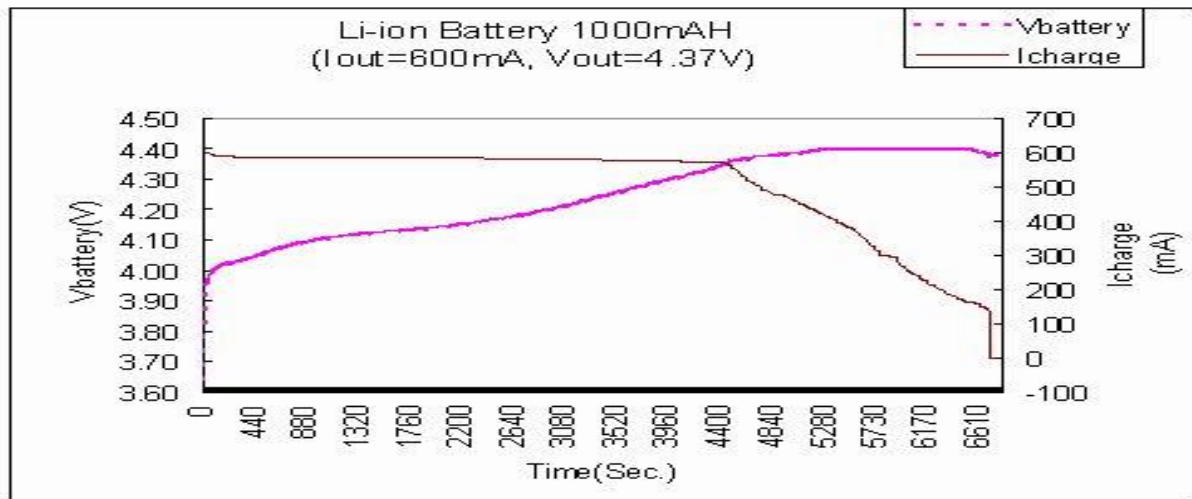
- n Charging Efficiency: 75%
- n Charging time: 2 Hour
- n Operation Temp.: Under 45°C



(The test condition is $V_{in}=12V$. If user changes the V_{in} , some parameters have to be adjusted.)

1. **$V_{out}=1.25V(1+R2/R1)$** ($V_{out}=4.375V$, $R1=10K\ \Omega$, $R2=25K\ \Omega$)
2. **$I_{out,max}= 0.3V /R5$** ($I_{out}=0.6A$, $R5=0.5\ \Omega$)
3. NTC, Temperature Protection: R3 is NTC Resistor. NTC in $25^{\circ}C$ is $10K\ \Omega$, $K=3977(R25/85)$, Tolerance:2%
 $R4=R3$ (NTC Value in $45^{\circ}C$) = $4.85K\ \Omega$ (substitute by $4.7K\ \Omega$)
 *NTC: $45^{\circ}C(4.83K)$, $50^{\circ}C(4.07K)$, $55^{\circ}C(3.45K)$, $60^{\circ}C(2.94K)$

Charging Curve:

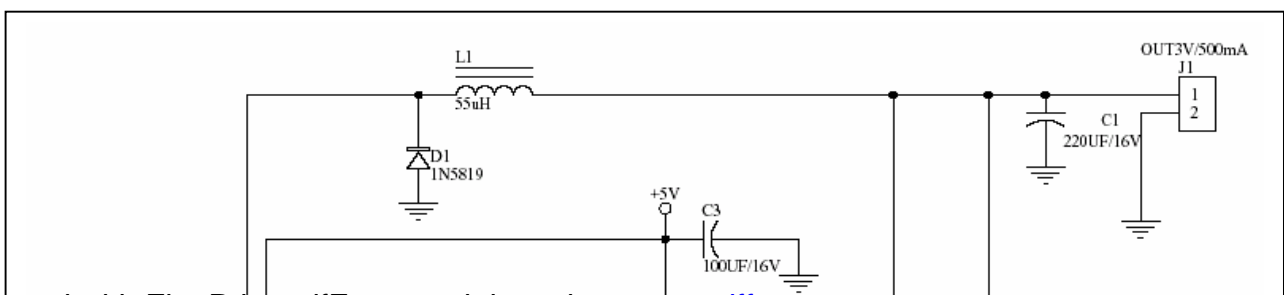


2. To design a USB Charge Circuit suitable for charging 2 series NiMH Battery, 2.4V/1500mAh.

- n Power Supply from USB Port: 5V/500mA
- n V_{out} : $R1=10K$, $R2=14K$
- n I_{max} : $R5=0.22\ \Omega /0.5W$
- n $V_L=1.8V$ Protection: $R11=2K$, $R4=2.5K$
- n $V_H=3.1V$ Protection: $R7=5K$, $R3=2.5K$

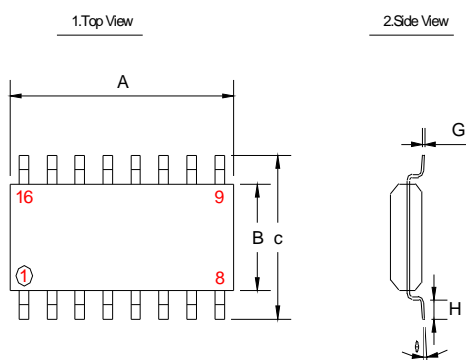


- n Output Voltage & Current: $V_{max}=3.0V$, $I_{max}=650mA$
- n Charging Efficiency: 75%
- n Charging time: 4~6 hour
- n Trickle Current: 30~50mA
- n Low Voltage Protection: 1.8V
- n High Voltage Protection: 3.1V



DIMENSION

SOP-16



3.DIMENSION

DIM	SOP-16 DIMENSION			
	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	9.8	10.20	0.386	0.402
B	3.80	4.00	0.150	0.157
C	5.80	6.20	0.228	0.244
D	1.35	1.50	0.053	0.059
E	0.35	0.49	0.014	0.019
F	1.27BSC		0.05BSC	