

PFC Specification

①

AC input : 90V ~ 265VAC
 Switching Frequency Fsw : 56kHz (R110 = 82k)
 Output Voltage Vout : 385V
 Pout ≈ 300 watt
 Power Factor : 95% @ Full load

A) 3) Max. peak line current

Efficiency, $\eta = 0.8$

Input Power $P_{in} = \frac{300}{0.8} = 375W$

$I_{max} = \frac{\sqrt{2} P_{in}}{V_{in(min)}} = \frac{1.41 \times 375}{90} = 5.875A$

II) Roughly take 20% as a ripple current from I_{max}

$\Delta I = 0.2 \times I_{max} = 0.2 \times 5.875 = 1.175A$ #

(peak to peak)

So, Max peak current $I_{Impk} = 5.875 + \frac{1}{2}(1.175) = 6.4625A$ #

III) Duty cycle at I_{pk} where $V_{in(min)}$ is at low line :

$D = \left[1 - \frac{V_{out}}{V_{in(min)}} \right]$

$= \left[1 - \frac{385}{90} \right]$

$= 0.766$ #

⇒ So, PFC circuit (Mosfet)

⇒ $V_{out} = 385V$

→ 耐圧電承受 400V 以上

⇒ $I_{Impk} = 6.4625A$, → 耐流電承受 6.4625A 以上

(B) PFC Boost Inductor (L102) ②

1) calculate inductance:

$$L = \frac{V_{in(peak)} * D}{F_{sw} * \Delta I} = \frac{590 * 0.766}{65000 * 1.175A} = 1.27mH \quad \#$$

(C) PFC Diode (D101)

$$\rightarrow V_{out} = 385V$$

→ 耐壓需承受 385V 以上.

$$I_{out(max)} = \frac{300W}{385V} = 0.78A \quad \#$$

→ 耐流需承受 0.78A 以上.

D) PFC Bulk Capacitor (C108)

取 V_{out} 允許 5% 的漣波

$$C_0 = \frac{I_{Lo} * \Delta V_0 * f}{0.78} = \frac{385 * 0.05 * 2 * 60}{0.78} = 337\mu F \quad \#$$

→ 可以選 ~~330~~ 330μF, 耐壓 450V 之 capacitor

I_{GTmax} = maximum gate drive capability of ICEIPSOI of $\pm 1A$.

$$R_{122} = \frac{V_{GTmax}}{I_{GTmax}} = \frac{11.5V \text{ (from Datasheet)}}{1A} \approx 11.5\Omega \approx 12\Omega \#$$

F) Gate drive resistor R_{122} (3)

F) Shunt Resistor R_{103}

→ current sense voltage limited to $V_{SENSEM} = -0.7V$

$$\therefore R_{103} = \frac{V_{SENSEM}}{I_{impk}} = \frac{-0.7V}{6.4625A}$$

$$= 0.1083\Omega$$

Shunt Resistor Power rating at least

$$P_{SH} = I_{impk}^2 \times R_{103}$$

$$= (6.4625)^2 \times 0.1083$$

$$= 4.52 \text{ watt} \#$$

R_{103} 選用 $1A$ 以下 0.1083Ω (可以選 0.07Ω), $5W$ 以上的電阻.

R_{104} 選用 200Ω #, limit the inrush current

$$\text{例 2} \quad C_{114} = 3.3 \mu\text{F} \quad \#$$

$$C_{114} = C'_{114} + 0.5 = 3.34 \mu\text{F}$$

为了电流环补偿，

$$C'_{114} (\text{pin 2 } I_{\text{comp}}) = \frac{I_{\text{OTA2}}}{2\pi \times f_p} = \frac{1 \text{ms}}{2\pi \times 56 \text{kHz}} = 2.84 \mu\text{F}$$

F) Current Control Loop Compensation.

$$\text{例 2} \quad \# \quad R_{106} = 220\text{k}, \quad R_{107} = 220\text{k}, \quad R_{108} = 220\text{k} \quad \#$$

$$\therefore R_{106} + R_{107} + R_{108} = 760 \text{ k}\Omega$$

$$= 760 \text{ k}\Omega \quad \#$$

$$= \frac{385 - 5\text{V}}{5\text{V}} \times 10 \text{ k}\Omega$$

$$R_A = \frac{V_{\text{out}} - V_{\text{ref}}}{V_{\text{ref}}} \times R_B$$

$$R_B = R_{112} = 10 \text{ k}\Omega$$

$$R_A = (R_{106} + R_{107} + R_{108})$$

G) Voltage divider of output voltage

5) Voltage compensated loop

⇒ 見例 C115 = 0.47μF

C116 = 0.47μF

R111 = 10kΩ

+) C132 見例電容 from 100pF ~ 470pF
不要太大, 因為會影響回過渡時間#