

SP6003 Synchronous Rectifier Driver

DESCRIPTION

The fundamental of SP6003 synchronous rectifier (SR) driver IC is based on our U.S. patented methods that utilize the principle of "prediction" logic circuit. The IC deliberates previous cycle timing to control the SR in present cycle by "predictive" algorithm that makes adjustments to the turn-off time, in order to achieve maximum efficiency and avoid cross-conduction at the same time. It also maintains the MOSFET's body diode conduction at minimum level. The SP6003 is capable to adapt in almost all existing flyback converters with few adjustments considered necessary.

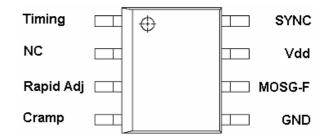
FEATURES

- Offers 4 to 8% efficiency improvement over Schottky Diode (depends on drive configuration of the SR).
- Drives all logic level Power MOSFET.
- Prediction gate timing control.
- Minimum MOSFET body diode conduction.
- Operating frequency up to 350 KHz.
- Synchronize to transformer secondary voltage waveform.

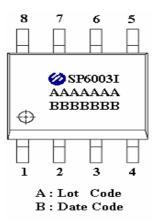
APPLICATIONS

- Servers & workstations
- Storage area network power supplies
- Telecommunication converters
- Embedded systems
- Industrial & commercial systems using high current processors

PIN CONFIGURATION (SOP-8)

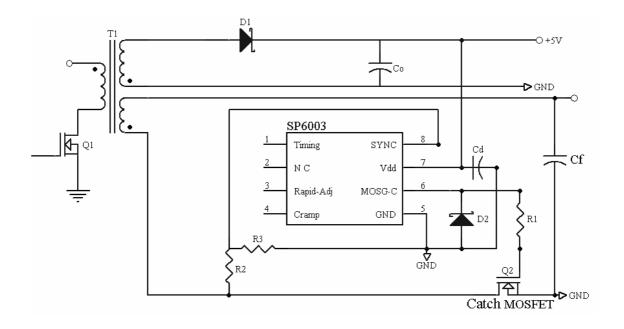


PART MARKING





TYPICAL APPLCATION CIRCUIT

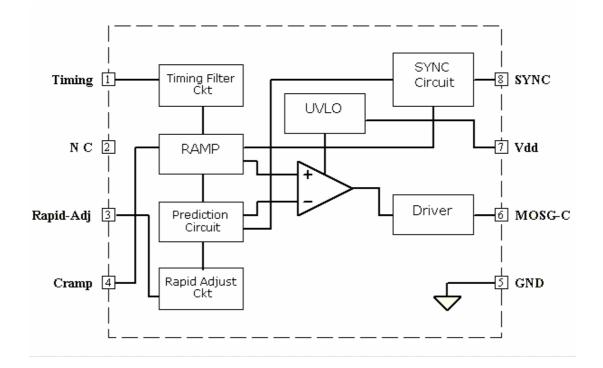


PIN DESCRIPTION

| Pin | Symbol | Description |
|-----|-----------|---|
| 1 | Timing | Discontinuous current filter timing adjustment resistor connection. |
| 2 | NC | No connection. |
| 3 | Rapid-Adj | Capacitor connection to adjust fast pulse width reduction response. |
| 4 | Cramp | Ramp capacitor adjustment to extend MOSFET's gate timing. |
| 5 | GND | Ground connection. |
| 6 | MOSG-C | Catch MOSFET gate drive. |
| 7 | Vdd | DC supply voltage. |
| 8 | SYNC | Synchronized signal from transformer's output. |



BLOCK DIAGRAM



ORDERING INFORMATION

| Part Number | Package | Part Marking |
|-------------|---------|--------------|
| SP6003S8RG | SOP-8 | SP6003I |
| SP6003S8TG | SOP-8 | SP6003I |

SP6003S8RG : Tape Reel ; Pb – Free

SP6003S8TG : Tube ; Pb – Free

ABSOULTE MAXIMUM RATINGS (TA=25 , unless otherwise specified.)

The following ratings designate persistent limits beyond which damage to the device may occur.

| Symbol | Parameter | Value | Unit |
|-------------------|--|------------|------|
| V_{dd} | DC Supply Voltage | 7 | V |
| SYNC | SYNC Voltage | 7.5 | V |
| I _{OUT} | Peak Source Current (Pulsed) | 1 | Α |
| | Peak Sink Current (Pulsed) | 1.5 | Α |
| P _D | Power Dissipation @ $T_A=85$ (*) | 0.25 | W |
| T_{J} | Operating Junction Temperature Range | -40 to125 | |
| T _{STG} | Storage Temperature Range | -40 to 150 | |
| T _{LEAD} | Lead Soldering Temperature for 10 sec. | 300 | |

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THERMAL RESISTANCE

| Symbol | Parameter | Value | Unit |
|--------|--|-------|------|
| Rojc | Thermal Resistance Junction – Case (*) | 45 | /W |

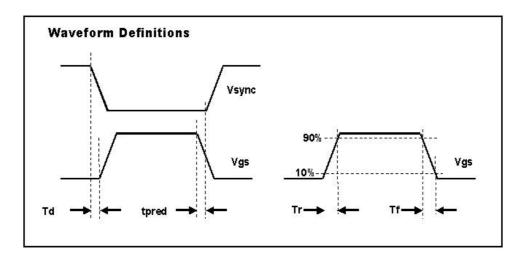
(*) The power dissipation and thermal resistance are evaluated under copper board mounted with free air conditions.

ELECTRICAL CHARACTERISTICS

(T_A=25 , V_{dd}=5V, V_{SYNC}=5V, Freq. =300 KHz, Duty Cycle=50%, unless otherwise specified.)

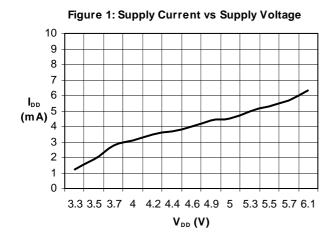
| Symbol | Parameter | Conditions | Min. | Typ. | Max. | Unit | | | |
|------------|------------------------|---------------|------|------|------|------|--|--|--|
| SUPPLY INI | UPPLY INPUT | | | | | | | | |
| IDD | Supply current | No load | 4 | 5.5 | 8 | mA | | | |
| Vonth | Vdd turn on threshold | | 4.1 | 4.2 | | V | | | |
| Voffth | Vdd turn off threshold | | | 4.0 | 4.1 | V | | | |
| SYNC REFE | CRENCE (SYNC) | | | | | | | | |
| Vshth | SYNC high threshold | | 3.7 | 3.9 | | V | | | |
| Vslth | SYNC low threshold | | | 0.9 | 1.1 | V | | | |
| MOSFET GA | ATE DRIVER (MOSG-C) | | | | | | | | |
| Voh | Output high voltage | Io = -200mA | 4.8 | 4.9 | 5.0 | V | | | |
| Vol | Output low voltage | Io=200mA | 0.0 | 0.1 | 0.2 | V | | | |
| Td | Propagation delay | No load | 15 | 20 | 25 | ns | | | |
| Tr | Rise time | Load = 1nF(*) | 24 | 28 | 36 | ns | | | |
| Tf | Fall time | Load = 1nF(*) | 20 | 23 | 30 | ns | | | |

(*) Tr & Tf are measured among 10% and 90% of starting and final voltage.

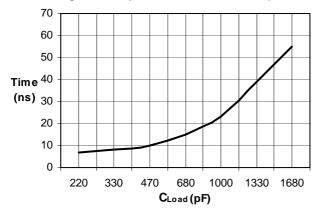


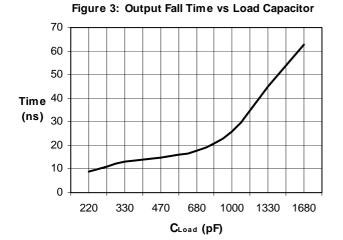


PERFORMANCE CHARACTERISTICS (T_A=25 , unless otherwise specified.)









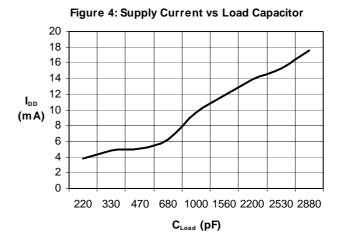
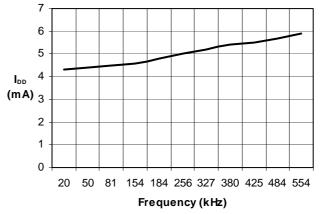
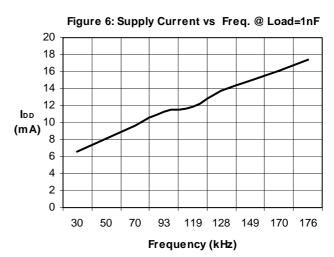


Figure 5: Supply Current vs Freq. @ No Load

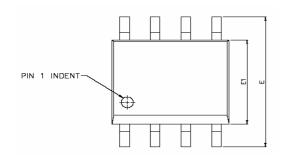


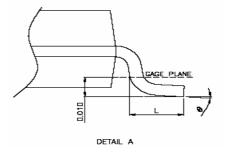


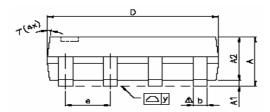
*Fig. 2-4: Frequency = 85 kHz.

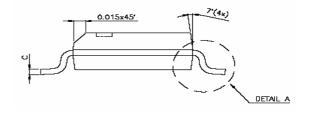


SOP- 8 PACKAGE OUTLINE









| 0/4/00/0 | DIMENSIONS IN MILLIMETERS | | DIMENSIONS IN INCHES | | | |
|----------|---------------------------|------|----------------------|--------|-------|--------|
| SYMBOLS | MIN | NOM | MAX | MIN | NOM | MAX |
| A | 1.47 | 1.60 | 1.73 | 0.058 | 0.063 | 0.068 |
| A1 | 0.10 | | 0.25 | 0.004 | | 0.010 |
| A2 | | 1.45 | | | 0.057 | |
| Ь | 0.33 | 0.41 | 0.51 | 0.013 | 0.016 | 0.020 |
| С | 0.19 | 0.20 | 0.25 | 0.0075 | 0.008 | 0.0098 |
| D | 4.80 | 4.85 | 4.95 | 0.189 | 0.191 | 0.195 |
| Е | 5.80 | 6.00 | 6.20 | 0.228 | 0.236 | 0.244 |
| E1 | 3.80 | 3.90 | 4.00 | 0.150 | 0.154 | 0.157 |
| е | | 1.27 | — | | 0.050 | |
| L | 0.38 | 0.71 | 1.27 | 0.015 | 0.028 | 0.050 |
| ∕∆ у | | | 0.076 | | | 0.003 |
| 0 | 0. | | 8' | 0. | | 8' |



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