# 承 認 規 格 書 SPECIFICATION

THE CONTENT	WILE I
品名規格 DESCRIPTION	:貼片電阻系列

安巨夕稲 CUSTOMER: 於旺達電子

增加專案 ADDITIONAL ITEM :

# 承認 APPROVED BY:

廠商	VENDOR	2	字戶 CUSTO	MER
CHECKED 確 認	APPROVED 承 認	CHECKED 確 認	APPROVED 承 認	APPROVED 承 認

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#### FEATURE

- 1. High reliability and stability
- 2. Reduced size of final equipment
- 3. Lower assembly costs
- 4. Higher component and equipment reliability

#### APPLICATION

- Consumer electrical equipment
- Automotive application
- EDP, Computer application
- Telecom application

#### DESCRIPTION

The resistors are constructed in a high grade ceramic body (aluminum oxide). Internal metal electrodes are added at each end and connected by a resistive paste that is applied to the top surface of the substrate. The composition of the paste is adjusted to give the approximate resistance required and the value is trimmed to within tolerance by laser cutting of this resistive layer.

The resistive layer is covered with a protective coat. Finally, the two external end terminations are added. For ease of soldering the outer layer of these end terminations is a Lead-tin solder alloy.

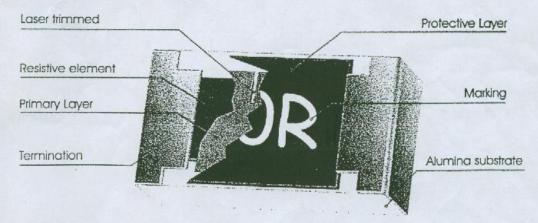


Fig 1. Consctruction of a Chip-R

## QUICK REFERENCE DATA

Item :		General Specification	1
Series No.	WR12	WR08	WR06
Size code	1206 (3216)	0805 (2012)	0603 (1608)
Resistance Tolerance	±1%	(E96 series), ±5% (E24 :	series)
Resistance Range	1Ω ~ 10MΩ (±5%	% tolerance), 10Ω ~ 1MΩ	2 (±1% tolerance)
TCR (ppm/°C) ≥10Ω ±5% Tolerance ≥10Ω ±1% Tolerance <10Ω		≤ ± 200 ppm/°C ≤ ± 100 ppm/°C -300~+500 ppm/°C	
Max. dissipation at Tamb=70°C	1/4 W	1/8 W	1/10 W
Max. Operation Voltage (DC or RMS)	200V	150V	50V
Climatic category (IEC 60068)	55/125/56	55/125/56	55/125/56

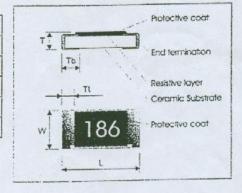
#### Note:

- This is the maximum voltage that may be continuously supplied to the resistor element, see "IEC publication 60115-8"
- 2. Max. Operation Voltage : So called RCWV (Rated Continuous Working Voltage) is determined by

RCWV = √Rated Power x Resistance Value or Max. RCWV listed above, whichever is lower.

### Dimensions

Jim Ciron	5110		4
	WR12	WR08	WR06
L	3.10 ± 0.10	2.00 ± 0.10	1.60 ± 0.10
W	1.60 ± 0.10	1.25 ± 0.10	$0.80 \pm 0.10$
Т	0.60 ± 0.15	0.50 ± 0.15	0.45 ± 0.15
Tb	0.45 ± 0.20	0.40 ± 0.20	0.30 ± 0.10
Tt	0.50 ± 0.20	0.40 ± 0.20	0.30 ± 0.10



#### Marking

Size	tolerance	±5%	±1%
1206		3-digits marking	4-digits marking
0805		3-digits marking	4-digits marking
0603		, 3-digits marking	No marking (3-digits marking upon requested)

#### 3-digits marking

Each resistor is marked with a three digits code on the protective coating to designate the nominal resistance value.

For values up to  $910\Omega$  the R is used as a decimal point. For values of  $1K\Omega$  or greater the first 3 digits apply to the resistance value and fourth indicate the number of zeros to follow.

#### 4-digits marking

Each resistor is marked with a four digits code on the protective coating to designate the nominal resistance

For values of  $976\Omega$  the R is used as a decimal point. For values of  $1K\Omega$  or greater the first 3 digits are significant, the fourth indicates the number of zeros to follow.

#### Example

RESISTANCE	10Ω	12Ω	100Ω	6800Ω	47000Ω
3-digits marking	100	120	101	682	473
4-digits marking	10R0	12R0	1000	6801	4702

## FUNCTIONAL DESCRIPTION

## Product characterization

Standard values of nominal resistance are taken from the E24 series for resistors with a tolerance of  $\pm 5\%$ , and E96 series for resistors with a tolerance of  $\pm 1\%$ . The values of the E24/E96 series are in accordance with "IEC publication 60063"

#### Derating

The power that the resistor can dissipate depends on the operating temperature; see Fig.2

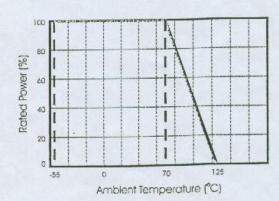


Fig.2 Maximum dissipation in percentage of rated power As a function of the ambient temperature

#### MOUNTING

Due to their rectangular shapes and small tolerances, Surface Mountable Resistors are suitable for handling by automatic placement systems.

Chip placement can be on ceramic substrates and printed-circuit boards (PCBs).

Electrical connection to the circuit is by individual soldering condition.

The end terminations guarantee a reliable contact.

#### SOLDERING CONDITION

The robust construction of chip resistors allows them to be completely immersed in a solder bath of 260°C for one minute. Therefore, it is possible to mount Surface Mount Resistors on one side of a PCB and other discrete components on the reverse (mixed PCBs).

Surface Mount Resistors are tested for solderability at 235°C during 2 seconds. The test condition for no leaching is 260°C for 60 seconds. Typical examples of soldering processes that provide reliable joints without any damage are given in Fig 3.

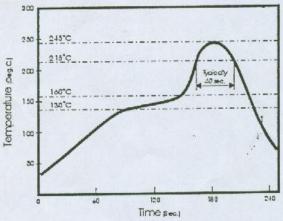


Fig 3. Infrared soldering profile for Chip Resistors

# TEST AND REQUIREMENTS

TEST DC resistance	DC societana	PROCE				REQUIREMENT	
DC resistance	IDC secietare				Resistor	Jumpe	
	voltages spec	e values r ified below :	measured :	at the tes	t Within the specified tolerance	< 50mΩ	
	Resistance	<100Ω	<1KΩ	<10KΩ			
	Test voltage	0.3V	1.0V	3.0V			
	Resistance	<100ΚΩ	<1MΩ	<10ΜΩ			
	Test voltage	10V	25V	50V			
TCR	Natural resista centigrade.			e in degree	Test temperature ~55~+125°C ≥10Ω, ±5% ≤±200ppm/°C	N/a	
	$\frac{R_2 - R_1}{R_1(t_2 - t_1)} \times 1$	10 <sup>6</sup> (ppm/°C	2)		≥10Ω, ±1% ≤±100ppm/°C <10Ω -300~+500ppm/°C		
	R <sub>1</sub> : Resistance	e at referenc	e temperat	ure			
	R <sub>2</sub> : Resistance	e at test tem	perature				
	t <sub>1</sub> : 25°C						
Short time overload	Permanent res application of a maximum ove above list, which	a voltage 2.	5 times RC ge specifie	WV or the	ΔR/R max. ±(2%+0.10Ω)	< 50mΩ	
Resistance to soldering heat	Unmounted chi	ps 10±1 sec	conds, 260±	5°C	no visible damage Δ R/R max. ±(1.0%+0.05Ω)	no visible damage, < 50mΩ	
Solderability	Unmounted ch seconds in a so	ips comple older bath at	tely immer 230±5°C	sed for 5	good tinning (>95% covered) no visible damage		
emperature	1. 30 minutes	at -55°C±3°	°C,		no visible damage	no visible	
ycling	2. 10~15minu	tes at room	temperatur	θ,	ΔR/R max. ±(1%+0.05Ω)	damage,	
	3. 30 minutes	at +125°±3°	°C,			< 50mΩ	
	4. 10~15minu	tes at room	temperature	э,			
	5 continuous cy	cles					
oad life endurance)	70±2°C, 1000 Vmax,1.5 hours	hours, loa	ded with f	RCWV or	10Ω~1MΩ ±(3%+0.1Ω)	< 50mΩ	
	VIIIdx, 1.5 Hours	on and 0.5	nours on		<10Ω or ≥1MΩ ±(5%+0.1Ω)		
oad life in umidity Damp heat)	1000 hours, at a in humidity char 90~95% relative hours off	mber contro	ller at 40°C	±2°C and	10Ω~1MΩ ±(3%+0.1Ω) <10Ω or ≥1MΩ ±(5%+0.1Ω)	< 50mΩ	
ending and ermination rength	Resistors mount PCB(FR4); bend Pulling test : >50	ling: 5 mm,	nm glass eg once for 10	poxy resin seconds	no visible damage ΔR/R max. ±(1%+0.05Ω)	no visible damage,	

## TEST CONDITION FOR JUMPER (0 Ω)

Item	WR12	WR08	WR06
Power Rating At 70°C	1/4W	1/8W	1/10W
Resistance		MAX.50mΩ	
Rated Current	7 2A	1.5A	1A
Peak Current	5A	3.5A	3A
Operating Temperature		-55~125°C	

## Catalogue numbers

The resistors have a catalogue number starting with

WR12	X	472
Size code	Type code	Resistance code
WR12 : 1206 WR08 : 0805 WR06 : 0603	X: Normal Y: Special A: Lead-Free	E24: 2 significant digits followed by no. of zeros $4.7\Omega = 4R7$ $10\Omega = 100$ $220\Omega = 221$ Jumper =000  E96: 3 significant digits followed by no. of zeros $102\Omega = 1020$ $37.4K\Omega = 3742$

172	J
ode	Tolerance
gnificant digits by no. of zeros =4R7 =100	F:±1% J:±5% P:Jumper
=221 =000 gnificant digits by no. of zeros =1020 =3742	
	ode gnificant digits by no. of zeros  =4R7  =100  =221  =000 gnificant digits by no. of zeros  =1020

	T	
Packag	ing code	
T	: Reeled	
В	: Bulk	
		.*
	75	

- Reeled tape packaging: 8mm width paper taping 5000pcs per reel.
- Bulk packaging : 5000pcs per polybag

