

Dual N-Channel 20-V (D-S) MOSFET

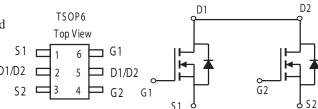
These miniature surface mount MOSFETs utilize High Cell Density process. Low $r_{DS(on)}$ assures minimal power loss and conserves energy, making this device ideal for use in power management circuitry. Typical applications are DC-DC converters, power management in portable and battery-powered products such as computers, printers, PCMCIA cards, cellular and cordless telephones.

•	Low _{DS(on)} Pr	rovides Higher	Efficiency	and
	Extends Batte			

• Low gate charge 7nC

- High performance
- High current handling
- Miniature TSOP-6 Surface Mount Package Saves Board Space

PRODUCT SUMMARY					
V _{DS} (V)	R _{DS (on)} (ohm)	I _D (A)			
20	$0.025 @ V_{GS} = 4.5V$	6.0			
20	$0.035 @ V_{GS} = 2.5V$	5.2			



ABSOLUTEM AXIMUM RATINGS (T $_{A}$ = 25 $^{\circ}$ C UNLES SOTHERWISEN OTED)					
Parameter			M axi mum	Units	
Drain-Source Voltage			20	V	
Gate-Source Voltage		V_{GS}	±10	V	
Continuous Drain Current ^a	$T_A=25^{\circ}C$ $T_A=70^{\circ}C$	I.	3.8		
Continuous Drain Current	$T_A=70^{\circ}C$	ъ	3.0	A	
Pulsed Drain Current ^b			10		
Continuous Source Current (Diode Conduction) ^a		I_S	0.46	A	
D	$T_A=25^{\circ}C$	D	1.25	W	
Power Dissipation ^a	$T_A=25^{\circ}C$ $T_A=70^{\circ}C$	LD	0.8	vv	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	-55 to 150	°C	

TH ER MALR ESISTANCE RATINGS						
Parameter	Symbol	M aximum	Units			
M · T	t <= 5 sec	D	100	00/11/		
Maximum Junction-to-Ambient ^a	Steady-State	R_{THJA}		C/W		

Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature



_			Li mits				
Parameter	Symbol	Test Conditions	Min		M ax	Unit	
Static	70.00		020				
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 \text{ V}, I_D = 250 \text{ uA}$	20			987	
Gate-Threshold Voltage	V _G S(th)	$V_{DS} = V_{GS}$, $I_D = 250 \text{ uA}$	0.5	0.8	1.5	V	
Gate-Body Leakage	Igss	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 10 \text{ V}$			±100	nA	
		$V_{DS} = 16 \text{ V}, V_{GS} = 0 \text{ V}$			1		
Zero Gate Voltage Drain Current	IDSS	$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 55^{\circ}\text{C}$			10	uA	
On-State Drain Current ^A	I _{D(on)}	$V_{DS} = 5 \text{ V}, V_{GS} = 4.5 \text{ V}$	10			A	
Δ	rDS(on)	$V_{GS} = 4.5 \text{ V}, I_D = 6.0 \text{A}$		25			
Drain-Source On-Resistance ^A		$V_{GS} = 2.5 \text{ V}, I_D = 5.2 \text{ A}$		35		mΩ	
Forward Tranconductance ^A	gs	$V_{DS} = 5 \text{ V}, I_D = 3.0 \text{ A}$		11		S	
Diode Forward Voltage	V _{SD}	$I_S = 2.00 A, V_{GS} = 0 V$		0.80	1.20	V	
Dynamic ^b							
Total Gate Charge	Qg	V10 V V 4 5 V		11		nC	
Gate-Source Charge	Qgs	$V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V},$		2.20			
Gate-Drain Charge	Qgd	$I_D = 4.0 A$		2.50			
Switching							
Turn-On Delay Time	td(on)			9	17		
Rise Time	tr	$V_{DD} = 10 \text{ V}, \qquad \text{ID} = 1 \text{ A},$		11	18		
Turn-Off Delay Time	td(off)	$R_G = 10 \text{ ohm}$, $V_{GEN} = 4.5 \text{ V}$		18	29	ns	
Fall-Time	tf			5	10		

- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.m

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