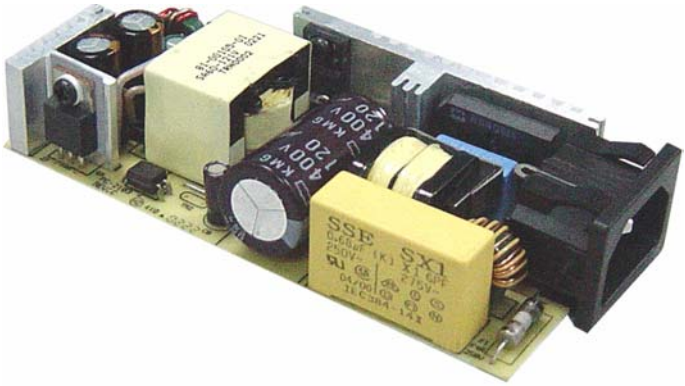
PNB60121T-SG6840

Contents

General Specifications



Input voltage.....90V AC to 264V AC

Input frequency.....47Hz to 63Hz

Outputs.....(12V/5A),60W 19V/3.16A

Efficiency.....85%

Hold up time.....≧17mSec

Over load protection

Short circuit protection

Over voltage protection

Cooling.....Free air convection

Storage temperature.....-20c to +85c

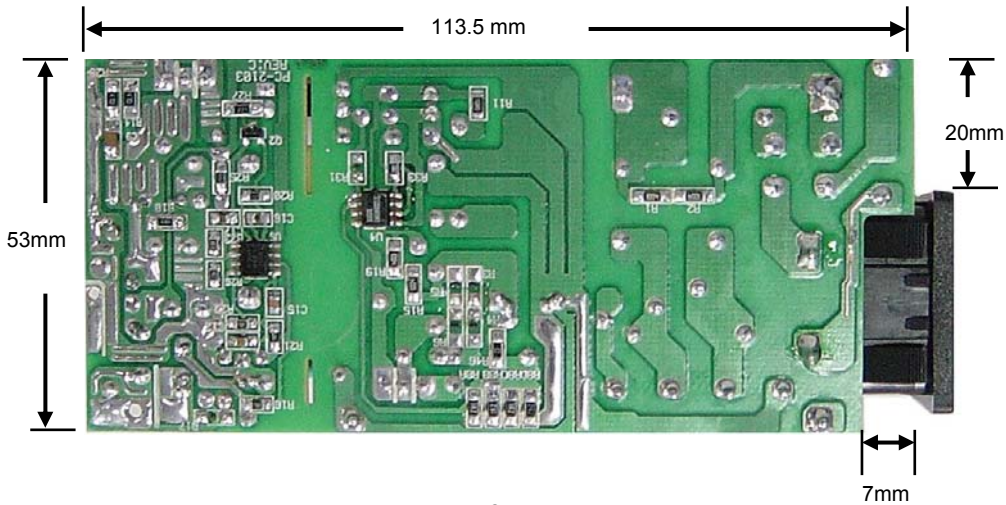
EMI test standard .....CISPR-22 Class B

EMS test standard.....EN61000-4-X VER:97

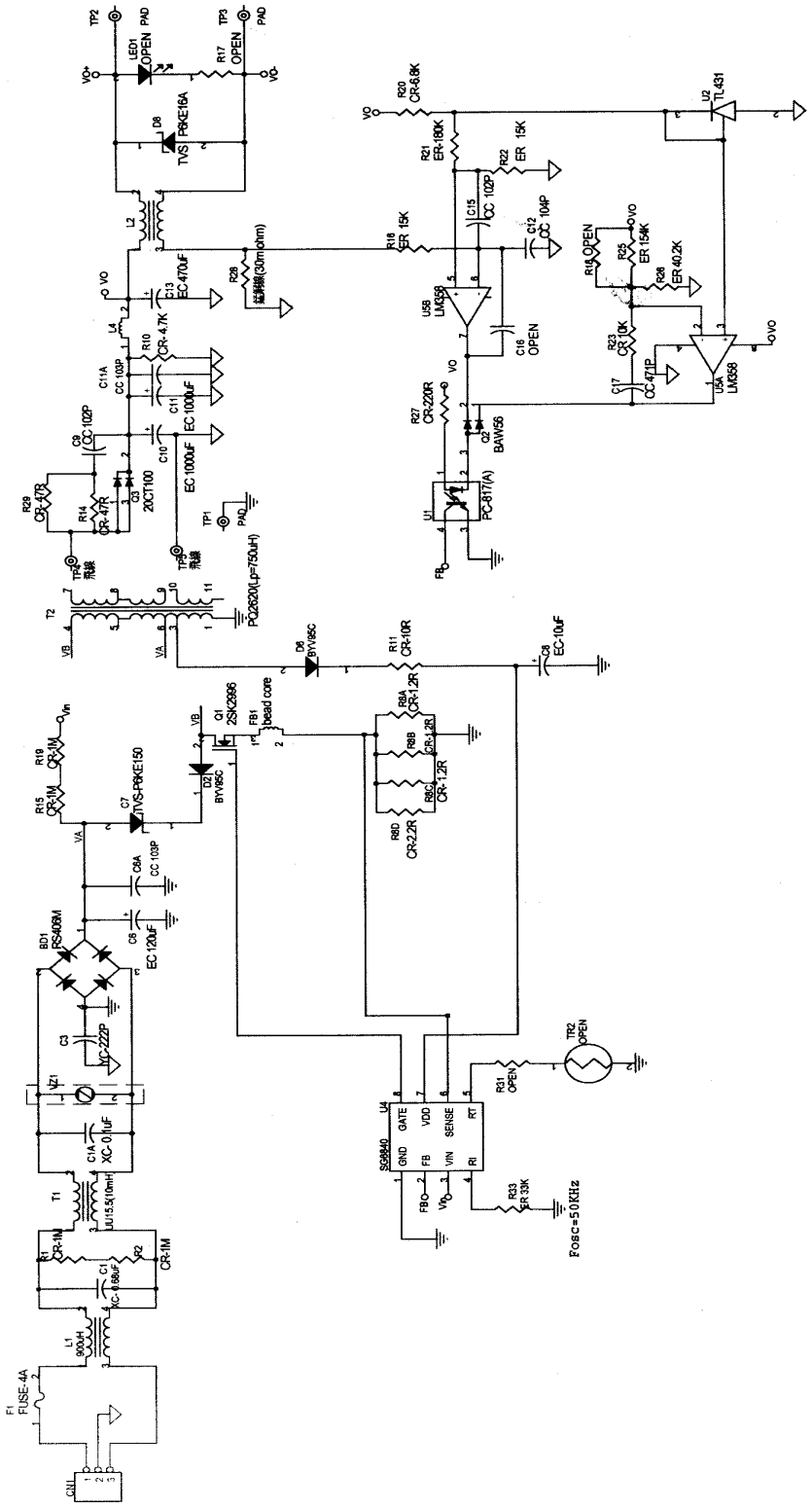
Notes:

1.Dimensions shown in mm as above. Tolerance specified is 0.4mm

2.PCB Size L:113.5 mm W:53mm H:20mm



Demo Board Circuit Diagram



PNB60121T-SG6840

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BOM

Quantity	Part Number	Description	Note
1	BD1	BD KBJ406G	
1	C1	XC 0.68u/275V	
1	C1A	XC 0.1u/275V (mini)	
1	C3	YC 222P/250V	F/mini
1	C6	EC 120u/400V-105℃(18Ø*31mm)	Nippon-KMG
1	C8	EC 10u/50V-105℃	
1	C9	CC 102P(100V/X7R)	1206
2	C10,C11	EC 680u/25V-105℃	TEAPO(10fØ20mm)
1	C12	CC 104P(50V/X7R)	0805
1	C13	EC 220u/35V-105℃	Nippon LXE(8Ø*20mm)
0	C16	OPEN	
1	C15	CC 102P(50V/X7R)	0805
1	C17,	CC 471P(50V/X7R)	0805
2	C6A,C11A	CC 103P(500V)	DIP
2	D2,D6	BYV95C	D2:W (with ferrite bead) D6:W (with ferrite bead)
1	C7	TVS P6KE150A	
1	D8	TVS P6KE16A	W
1	F1	FUSE 4A/250V	W/5*15
1	T1	TRN0018	
1	T2	TRN0002-121(REV:2.0)	
1	L1	TRN0003	
1	L4	TRN0083	
1	Q1	2SK2996	
1	Q2,	BAW56	SMD
1	Q3	20CTQ100	S.G.S
4	R1,R2,R15,R19	ER 1M	1206
0	R3,R4,R5,R6,R10	Open	
0	R17,R18,R31,TR2	Open	
3	R8A,R8B,R8C	CR 1.2W	1206

1	R8D	CR 2.2Ω	1206
1	R11	CR 10Ω	0805
2	R16, R22	ER 15K	0805
2	R14,R29	CR 47Ω	1206
1	R20	CR 6.8K	0805
1	R21	ER 180K	0805
1	R23	CR 10K	0805
1	R25	ER 154KΩ/1/8W±1%	0805
1	R26	ER 40.2KΩ/1/8W±1%	0805
1	R27	CR 220Ω	1206
1	R28	30mR Mn-Cu Wire	
1	R33	ER 33K	0805
1	R46	0Ω	1206
1	U1	K1010H	DIP (4 Pin)
1	U2	TL431 1%	
1	U4	SG6840	SMD(8PIN)
1	U5	LM358	SMD(8PIN)
1	FB1	BEAD CORE(MCH0041)	(C8B 3.5*3.2*1.0+T)
3	D6,D2	BEAD Core (MCH0040)	(C8B 3.5*3.2*1.0)
3	J1,2,3	JUMPER	
2	Q1,Q3	3*8 screw	
1	Q3	TR-220 Isolator- 1	
1	Q3	TO-220 Isolator- 2	
1	Q1 Heatsink	MCH0089	
1	Q3 Heatsink	MCH0090	
1	AC Socket	SC-8R(3P1S)	
3	J1,C6	1Ø*10mm Silicon tube	MCH0340
1	PCB	PC-2103 REV: C	

Transformer Specification

1.Safety reference standard IEC950

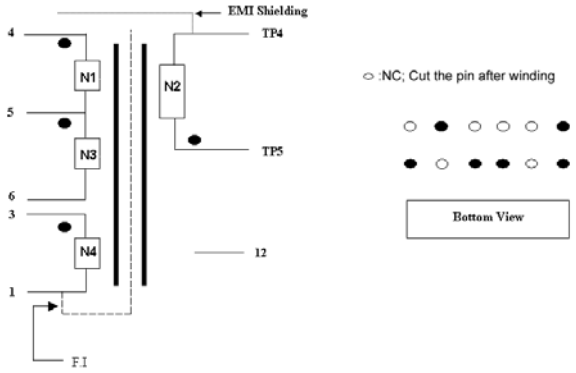
2.Surface, Structure

2.1Surface: damage, rusting, etc. are not permitted
2.2The shape, dimension and marking of the transformer: are as below mention.

3.Mechanical Performance

Terminal strength: Each terminal of the transformer must be withstandinga pull 5Kg for 10 second ,without loosening ,breaking.

4.Electrical:



5.Winding Table

Insulation Tape	Winding	Terminal	Wire Gauge (mm)	Turns (T)	Note
P	N1	4 - 5	0.4*1	20	(A)
1	Copper-Foil -> Pin1 1.2T open loop				
S 3	N2	TP5-TP4	1.0	7	(B)
3	Copper-Foil -> Pin1 1.2T open loop				
P 1	N3	5 - 6	0.4*1	18	(A)
P 1	N4	1 - 3	0.25*2	9	(C)
3	PQ2620 Ferrite Code				

(A)Lp1(Pin4-6) inductance = 680~730uH;Leakage inductance < 10uH(Shortcircuit N2, measure the inductance of pin 4-6)
(B)By using "Triple insulated wire" for N2; winding is from the bottom to the top of bobbin.
(C)N4:loosely winding
(D)The output with tube

6.Core

Core Type : PQ2620

7.Core Gap

Bobbin & Pin
Model No: PQ2620

8.Magnet Wire

Polyurethane Enamel Copper Wire≧130°C
Pacific; UL E84081

9.Triple insulated winding wire

TEX-E ; No.1950
UL E57568(S)

10.Tape

0.025mm, Polyester film tape; No.35660
Four Pillars; UL E520292(S)
Teraoka Seisakusho; UL E56086(S)

11.Impregnation

Varnish Ts-2414V(Tesh Chemical Co.)≧130°C

12.Insulation

Outerwrap : Three layers min.;0.025mm/layer polyester film tap total0.075mm.
Primary to Primary : One layer min.; 0.025mm polyester film tape.
Secondary to Primary :Three layers min.; 0.025mm/layer Polyester film tape total 0.075mm.
Primary to Core : obbin,1.0mm min. thick.
Crossover : One layer min.; 0.025mm polyester film tape.

13.Isolation

Primary to Secondary : 100M ohms min.
Primary to Core : 100M ohms min.
All winding to each other : 100M ohms min.
Primary to Secondary : 3000VAC,50/60Hz,0.5mA max.
Primary to Core : 500VAC,50/60Hz,0.5mA max.

14.Primary Inductance of Pin4 To Pin6:770-820uH

C h e c k L i s t

Test Model	SG6840 for 60W - 12V/5A		
Test Date:			
Test Temperature	Ambient		
Test Equipment	AC Source: CHROMA 6530 Electronic Load: Zentech 2600A Multimeter: BRYMEN BM729 Oscilloscope: Tektronix TDS3032		
Test Summary	1 Input Current:	√ Pass	Fail
	2 Input Wattage at DC output no-load condition:	√ Pass	Fail
	3 Line Regulation & Load Regulation:	√ Pass	Fail
	4 Ripple & Noise:	√ Pass	Fail
	5 Over shoot & Under Shoot Test:	√ Pass	Fail
	6 Current Limit & Constant Power:	√ Pass	Fail
	7 Short circuit protection:	√ Pass	Fail
	8 Efficiency:	√ Pass	Fail
	9 DC output rise time:	√ Pass	Fail
	10 Turn on time:	√ Pass	Fail
	11 Hold up time:	√ Pass	Fail
	12 Dynamic load:	√ Pass	Fail
	13 Over Voltage Protection:	√ Pass	Fail
	14 Burn-in test:	√ Pass	Fail
	15 Brown out test:	√ Pass	Fail
Note	Max. Load=5A Mid. Load=2.5A Min. Load=0A		

1.Input Current:

1.1 Test Condition:

Load: Max. Load Input current: 1.3A Max.

1.2 Test Result:

Input Voltage	Input Current	Test Specifications
115V/60Hz	1.25A	
230V/60Hz	0.7A	

2.Input Wattage at DC output no-load condition:

2.1 Test Condition:

Load: Min. Load Less than 1.0Watt at nominal line condition.

2.2 Test Result:

Input Voltage	Input Power	Stability	Test Specifications
120V/60Hz	0.25W	12V	<1W
240V/50Hz	0.35W	12V	<1W
264V/50Hz	0.4W	12V	<1W

3.Line Regulation & Load Regulation:

3.1 Test Condition:

Line regulation: 1% Max. Load regulation: 5% Max.

3.2 Test Result:

Input Voltage	Max. Load	Mid. Load	Min. Load	Test Spec.
90V/60Hz	11.78V	11.88V	12V	11.4V~12.6V
115V/60Hz	11.78V	11.88V	12V	11.4V~12.6V
132V/60Hz	11.78V	11.88V	12V	11.4V~12.6V
180V/50Hz	11.78V	11.88V	12V	11.4V~12.6V
230V/50Hz	11.78V	11.88V	12V	11.4V~12.6V
264V/50Hz	11.78V	11.88V	12V	11.4V~12.6V
Line Regulation	0%			1%
Load Regulation	1.8%			5%

4.Ripple & Noise:

4.1 Test Condition:

Tested by DC loading side parallel with a 10uF/EC and 0.1uF/CC capacitor and Measured Band-width with DC-20MHz

4.2 Test Result:

Input Voltage	Max. Load	Mid. Load	Min. Load	Test Spec.
90V/47Hz	40mV	30mV	10mV	100mV
115V/60Hz	35mV	25mV	10mV	
230V/63Hz	25mV	15mV	10mV	

5.Over shoot & Under Shoot Test:

5.1 Test Condition:

Less than 5% of nominal voltage value Load: Max. load

5.2 Test Result:

Input Voltage	Over Shoot	Under Shoot	Test Spec.
90V/47Hz; Min. Load	160mV	0mV	<5%
90V/47Hz; Max. Load	240mV	100mV	<5%
264V/63Hz; Min. Load	160mV	0mV	<5%
264V/63Hz; Max. Load	240mV	100mV	<5%

6.Current Limit & Constant Power:

6.1 Test Condition:

An over current from the output to return line will not damage the power supply. The protection will be enabled if the output current exceeds 5.5A ~ 6.5A.

6.2 Test Result:

Input Voltage	Output Current (A)	Test Spec.
90V/60HZ	5.8A	
115V/60HZ	5.8A	
132V/60HZ	5.8A	
180V/50HZ	5.8A	
230V/50HZ	5.8A	
264V/50HZ	5.8A	

7.Short circuit protection:

7.1 Test Condition:

Short-circuit the output the power supply will be protected and AC power input will be less than 5W (auto recovery)

7.2 Test Result:

Input Voltage	Max. Load		Mid. Load		Min. Load		Test Condition
90V/47Hz	√ Pass	Fail	√ Pass	Fail	√ Pass	Fail	<5W
264V/63Hz	√ Pass	Fail	√ Pass	Fail	√ Pass	Fail	<5W

8.Efficiency

8.1 Test Condition:

Load: Max. load Efficiency: 80% minimum at nominal line input

8.2 Test Result:

Input Voltage	Max. Load	Test Spec.
90V/60HZ	85%	>80%
115V/60HZ	85%	>80%
132V/60HZ	85%	>80%
180V/50HZ	86%	>80%
230V/50HZ	87%	>80%
264V/50HZ	87%	>80%

9.DC output rise time:

9.1 Test Condition:

Load: Max. load & Min. load DC Output rise time: 20mS max.

9.2 Test Result:

Input Voltage	Max. Load	Min. Load	Test Spec.
90V/47Hz	9mS	2.5mS	< 20mS
264V/63Hz	9mS	2.5mS	< 20mS

10.Turn on time:

10.1 Test Condition:

Load: Max. load & Min. load AC Switch on time: 4Sec max.

10.2 Test Result:

Input Voltage	Max. Load	Min. Load	Test Spec.
90V/47Hz	3.3Sec	3.3Sec	<4Sec
110V/60Hz	2.2Sec	2.2Sec	<3Sec
220V/50Hz	1Sec	1Sec	<3Sec

11.Efficiency

11.1 Test Condition:

Load: Max. load & Min. load DC Hold up time:

11.2 Test Result:

Input Voltage	Max. Load	Min. Load	Test Spec.
90V/47Hz	8mS	N/A	
110V/60Hz	15mS	N/A	
220V/50Hz	80mS	N/A	

12.DC output rise time:

12.1 Test Condition:

Dynamic loading (20% ~ 80% of the full load, 50mesc duty cycle)

12.2Test Result:

Input Voltage	Over Shoot	Under Shoot	Stability	Test Spec.
115V	250mV	250mV	N/A	
230V	250mV	250mV	N/A	

13.Over Voltage Protection:

13.1 Test Condition:

Over voltage protection: 16~ 17 VDC Load: Max. load & Min. load

13.2Test Result:

Input Voltage	Max. Load	Min. Load	Test Spec.
115V	N/A	17V	
230V	N/A	17V	

14.Burn-in test:

14.1 Test Condition:

100% Burn-in at full load and 1 hours at least.

14.2 Test Result:

Burn-in test	√ Pass	Fail	Test Spec. : >1hours

15.Brown out test

15.1 Test Condition:

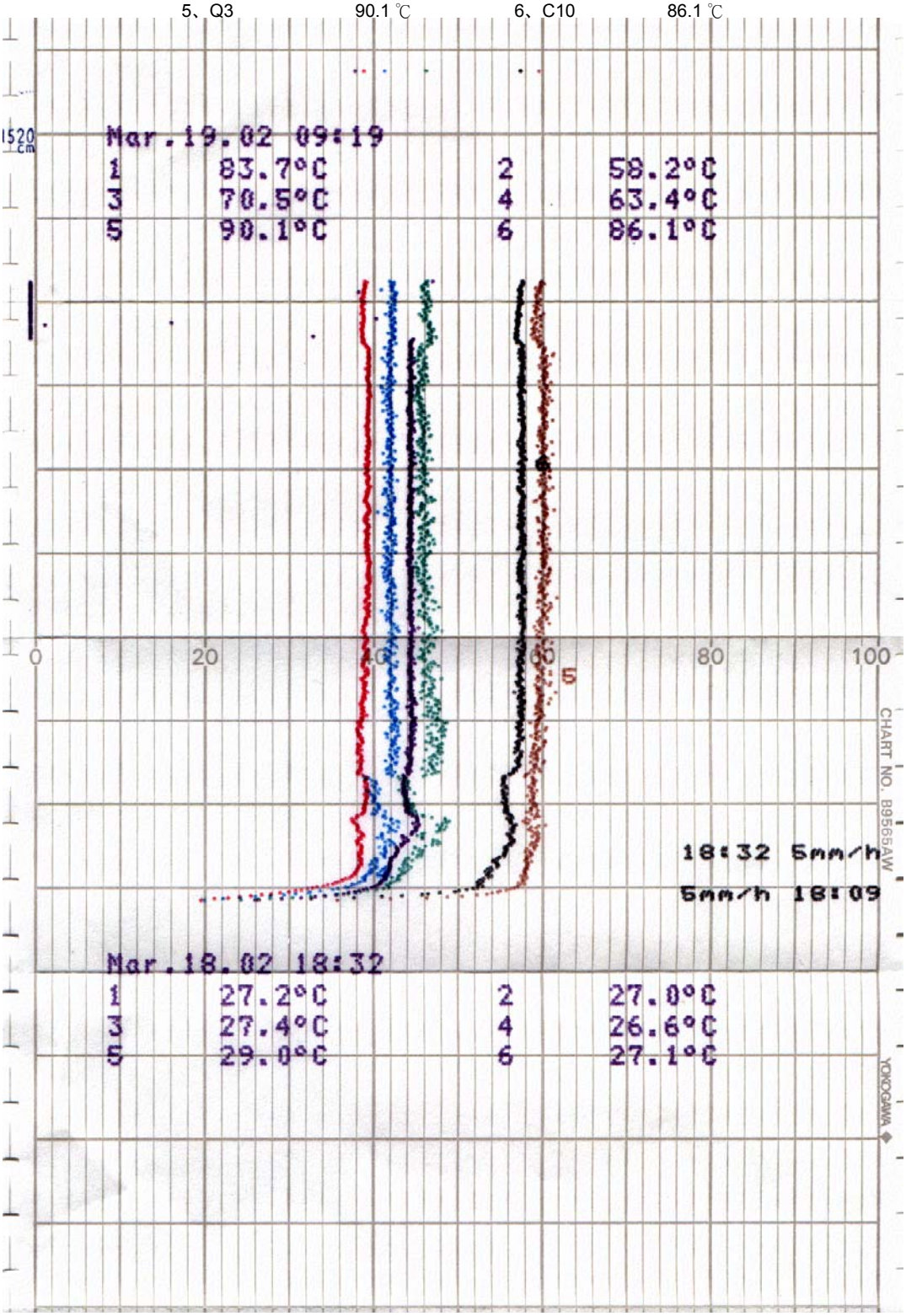
The power supply shall meet its output specification while the line voltage is reduced in 5 voltage decrements from 264Vac to 90Vac, with a minimum dwell at each increment of 15 minutes. The voltage transition time shall be equal to or less than 5 seconds. The power supply shall recover and continue to meet its output specification after the line voltage is reduced in 5 volt decrements from 264Vac to 0Vac and subsequently increased to 85Vac (minimum dwell at each increment of 15 minutes). The voltage transition time shall be equal to or less than 5 seconds.

15.2 Test Result:

Input Voltage	Input Power
Vin=90V	71W
Vin=85V	71.3W
Vin=80V	71.9W
Vin=75V	72.6W
Vin=70V	72.8W
Vin=65V	71W
Vin=60V	67W
Vin=55V	62W
Vin=50V	56W
Vin=45V	50W
Vin=40V	42W
Vin=35V	0W
Vin=30V	0W
Vin=25V	0W
Vin=20V	0W
Vin=15V	0W
Vin=10V	0W

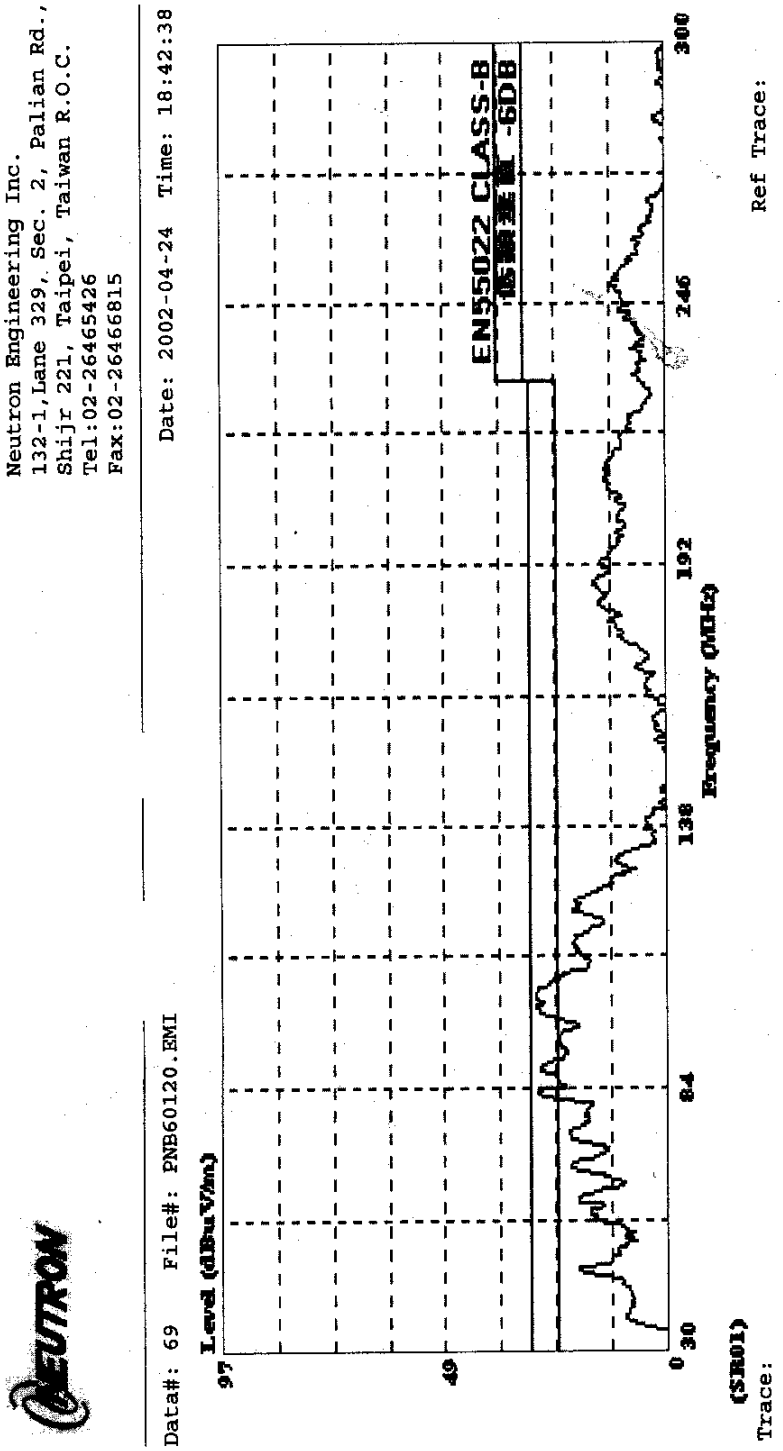
Temperature Record

1、BD1	83.7 ℃	2、C6	58.2 ℃
3、Q1	70.5 ℃	4、T2	63.4 ℃
5、Q3	90.1 ℃	6、C10	86.1 ℃

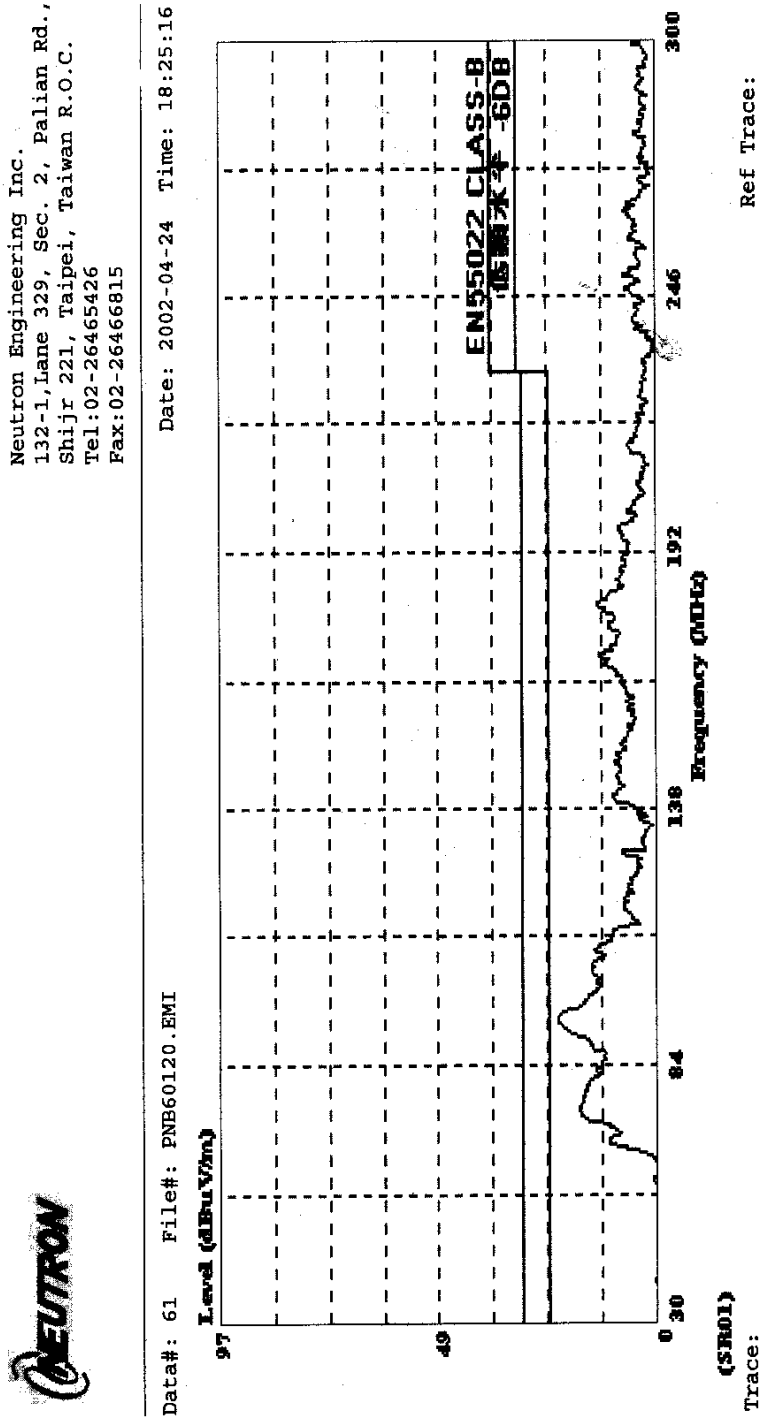


EMI Test Report

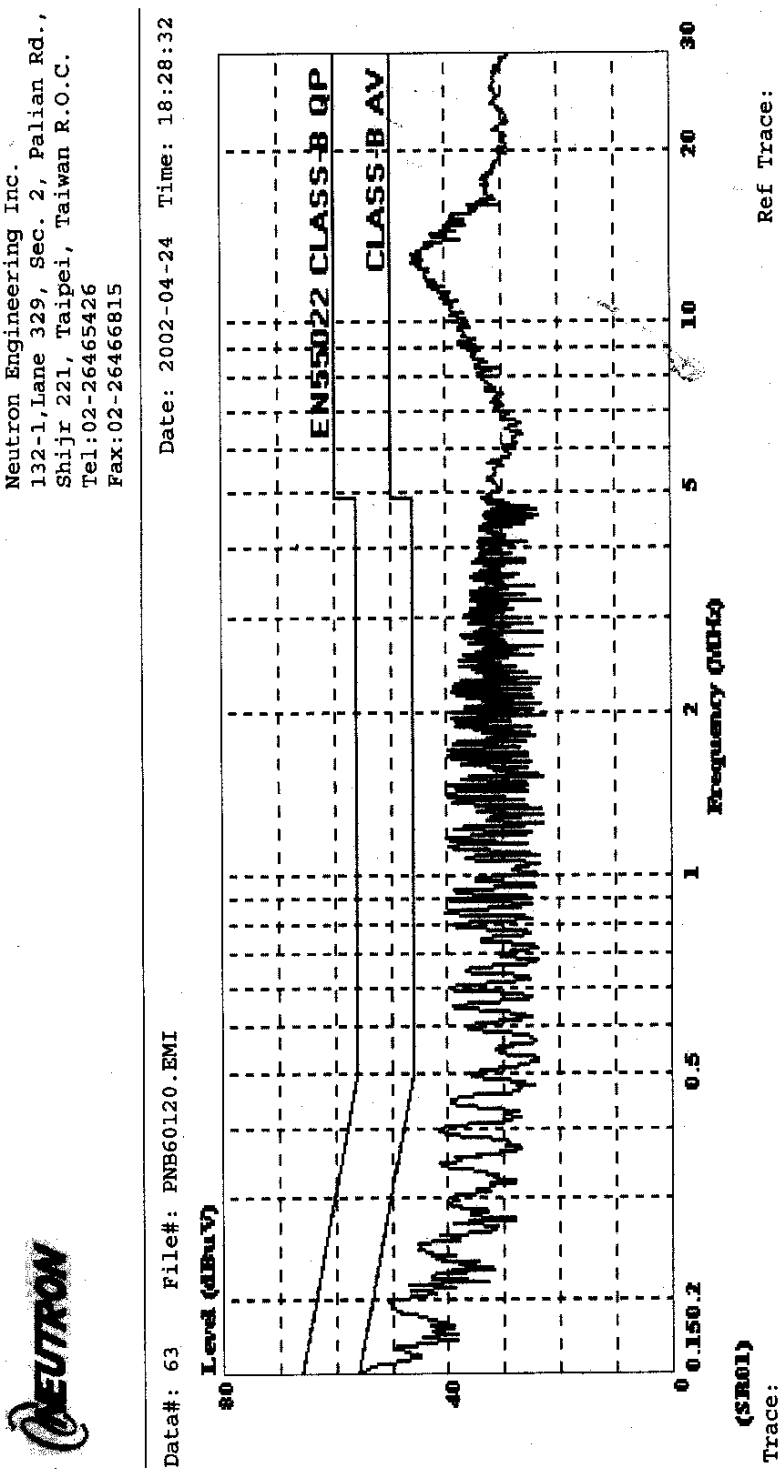
1. Radiation Vertical Testing



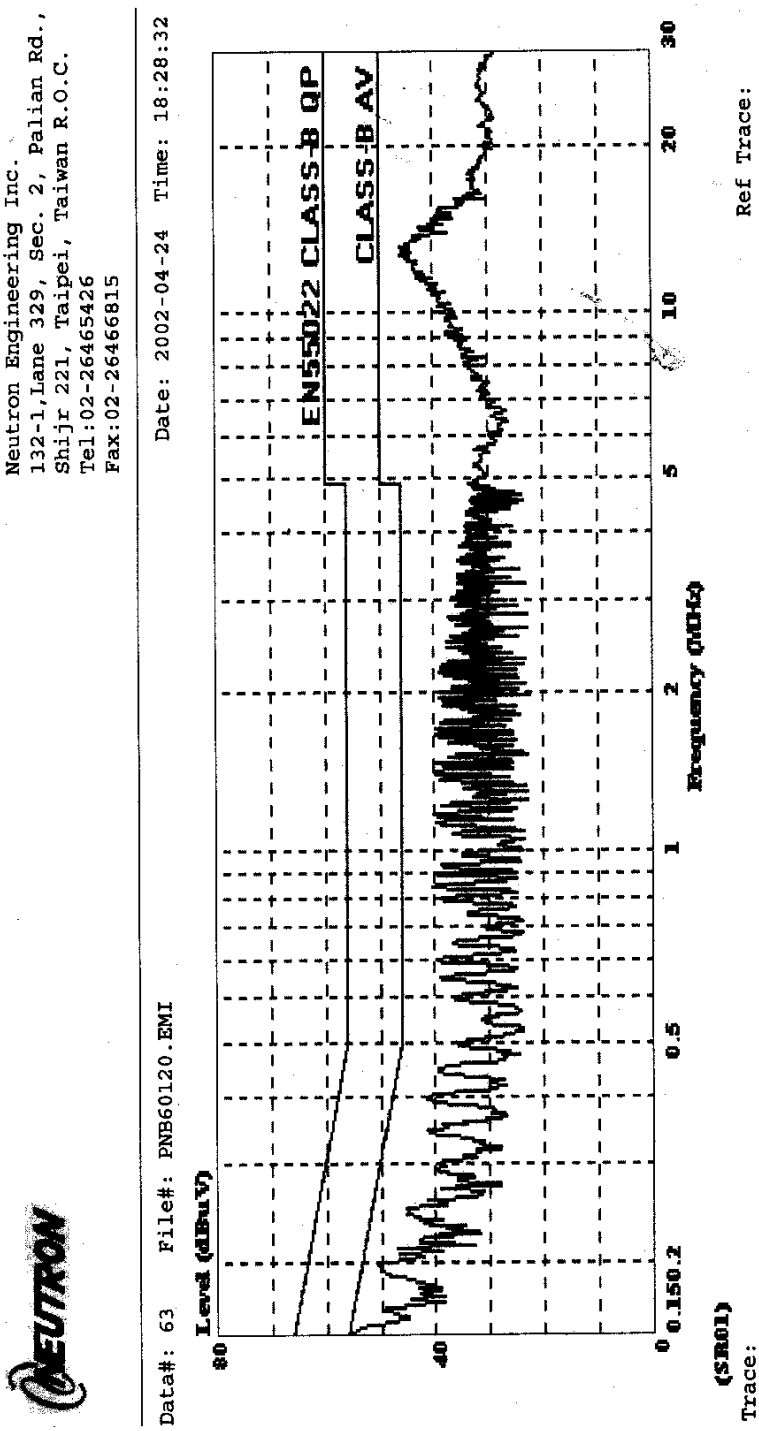
2. Radiation Horizontal Testing



3.Conduction Line Test



4. Conduction Neutral Test



EMS Test Report

1.EFT/Burst Testing

NEUTRON EMC LAB.

EFT/Burst Testing
(EN 55024 for I.T.E.)

Applicant : [illegible] Product Name : ADAPTER
Project No. : Model/Type No. : PNB60FORSG6840
Temperature : 25 °C Relative Humidity : [illegible] % Test Date : 91.3.19
Standard No. Apply : EN 61000-4-4 AC Mains Supply : 230V, 50Hz, 1Ø
Voltage (Peak) : 1 KV Repeat. Rate : (X)5 KHz Impulse Rise Time (Tr) : 5 ns± 30%
Impulse Duration (Th) : 50 ns± 30% Burst Duration : 15 ms± 20%
Burst Period : 300 ms± 20% Duration of Test : 1 min , 15 sec. rest between each testing
Special Notes : (EUT Operation Mode or Test Configuration Mode, if applicable)

Mode / Port(s)	(X) AC Power Line	
	Polarity P	Polarity N
Line (L)	A	A
Neutral (N)	A	A
Ground (PE)	A	A
Criteria	B	B

Mode / Port(s)	( ) DC POWER PORT	
	Polarity P	Polarity N
POSITIVE		
NEGATIVE		
Criteria	B	B

Mode / Port(s)	( ) SIGNAL/ CONTRAL PORT	
	Polarity P	Polarity N
Criteria	B	B

Note:
1) P/N denotes the Positive/Negative polarity of the output voltage.
2) A/B/C denotes the performance criterion class:
A - No degradation of performance or loss of function.
B - Temporary degradation of performance or loss of function, but no change of actual operating state or stored data.
C - Temporary loss of function is allowed, provided the function is self-recoverable or can be restored by operation of the controls, or by any operation specified in the instructions for use.

Test Engr.: [illegible]

FORM-00

2.Injection Current Testing

NEUTRON EMC LAB.

Injection Current Testing
(EN 55024 for I.T.E.)

Applicant : [redacted]　　Produce Name : ADAPTER
Project No. :　　Model/Type No. : PNB[redacted]
Temperature : 25 ℃　Relative Humidity : 55 %　Test Date : 91. 3. 19
Mains Supply : (V) AC 230V, 50Hz , 1Ø　( ) + DC 5 V
Standard No. Apply : (X) EN 61000-4-6
Frequency Range : (X) 150 KHz - 80 MHz
Test Level : ( ) 1 V(rms)　(X) 3V(rms)　( ) 10 V(rms)
Modulation : (X) Modulated　Signal Type : 1KHz , 80% , AM
Test Mode : ( ) AC Power Port　( ) DC Power Port　( ) Signal/CTL Line
Special Notes : (EUT Operation Mode or Test Configuration Mode, if applicable)

Test Ports (Mode)	Freq. Range (MHz)	Field Strength	Perform. Criteria	Results	[redacted]	Remark
Input/ Output AC. Power port	0.15 —80	3V(rms)	A	A	[redacted]	
Input/ Output DC. Power Port	0.15 —80	3V(rms)	A		[redacted]	N/A
Signal / CTL Data Line	0.15 —80	3V(rms)	A		[redacted]	N/A

Remark: * N/A - denotes test is not applicable in this Test Report

Note:
1) P/N denotes the Positive/Negative polarity of the output voltage.
2) A/B/C denotes the performance criterion class:
A - No degradation of performance or loss of function.
B - Temporary degradation of performance or loss of function, but no change of actual operating state or stored data.
C - Temporary loss of function is allowed, provided the function is self-recoverable or can be restored by operation of the controls, or by any operation specified in the instructions for use.

Test Engr.: [redacted]

FORM-00

3.Voltage Dips/Interruption Testing

NEUTRON EMC LAB.

Voltage Dips/Interruption Testing
(EN 55024 for I.T.E.)

Applicant : [illegible] Produce Name : ADAPTER
Project No. : Model/Type No. : PNB120TOR SG6840
Temperature : 25 ℃ Relative Humidity : 61 % Test Date : 91.3.19
Mains Supply : AC 230V , 50Hz , 1Ø
Standard No. Apply : (X) EN 61000-4-11

Special Notes : (EUT Operation Mode or Test Configuration Mode , if applicable)

Test Mode	Test Level in &U_t	Duration (period)	Perform Criteria	Results	Judgement
Voltage dip >95%	< 5%	0.5	B	A	PASS
Voltage dip 30%	70%	25	C	A	PASS
Interruption >95%	< 5%	250	C	C_1	PASS

Remark : * N/A - denotes test is not applicable in this Test Report

Note :

1) P/N denotes the Positive/Negative polarity of the output voltage.
2) A/B/C denotes the performance criterion class :
   A – No degradation of performance or loss of function.
   B – Temporary degradation of performance or loss of function , but no change of actual operating state or stored data.
   C – Temporary loss of function is allowed , provided the function is self-recoverable or can be restored by operation of the controls , or by any operation specified in the instructions for use.
3) Changes to occur at 0 degree crossover point of the voltage waveform..

Test Engr. : [illegible]

FORM-0083 REV.1

4.Surge 2KV Testing

NEUTRON EMC LAB.

Surge Testing
(EN 55024 for I.T.E.)

Applicant : 采真 Produce Name : ADAPTER
Project No. : Model/Type No. : PNB1xxFORSG6840
Temperature : 24 ℃ Relative Humidity : 55 % Test Date : 91.3.19
Mains Supply : AC 230V , 50Hz , 1Ø
Standard No. Apply : (X) EN 61000-4-5
Surge Waveform Generated : Combination Wave , 1.2/50us-80/20us
Polarity and Numbers of Impulses : 5 Pst/Ngt at each tested mode
Impulse Repetition Rate : 5 sec.
Special Notes : (EUT Operation Mode or Test Configuration Mode , if applicable)

Wave Form / EUT Ports Tested	1.2/50(8/20)Tl/Th us Polarity	Phase	Voltage	Results	Perform. Criteria	Judgement
L - N	+/-	0°	1kV	A	B	PASS
	+/-	90°	1kV	A	B	PASS
	+/-	180°	1kV	A	B	PASS
	+/-	270°	1kV	A	B	PASS
L - PE	+/-	0°	2kV	A	B	PASS
	+/-	90°	2kV	A	B	PASS
	+/-	180°	2kV	A	B	PASS
	+/-	270°	2kV	A	B	PASS
N - PE	+/-	0°	2kV	A	B	PASS
	+/-	90°	2kV	A	B	PASS
	+/-	180°	2kV	A	B	PASS
	+/-	270°	2kV	A	B	PASS

Remark : * N/A - denotes test is not applicable in this Test Report

Note :

1) P/N denotes the Positive/Negative polarity of the output voltage.
2) A/B/C denotes the performance criterion class :
   A - No degradation of performance or loss of function.
   B - Temporary degradation of performance or loss of function , but no change of actual oper-ating state or stored data.
   C - Temporary loss of function is allowed , provided the function is self-recoverable or can be restored by operation of the controls , or by any operation specified in the instructions for use.

Test Engr. : 江家豪

FORM-00

5.Surge 4KV Testing

NEUTRON EMC LAB.

Surge Testing
(EN 55024 for I.T.E.)

Applicant : 崇 賢　　Produce Name : ADAPTER
Project No. :　　Model/Type No. : PNB60121TSG6840
Temperature : 24 ℃　Relative Humidity : 61 %　Test Date :
Mains Supply : AC 230V , 50Hz , 1Ø
Standard No. Apply : (X) EN 61000-4-5
Surge Waveform Generated : Combination Wave , 1.2/50us-80/20us
Polarity and Numbers of Impulses : 5 Pst/Ngt at each tested mode
Impulse Repetition Rate : 5 sec.
Special Notes : (EUT Operation Mode or Test Configuration Mode , if applicable)

Wave Form / EUT Ports Tested	1.2/50(8/20)Ti/Th us Polarity	Phase	Voltage	Results	Perform. Criteria	Judgement
L - N	+/-	0°	1kV		B	
	+/-	90°	1kV		B	
	+/-	180°	1kV		B	
	+/-	270°	1kV		B	
L - PE	+/-	0°	[illegible]	A	B	
	+/-	90°	[illegible]	A	B	
	+/-	180°	[illegible]	A	B	
	+/-	270°	[illegible]	A	B	
N - PE	+/-	0°	[illegible]	A	B	
	+/-	90°	[illegible]	A	B	
	+/-	180°	[illegible]	A	B	
	+/-	270°	[illegible]	A	B	

Remark : * N/A - denotes test is not applicable in this Test Report

Note :

1) P/N denotes the Positive/Negative polarity of the output voltage.
2) A/B/C denotes the performance criterion class :
   A - No degradation of performance or loss of function.
   B - Temporary degradation of performance or loss of function , but no change of actual oper-ating state or stored data.
   C - Temporary loss of function is allowed , provided the function is self-recoverable or can be restored by operation of the controls , or by any operation specified in the instructions for use.

Test Engr. : [illegible]

FORM-00

PNB60121T-SG6840

SG6840 Data Sheet

DESCRIPTION

This high-integrated PWM controller provides several special enhancements to satisfy the needs for low power standby and protection features. In standby mode, PWM frequency reduction is used to lower the power consumption and support a stable output voltage. Due to Bi-CMOS process, the SG6840 reduces start-up and operation current to achieve a higher efficiency power conversion. Start-up current has been reduced to 30uA typical and operating current has been shrunk to 3mA. The SG6840 is a fixed frequency PWM controller in normal operation; its patented green-mode function will decrease the PWM frequency in response to the decrease of the load. This green function dramatically reduces the power loss in no load and light load conditions that assist the power supply to meet the power conservation requirement. The proprietary synchronized slope compensation ensures the stability of the current loop for continuous-mode operation. Built-in line-voltage compensation maintains an identical output power for a wide input range. An NTC thermistor is applied to sense the temperature for over-temperature protection. The SG6840 is available in 8-pin DIP and SO packages.

APPLICATIONS

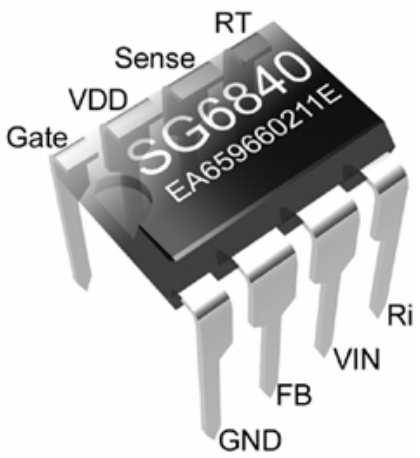
General-purpose switching mode power supplies and flyback power converters, and

- Power Adapter
- Open-frame SMPS
- Battery Charger Adapter

FEATURES OVERVIEW

- Green-mode PWM to support “Blue Angle”Norm
- Low start up current 30uA
- Low operation current 3mA
- Leading-edge blanking
- Built-in synchronized slope compensation
- Totem pole output includes soft driving
- Constant output power
- Current mode operation
- Cycle-by-cycle current limiting
- Under voltage lockout (UVLO)
- Short circuit protection
- Programmable over-temperature protection
- Few external components & low cost solution

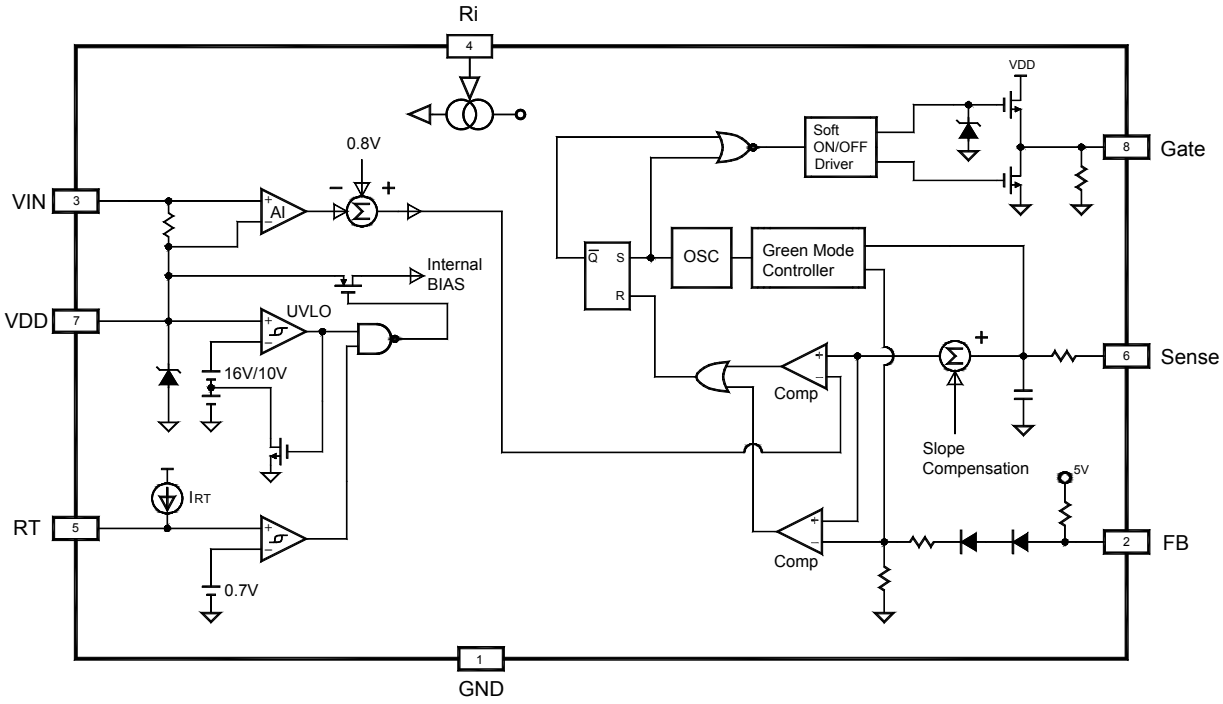
PIN CONFIGURATION



PIN DESCRIPTIONS

Name	Pin No.	Type	Function
VDD	7	Supply	Power supply.
VIN	3	Analog in-put	The start-up current input. A start-up resistor is connected from the line-input to this pin, such as 1.5MΩ for off-line converter. Adjust the start-up resistor to vary the line voltage compensation for constant
FB	2	Analog in-put	Feedback. The FB pin provides the information of the regulation, it effects to the internal PWM comparator to control the duty cycle.
Sense	6	Analog in-put	Current sense. It senses the voltage developed on a sensed resistor. When it reaches the internal threshold, the PWM output is disabled. Therefore, the over-current protection is realized. Besides, the current information is providing for the current mode control.
RT	5	Analog in-put/output	For over-temperature protection. A constant current is output. An NTC thermistor is connected from this pin to ground to sense the temperature. When the voltage in this pin is lower than the limit, which will enable the over-temperature protection.
Gate	8	Driver out-	The totem-pole output driver to drive the power MOSFET.
Ri	4	Program-	Reference setting. Connect a resistor to ground to generate a con-
GND	1	Supply	Ground.

BLOCK DIAGRAM



ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
VDD	DC Supply Voltage – note 1	20	V
Iout	Gate Output Current	500	mA
V_{FB}	Input Voltage to FB Pin	-0.3 to 7 V	V
V_{Sense}	Input Voltage to Sense Pin	-0.3 to 7V	V
Pd	Power Dissipation	1	W
T_J	Operating Junction Temperature	150	◦ C
T_A	Operating Ambient Temperature	-25 to 85	◦ C
Tstg	Storage Temperature Range	-55 to +150	◦ C

Note: All voltage values, except differential voltage, are with respect to network ground terminal.

ELECTRCIAL CHARACTERISTICS(VDD=15V, TA=25)

Feedback Input Section

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
Av	Input-voltage to current-sense attenuation		1/4.5	1/5	1/5.5	V/V
Zfb	Input impedance		3	4.5	6	KΩ
Ifb	Bias current				2	mA
Voz	Input voltage for zero duty cycle				1.2	V

Current Sense Section

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
Zcs	Input impedance		8	12	16	KΩ
T_{PD}	Delay to Output			150	200	nS
Vth	Threshold voltage for current limit		0.8	0.85	0.9	V
ΔVth @ Iin	The change of threshold voltage versus the input current of the Vin	Iin = 220 uA	-0.09	-0.15	-0.21	V

Oscillator Section

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
Fosc	Frequency	Ri=26KOhms	60	65	70	KHz
Fosc-green	Frequency in green mode	Ri=26KOhms		10	15	KHz
Vg	Green mode voltage (Vg = Vfb - Vd)			1.3		V
Vn	Normal mode voltage (Vn = Vfb - Vd) Vn = 4 V for maximum duty cycle		1.7	2	2.3	V
Sg	Slope for green mode modulation	Ri=26KOhms	50	80	120	Hz/ mV
Fdv	Frequency variation versus VDD deviation	VDD=10 to 20V			5	%
Fdt	Frequency variation versus Temp. deviation	TA=-25 to 85 ℃			5	%

PWM Section

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
$DC_{(MAX)}$	Maximum Duty Cycle		75	80	90	%
$DC_{(MIN)}$	Minimum Duty Cycle		-	-	0	%
Bnk	Leading edge blanking time		200	270	350	nsec

Output Section

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
Vol	Output Voltage Low	VDD= 12V, Io = 150mA			1.5	V
Voh	Output Voltage High	VDD= 12V, Io = 50mA	8V			V
tr	Rising Time	VDD=13V, CL=1nF	150	250	350	NS
tf	Falling Time	VDD=13V, CL=1nF	30	50	90	NS

Under-voltage Lockout Section

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
$V_{TH(ON)}$	Start Threshold Voltage		15	16	17	V
$V_{DD(min)}$	Min. Operating Voltage		9	10	11	V

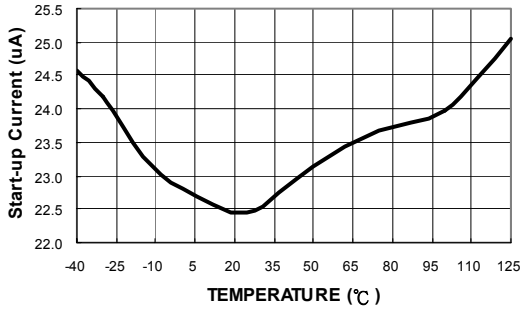
Over-temperature Protection Section

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
Irt	Output current of pin RT	Ri=26KOhms	92	100	108	uA
Vtov	Threshold voltage for over-temperature protection		0.665	0.7	0.735	V

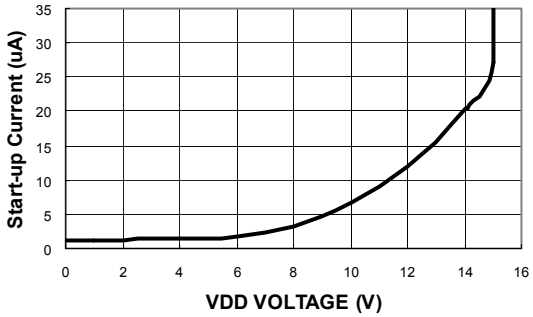
Total Standby Current Section

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
$I_{DD\ ST}$	Start-up Current			30	40	uA
$I_{DD\ OP}$	Operating Supply Current	-	-	3	5	mA

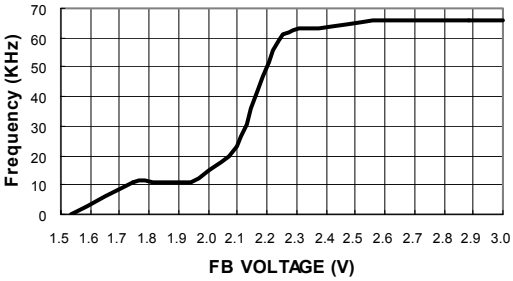
Start-up Current ($I_{DD\ ST}$) vs Temperature



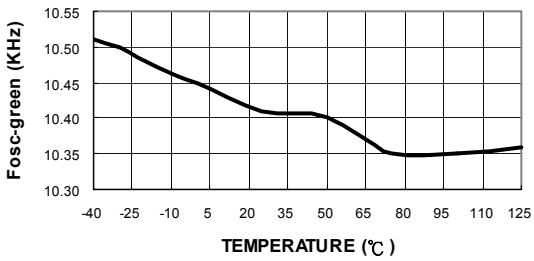
Start-up Current ($I_{DD\ ST}$) vs VDD Voltage



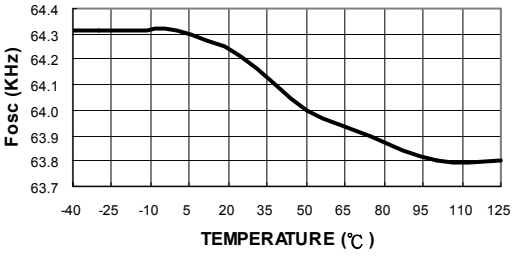
FB Voltage vs Frequency



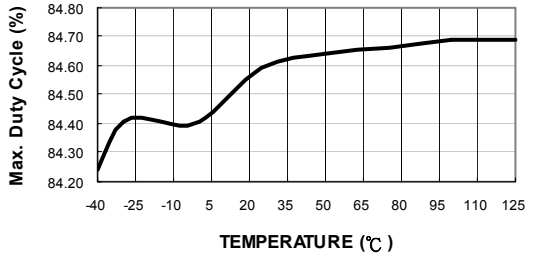
Frequency in green mode (Fosc-green) vs Temperature



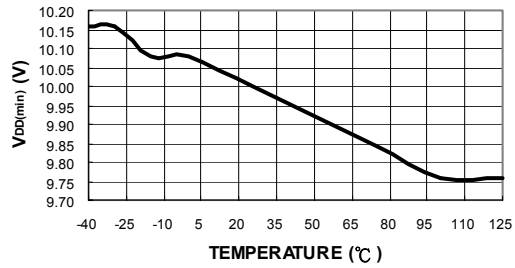
PWM Oscillator Frequency (Fosc) vs Temperature



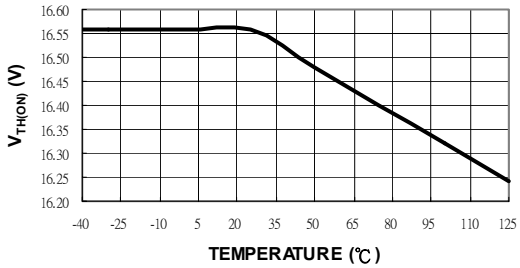
Maximum Duty Cycle DC($_{MAX.}$)vs Temperature



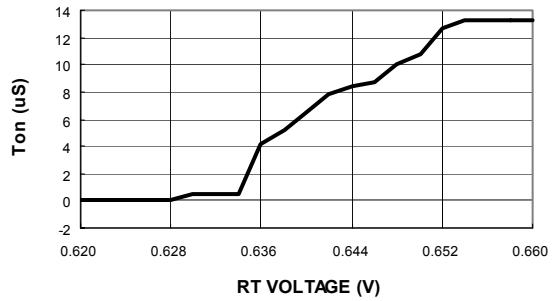
Min. Operating Voltage ($V_{DD(min)}$) vs Temperature



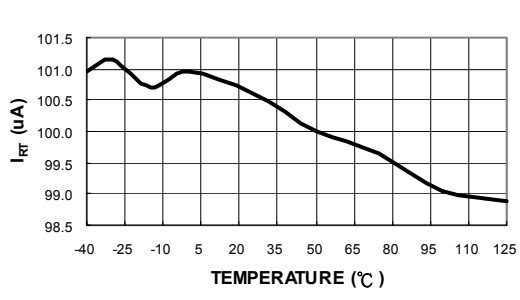
Start Threshold Voltage ($V_{TH(ON)}$) vs Temperature



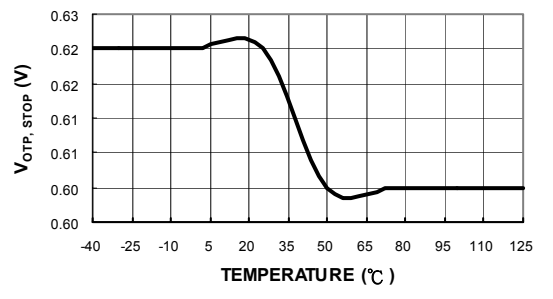
RT Voltage vs Ton



Output Current of pin RT (I_{RT}) vs Temperature



$V_{OTP,\ STOP}$ vs Temperature



OPERATION DESCRIPTION

Start-up Current

Typical start-up current is only 30uA This ultra low start-up current allows users to use a high resistance, and low-wattage, start-up resistor to supply the start-up power required by SG6840. Take a wide input-range (100V_{AC}-~240V_{AC}) of AC-to-DC power adapter as an example, an 1.5 MΩ, 0.25W, start-up resistor and a 10uF/25V VDD hold-up

Operating Current

Operating current has been reduced to 3mA. The low operating current enables a better efficiency and reduces the

Green Mode Operation

The patented green-mode function provides an off-time modulation to reduce the switching frequency in the light load and no load conditions. The feedback voltage, which is derived from the voltage feedback loop, is taken as the reference. Once the feedback voltage is lower than the threshold voltage, switching frequency will linearly decrease until the minimum green mode frequency around 10kHz (R_i =26kΩ). We can find that all of the losses are in proportional to the switching frequency, such as the switching loss of the transistor, the core loss of the transformer and inductors, and the power loss of the snubber, etc. The off-time modulation in the PWM controller can reduce the power consumption of the power supply in light load and no load conditions. In normal load and high load conditions, the PWM frequency is at its maximum frequency around 65kHz (R_i =26kΩ) and not affected by the off-time modulation.

Oscillator Operation

An external resistor R_i determines the PWM oscillation frequency. A 26kΩ resistor R_i creates a 50uA constant current I_i and generates 65kHz switching frequency.

I_i (mA) = 1.3V / R_i (kΩ);

$$f_{PWM} = \frac{1690}{R_I\ (k\Omega)}(kHz) \quad \text{——————} \quad (1)$$

Current sensing and PWM current limiting

SG6840 consists of two feedback loops: voltage loop and current loop, to control the load regulation. SG6840's current sense input is designed for the current-mode control. A current-to-voltage conversion is done externally through a current-sense resistor Rs. Under normal operation, the FB voltage V_{FB} controls the peak voltage across the sense resistor Rs, hence the PWM duty cycle, as follows:

Ipk = (V_{FB} – 1.4) / 5Rs;

where V_{FB} is the voltage on pin FB

When the DC output voltage of secondary side decreases due to heavy load conditions, the FB voltage V_{FB} will increase such that the PWM duty cycle increases to regulate the output voltage of secondary side back to its normal voltage. The inverting input to SG6840's current-sense comparator is internally clamped to a variable voltage around 0.85V (note: see Constant Output Power Limit section). The current limiting occurs if the voltage of SENSE pin reaches this 0.85V threshold value, such as Ipk (max) = 0.85V/Rs. The value of sense resistor Rs decides the maximum power limit. Larger Rs, whose Ipk is smaller, results in a smaller power limit

Leading Edge Blanking

Each time when the power MOSFET is switched on, a leading spike is generated due to parasitic capacitance. To avoid premature termination of the switching pulse, this leading edge spike is blanked out with a time constant 270 nsec. During this time period, the current-limit comparator is disabled and cannot switch off the gate drive regardless how big the SENSE voltage is.

Under-voltage lockout (UVLO)

The UVLO Under-Voltage Lockout (UVLO) function ensures the supply voltage V_{DD} for SG6840 is adequate to fully function before enabling the output stage. The turn-on and turn-off threshold voltages are fixed internally at 16V/10V. The hysteresis voltage between turn-on and turn-off prevents V_{DD} from being unstable during power on/off sequencing. Start-up current is typically 30uA for efficient bootstrapping from the rectified input for an off-line converter. During the normal operation, V_{DD} is developed from an auxiliary winding of the transformer. At the moment of start-up, V_{DD} hold-up capacitor C_{IN} must be charged up to 16V through the start-up resistor R_{IN} before enabling the output switch. With an ultra small start-up current of 30uA, R_{IN} can be as large as 1.5 MΩ and still be able to charge up the hold-up capacitor C_{IN} even when V_{AC} = 90Vrms. Power dissipation of this large resistance R_{IN} would then be less than 70mW (0.07W) even under high line (V_{AC} =

Gate Output / Soft Driving

The SG6840 BiCMOS output stage is a fast totem pole gate driver, which is designed to avoid cross conduction current. This minimizes heat dissipation, increases efficiency and enhances reliability. The output driver is clamped by an internal 18V Zener diode in order to improve the control of the power MOSFET transistors and protect them against undesirable gate over-voltage. By controlling the rising time of the switch-on waveform and falling shape of the switch-off waveform, the output stage is optimized to reduce switching noise, improve EMI, and to provide a stable MOSFET gate drive.

Built-in Slope Compensation

Current mode control regulates the peak transformer/inductor current via the current control loop. In a continuous mode operation, the current is the average current, and composed of both AC and DC components. Since the output is proportional to the average, not the peak current, this causes oscillation when input voltage is changed. Adding the slope compensation to the current loop (reduce the current loop gain) to correct the problem is a simple approach. The SG6840 inserts a synchronized 0.33V positive-going ramp at every switching cycle to stabilize the current loop. Vs-comp = 0.33V.

Constant Output Power Limit

Every time when the SENSE voltage, across the sense resistor Rs, is larger than the threshold voltage around 0.85V, the output GATE drive is turned off after a small propagation delay t_D. Since the propagation delay is constant regardless the input line voltage V_{IN}, the output power would not be equal for the wide input voltage V_{IN} of 90Vrms to 265Vrms. To compensate the different output power limit between high line voltage and low line voltage, the internal threshold voltage is adjusted dependent on the input line voltage V_{IN} through the VIN pin. The threshold voltage is decreased from 0.85V to a smaller voltage when input line voltage V_{IN} increases. Smaller threshold voltage, at higher input line voltage, forces the output GATE drive to terminate earlier, thus reduce the total PWM turn-on time and make the output power equal to that of low input line voltage.

Thermal Protection

A constant current I_{RT} is output from pin RT. The resistor in pin Ri decides the current $I_{RT.}$

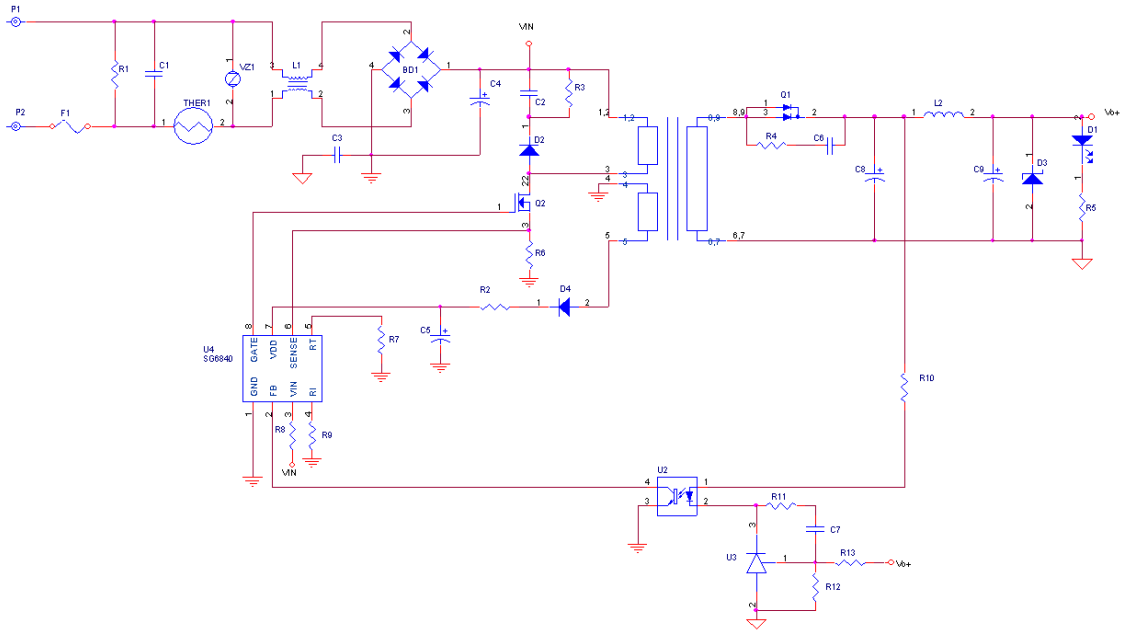
$I_{RT} = 2 \times (1.3V / Ri);$

An NTC thermistor Rntc in series with a resistor Ra can be connected from pin RT to ground. The over-temperature

Noise immunity

Noise on the current sense or control signal can cause significant pulse width jitter, particularly with the continuous-mode operation. While slope compensation helps alleviate this problem. Note that the SG6840 has a single ground pin. High sink current in the output therefore cannot be returned separately. Good high frequency or RF layout practices should be followed. Avoid long PCB traces and component leads. Locate components such as Ri, Rt and VDD capacitor near to the SG6840. The noise, which often causes the problem, is caused by the output (pin 8) being pulled below ground at turn-off by external parasitic. This is particularly true when driving MOSFET. A resistor (10 ~ 20 ohms) series connected from the output (pin 8) to the gate of MOSFET will prevent such output noise.

APPLICATION CIRCUIT

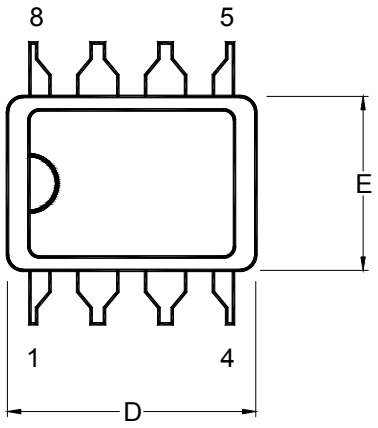


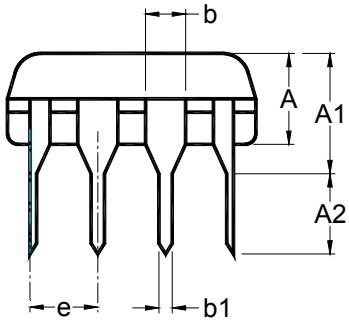
BOM

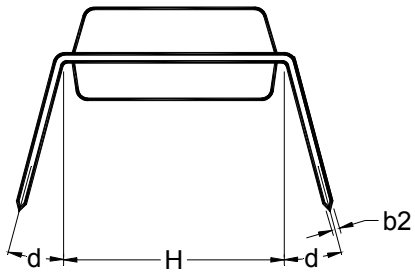
Reference	Component	Reference	Component
BD1	BD 1A/600V	Q2	MOS 2A/600V
C1	XC 0.22u	R1,R2	R 470Kohm 1/4W
C2	EC 0.1u 250V	R3	R 47ohm 1/4W
C3,C6,C7	YC 222p	R4	R 22ohm 1/4W
C4	EC 68u/400V	R5	R 4.7Kohm 1/4W
C5	CC 102p/1KV	R6	R 0.5ohm 1W
C8	EC 1200u/16V	R8,R12	R 510Kohm 1/4W
C9	EC 680u/16V	R9	R 20Kohm 1/8W 1%
C10	EC 10u/25V	R10	R 100ohm 1/8W
D1	LED	THER1	Thermistor SCK054
D3	ZD 12V	T1	Transformer EI28
F1	FUSE 2A/250V	U1	IC SG6840
L1	UU10.5	U2	IC 4N35D
L2	L04	U3	IC TL431
Q1	DIODE	VZ1	VZ 9G

MECHANICAL DIMENSIONS

8 PINS – PLASTIC DIP (D)





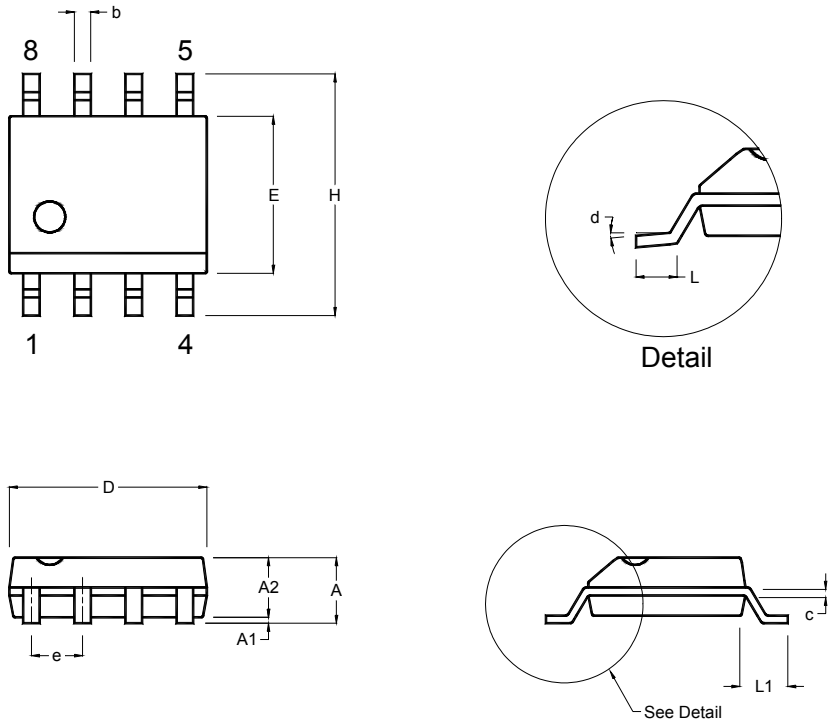


Symbol	Millimeter			Inch		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A		3.4			0.134	
A1			4.5			0.177
A2	3.0			0.118		
b		1.5			0.059	
b1	0.4	0.5	0.6	0.016	0.020	0.024
b2	0.25	0.3	0.4	0.010	0.012	0.016
d	0°		15°	0°		15°
D		9.3			0.366	
E		6.5			0.256	
e	2.29	2.54	2.79	0.090	0.100	0.110
H		7.6			0.299	

MECHANICAL DIMENSIONS

8 PINS – PLASTIC SMD (S)

Dimension:



Symbol	Millimeter			Inch		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	1.35	1.63	1.75	0.053	0.064	0.069
A1	0.10	0.15	0.25	0.004	0.006	0.010
A2	1.30	1.40	1.50	0.051	0.055	0.059
b	0.33	0.41	0.51	0.013	0.016	0.020
c	0.19	-	0.25	0.007	-	0.010
d	0°	-	8°	0°	-	8°
D	4.80	4.90	5.00	0.189	0.193	0.197
E	3.80	3.90	4.00	0.150	0.154	0.157
e	-	1.27	-	-	0.050	-
H	5.80	6.00	6.20	0.228	0.236	0.244
L	0.40	0.64	1.27	0.016	0.025	0.050
L1	-	1.07	-	-	0.042	-
Y	-	-	0.10	-	-	0.004

P C B L a y o u t N o t e

(A) In order to get better EMI performance and reduce line frequency ripples, the output of the bridge rectifier should be connected to capacitor C_2 first, and then to the switching circuits. Step 1.

(B) The high (switching) current loop is in C_2 – Transformer – MOSFET – R_S – C_2. The wire between R_S and C_2 should be as short as possible. Don't place the components in between the R_S and C_2. Step 2.

(C) Separate the ground loop of capacitor C_1; one path connects to the 6840 and another path to the bias winding. Step 3 and Step 3.

(D) Place C_1 close to the VDD and GND of SG6840 for a good decoupling. Step 3.

(E) The SG6840 control circuits (R_I, R_T) and the ground of opto-coupler should be connected and placed close to the GND of 6840. Step 4