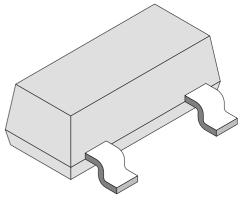


Dual Switching Diode

Features

- Fast switching speed
- High conductance
- Surface mount package ideally suited for automatic insertion
- Common anode



94 8550

Order Instruction

Type	Type Differentiation	Ordering Code	Remarks
BAW56	V _R = 70 V	BAW56-GS08	Tape and Reel

Absolute Maximum Ratings

 $T_i = 25^{\circ}C$

Parameter	Test Conditions	Туре	Symbol	Value	Unit
Non repetitive peak reverse voltage			V_{RM}	100	V
Repetitive peak reverse voltage= Working peak reverse voltage= DC Blocking voltage			$V_{RRM} = V_{RWM} = V_{R}$	70	V
Peak forward surge current	t _p =1s		I_{FSM}	1	Α
Feak lorward Surge Current	t _p =1μs		I_{FSM}	4.5	Α
Average forward current	half wave rectification with resistive load and f ≥ 50 MHz, on ceramic substrate 10 mmx8 mmx0.7 mm		I _{FAV}	150	mA
Forward current on ceramic substrate			I _F	250	mΑ
Power dissipation 8 mmx10 mmx0.7 mm			P _{tot}	300	mW
Junction and storage temperature range			T _j =T _{stg}	<i>–</i> 55+150	°C

Maximum Thermal Resistance

 $T_j = 25^{\circ}C$

Parameter	Test Conditions	Symbol	Value	Unit
Junction ambient	on ceramic substrate 8 mmx10 mmx0.7 mm	R _{thJA}	430	K/W

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BAW56

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Electrical Characteristics

 $T_j = 25^{\circ}C$

Parameter	Test Conditions	Type	Symbol	Min	Тур	Max	Unit
Forward voltage	I _F =1mA		V_{F}			715	mV
	I _F =10mA		V_{F}			855	mV
	I _F =50mA		V_{F}			1	V
	I _F =150mA		V_{F}			1.25	V
Reverse current	V _R =70 V		I _R			2.5	μΑ
	V _R =70 V, T _j =150 °C		I _R			50	μΑ
	V _R =25 V, T _i =150 °C		I _R			30	μΑ
Diode capacitance	V _R =0, f=1MHz		C _D			1.5	рF
Reverse recovery time	I_F =10mA to I_R =1mA, V_R =6V, R_L =100 Ω		t _{rr}			6	ns

Characteristics $(T_j = 25^{\circ}C \text{ unless otherwise specified})$

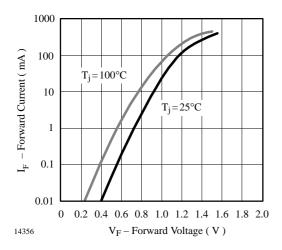


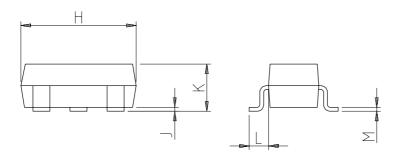
Figure 1. Forward Current vs. Forward Voltage

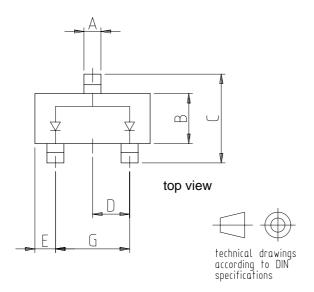




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Dimensions in mm





SOT-23				
Dim	Min	Max		
А	0.37	0.50		
В	1.19	1.40		
	2.10	2.50		
D	0.89	1.05		
Е	0.45	0.61		
G	1.78	2.05		
Н	2.79	3.05		
J	0.013	0.15		
K	0.89	1.10		
L	0.45	0.61		
М	0.076	0.130		
All Dimensions in mm				

14371

Case: SOT23, plastic Terminals: solderable per MIL-STD-202, method 208

Polarity: see diagram Marking: KJD, JD

Approx. weight: 0.008 grams

Vishay Semiconductors



Ozone Depleting Substances Policy Statement

It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice.

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Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay-Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay-Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

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