

# Flyback with Active Clamp: a suitable topology for Low Power and very wide Input Voltage Range applications

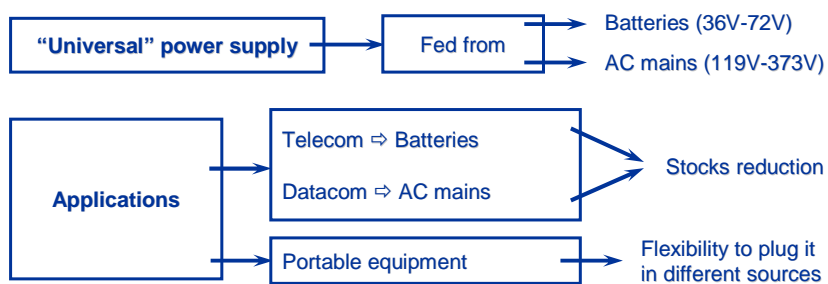
P. Alou, O. García, J.A.Cobos, J.Uceda and M. Rascón <sup>(1)</sup>

Universidad Politécnica de Madrid  
División de Ingeniería Electrónica (DIE)  
Madrid (SPAIN)

<sup>(1)</sup> ALCATEL  
Components Division  
Technology Group  
Madrid, Spain

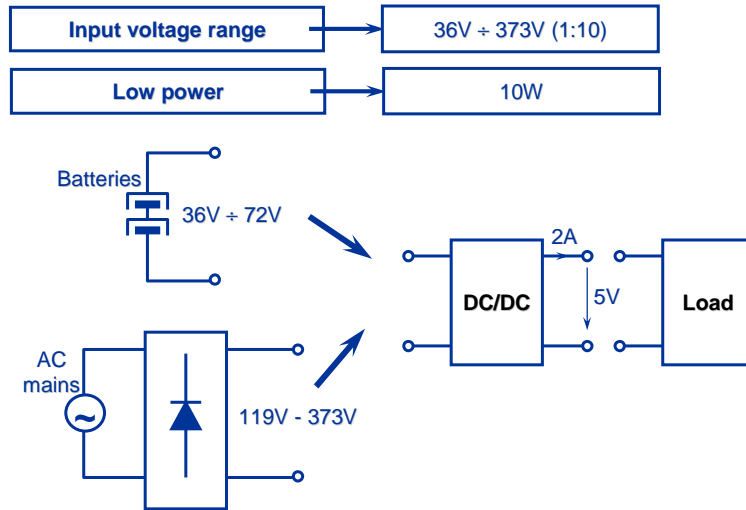
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## Motivation



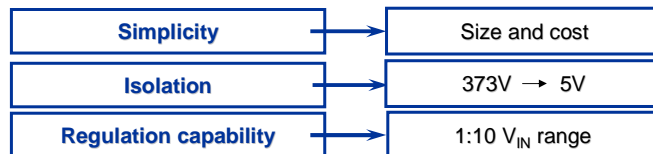
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## Specifications



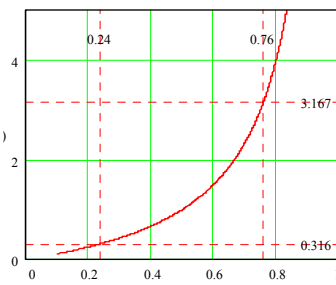
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## Critical issues to select the topology



### Flyback type topologies

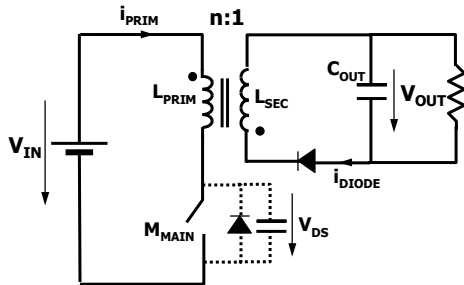
$$\text{gain} = \frac{d}{1-d}$$



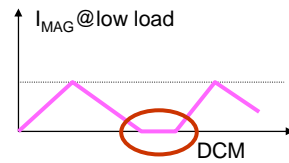
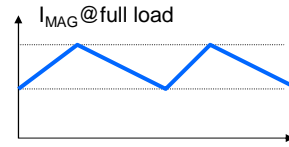
$$24\% < d < 76\%$$

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## Flyback topology: DCM



Discontinuous Conduction Mode



$$\frac{V_o}{V_{IN}} = d \sqrt{\frac{T}{2L_M R_L}}$$

Load dependent

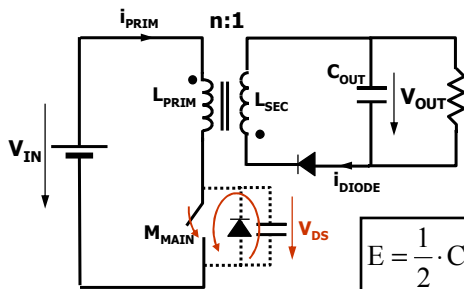
Minimum load  
Increase transformer size

to avoid DCM

Regulation capability is constrained

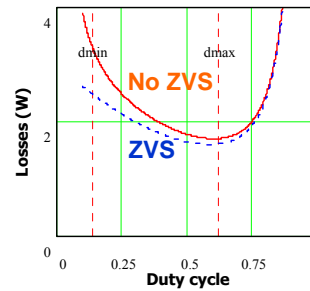
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## Flyback topology: Turn-on losses



$$E = \frac{1}{2} \cdot C_{OSS} \cdot V_{DS}^2$$

f<sub>sw</sub> = 150kHz



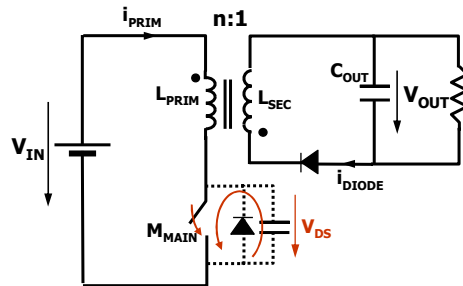
NO ZVS	3.3W @ V <sub>IN</sub> = 373V
ZVS	2.4W @ V <sub>IN</sub> = 373V

Turn-on losses are 30% of total losses at high V<sub>IN</sub>

ZVS should be regarded to select the topology

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## Flyback topology: summary

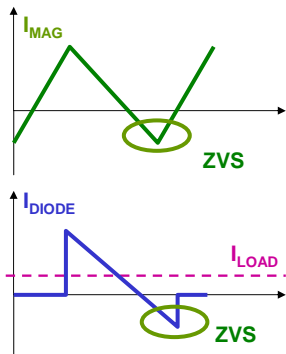
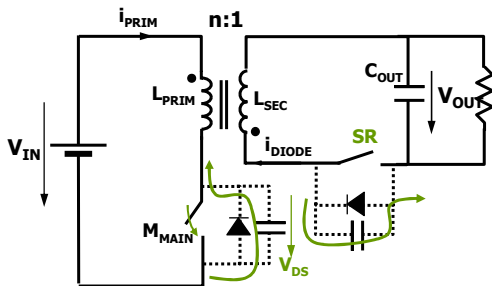


- ✓ Very simple topology
- ✗ It goes into DCM
- ✗ ZVS is NOT feasible

**NO SELECTED:  $V_{IN}$  range too wide**

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## Flyback with Synchronous Rectification



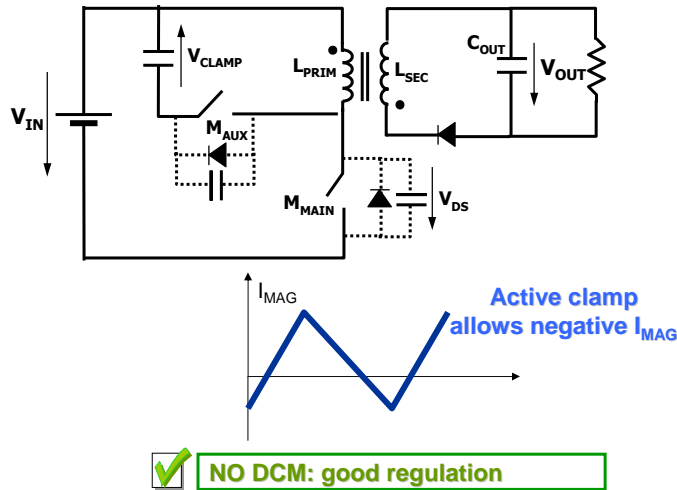
- ✓ NO DCM: good regulation capability
- ✓ ZVS achievable by negative  $I_{MAG}$
- ✓ Synchronous rectification
- ✗ SRs should be externally driven
- ✗ Higher RMS secondary current

High RMS current → Resistive losses  
 → Output filter

**More suitable at lower  $V_{OUT}$  (1.5V, 3.3V)  
 Trade-off: ZVS - Conduction losses**

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## Flyback with Active Clamp



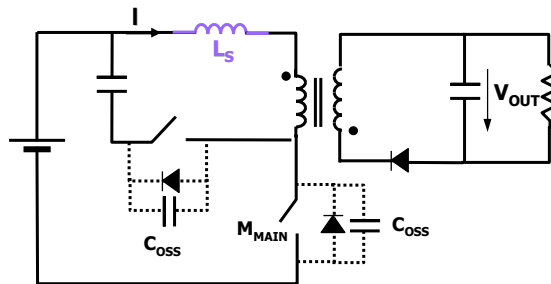
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## Flyback with AC: Series inductance for ZVS

ZVS with the leakage inductance + extra inductance



$$\frac{1}{2} L_s I^2 > \frac{1}{2} C_{OSS} V_{DS}^2$$



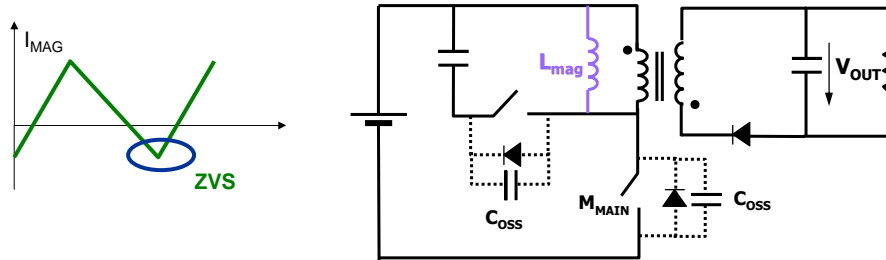
$L_{LK}$  is not enough, extra  $L_s$  should be added in a low power (10W) high  $V_{IN}$  (373V) application

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## Flyback with AC: ZVS with $L_{MAG}$

With the Magnetizing Inductance

[5]  
C.P. Hince, et al  
APEC'88

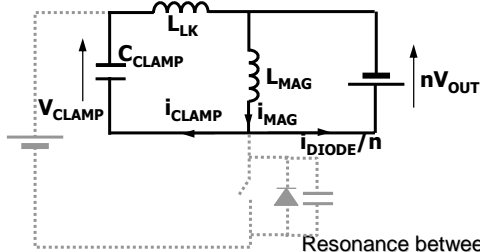


- ✓ No extra inductance for ZVS
- ✓ Smaller transformer size since  $\Delta I_{MAG}$  is higher
- Current ripple is not very high in a low power application

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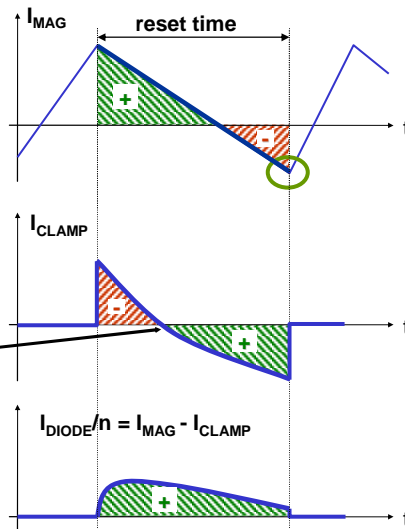
## Flyback with AC: Operation during reset time

Equivalent circuit during reset time



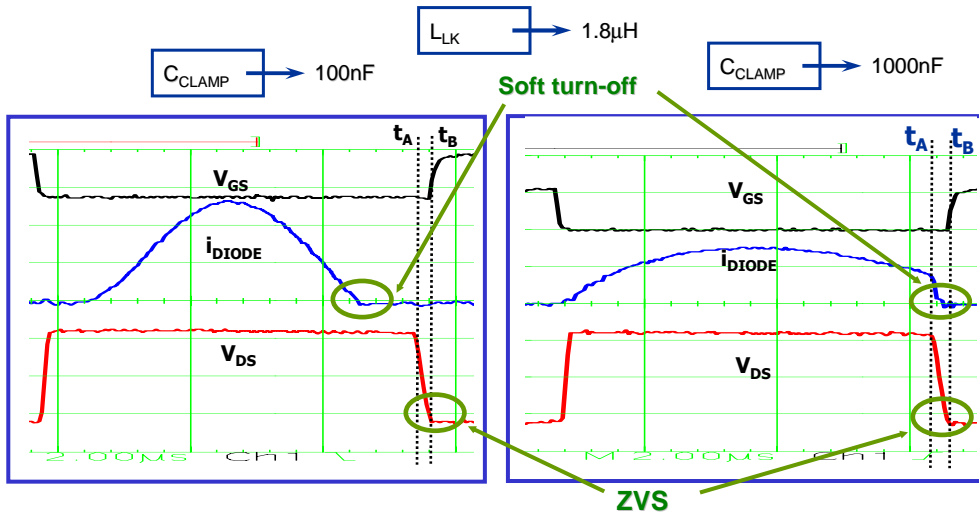
Resonance between  $C_{CLAMP}$  and  $L_{LK}$

- ✓ Flyback with SR Leakage energy is recycled
- ✓ Negative for ZVS
- ✓ Smaller RMS current for flyback SR



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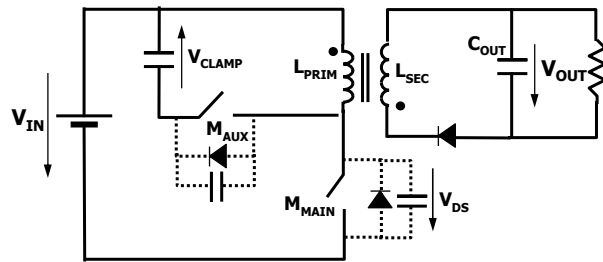
## Flyback with AC: Analysis of diode turn-off



Soft turn-off of diode

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## Flyback with Active Clamp, ZVS with $L_{MAG}$

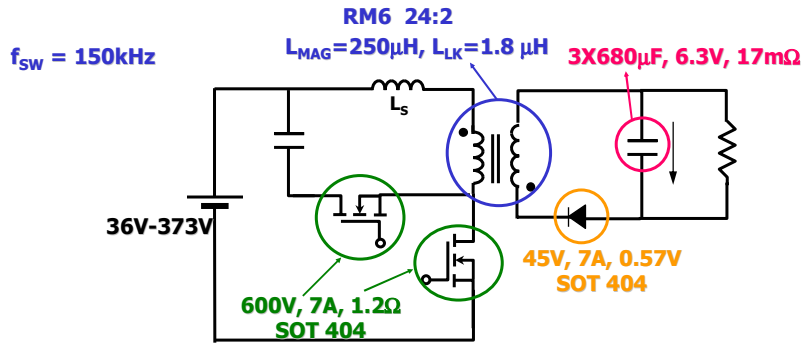


- ✓ NO DCM: good regulation
- ✓ ZVS achievable
- ✓ Soft diode turn-off
- ✓ Leakage energy is recycled
- ✓ Depending on  $L_{LK} - C_{clamp}$ , smaller RMS secondary current
- ⊗ Two MOSFETs to be driven: Cost

Selected topology for this 5V, 10W application

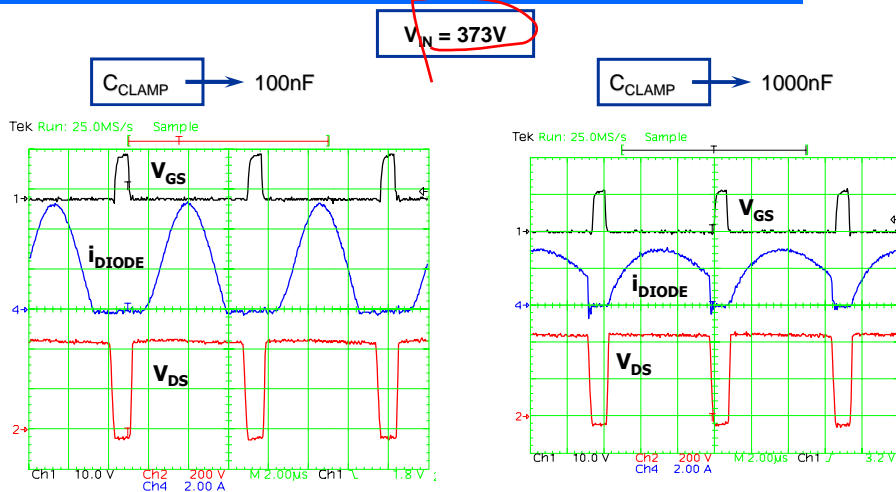
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# Power supply design



Design of  $f_{SW}$  and  $L_{MAG}$ : Trade-off between ZVS and circulating energy ( $\Delta I_{MAG}$ )

# Measurements



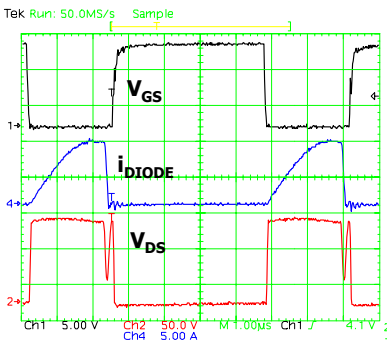
$C_{CLAMP}$  value affects  $i_{DIODE}$  waveform at high  $V_{IN}$



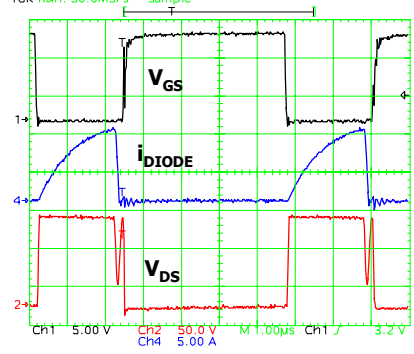
# Measurements

$V_{IN} = 36V$

$C_{CLAMP} \rightarrow 100nF$

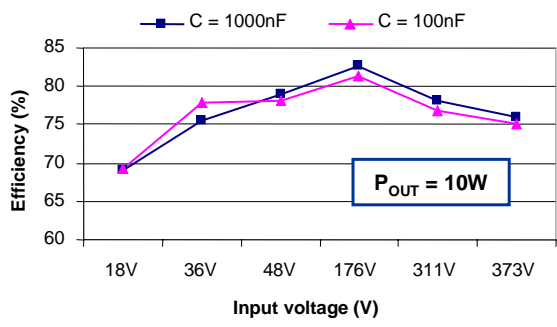


$C_{CLAMP} \rightarrow 1000nF$



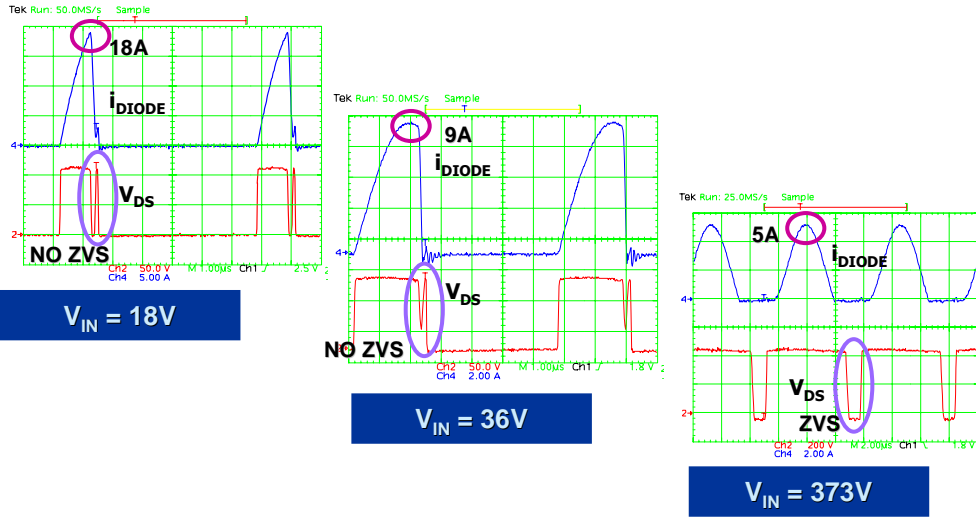
$C_{CLAMP}$  does not affect  $i_{DIODE}$  waveform at low  $V_{IN}$

# Measurements



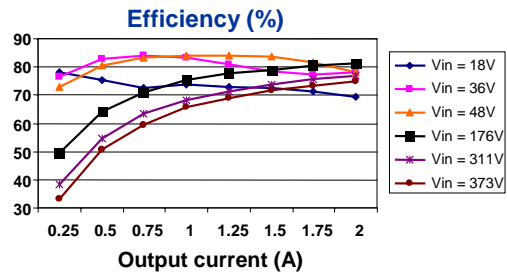
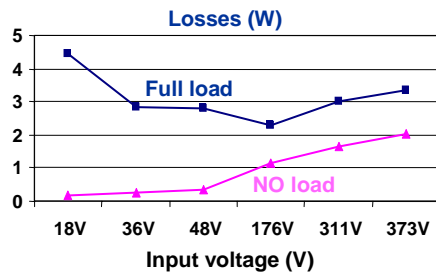
- $C_{CLAMP} = 1000nF$  provides higher efficiency at high  $V_{IN}$  since RMS current is smaller
- The converter operates even with  $V_{IN} = 18V$  (1:20 range)
- For the sake of size:  $C_{CLAMP} = 100nF$

## Waveform measurements for $C_{CLAMP}=100nF$



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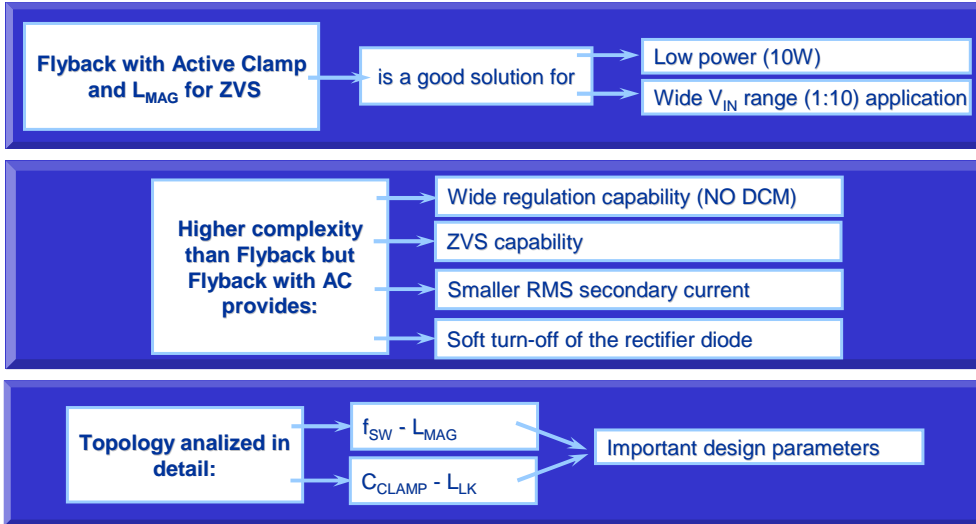
## Losses with $C_{CLAMP} = 100nF$



- Losses at  $V_{IN} = 311V$  are 3W with ZVS. If partial ZVS (125V hard switching), measured losses are 5W at  $V_{IN} = 311V$

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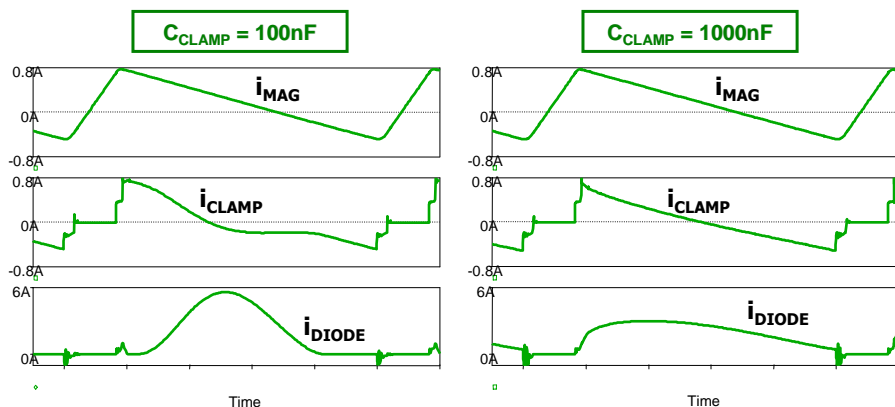
## Conclusions



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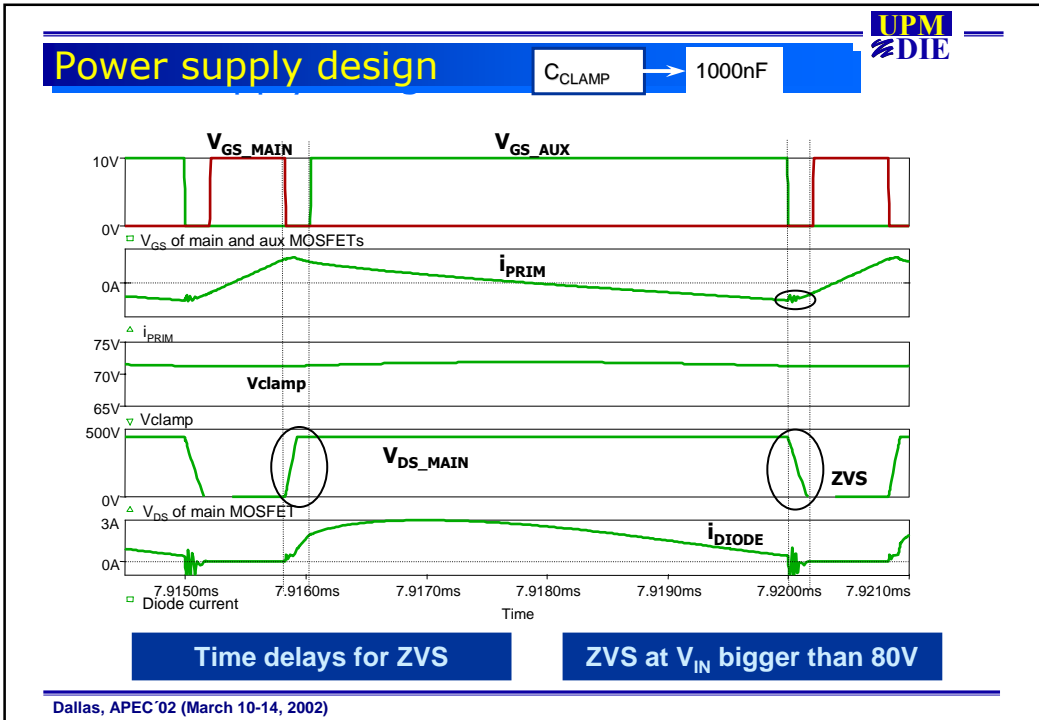
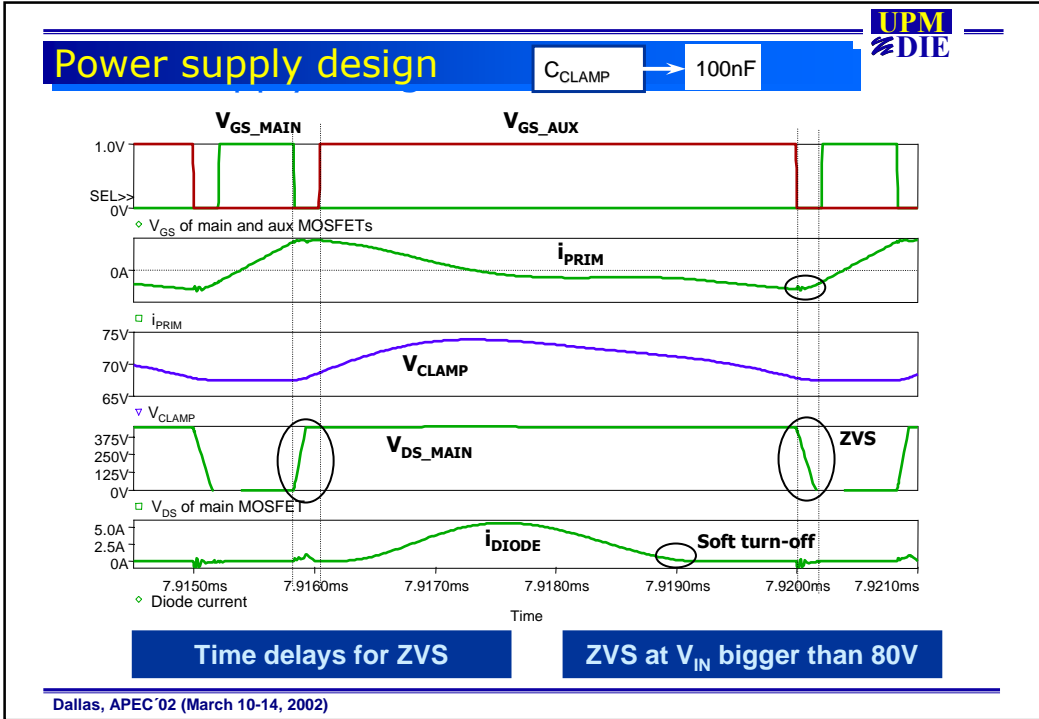
## Flyback with AC: Operation during reset time

Turns ratio → 12:1       $L_{MAG}$  → 250 $\mu$ H       $L_{LK}$  → 1.8 $\mu$ H



$C_{CLAMP}$  and  $L_{LK}$  become important design parameters

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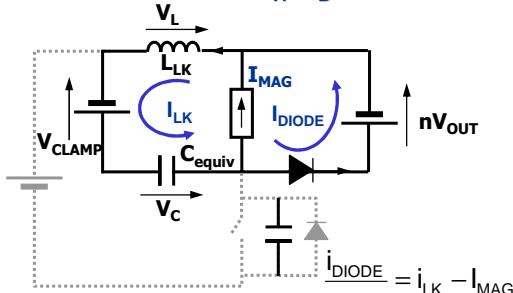


## Flyback with AC: Analysis of diode turn-off

$C_{CLAMP} \rightarrow 1000nF$

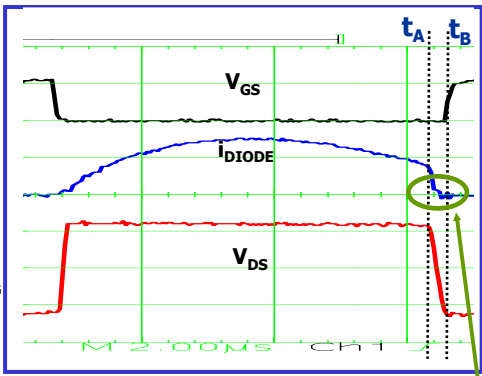
$L_{LK} \rightarrow 1.8\mu H$

Time interval:  $t_A - t_B$



$$i_{DIODE} = i_{LK} - I_{MAG}$$

$$\frac{d\left(\frac{i_{DIODE}}{n}\right)}{dt} = \frac{di_{LK}}{dt}$$



Soft turn-off

Soft turn-off of diode