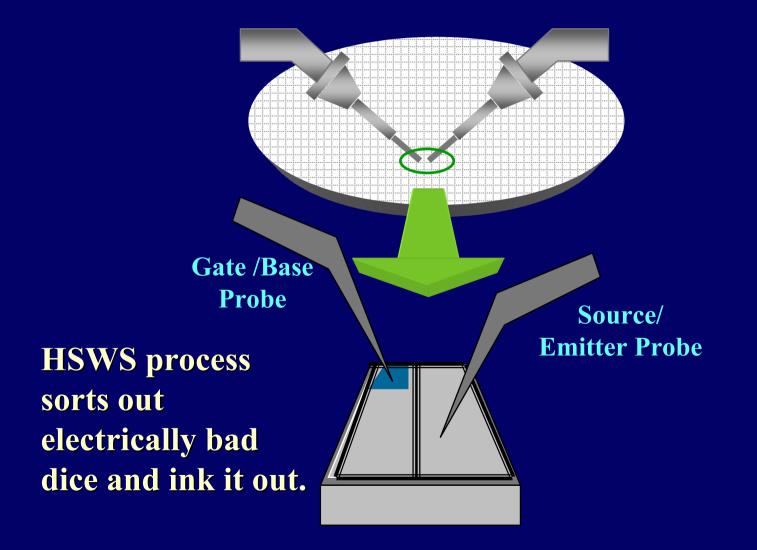
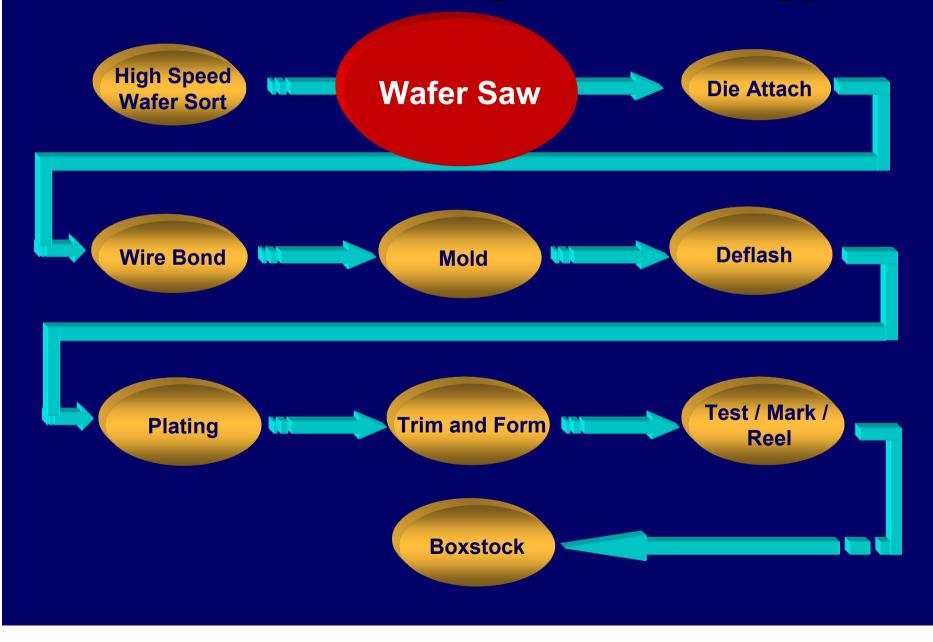


# **High Speed Wafer Sort**

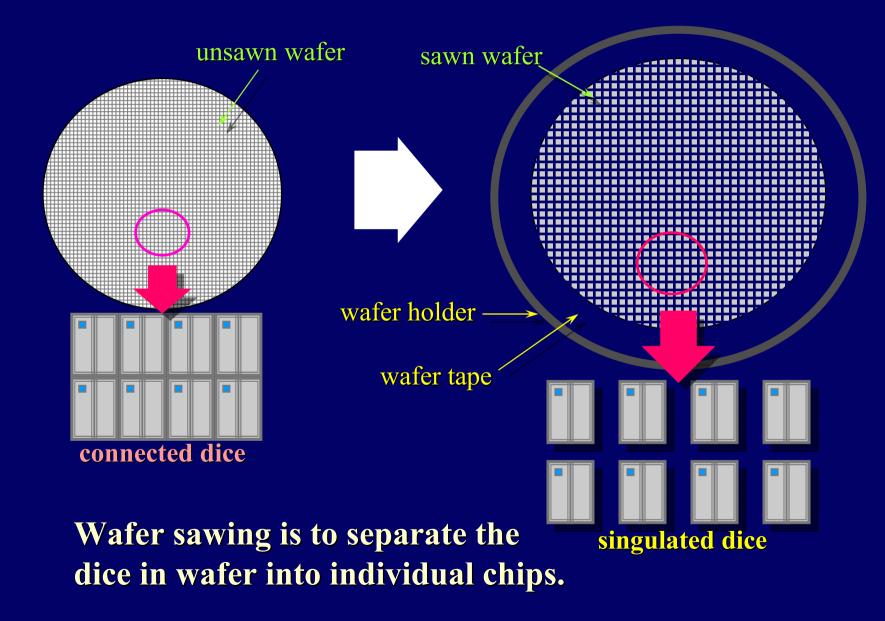
Semiconductor Manufacturing Process

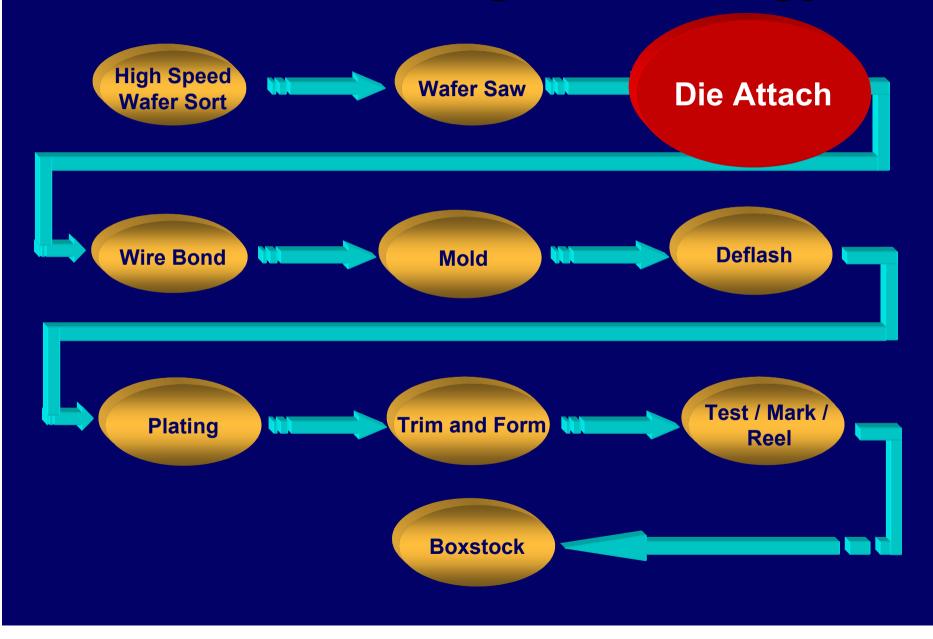




## **DICING / WAFER SAWING**

**Semiconductor Manufacturing Process** 



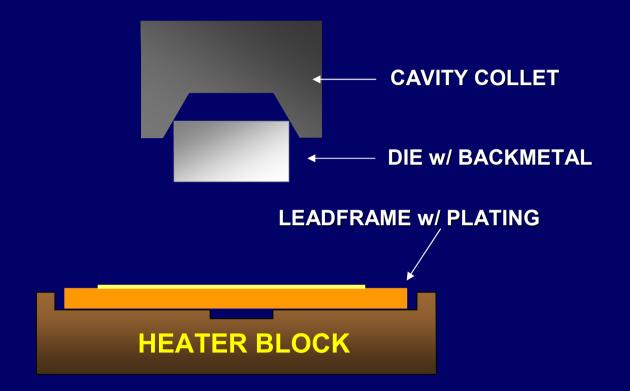




**Semiconductor Manufacturing Process** 

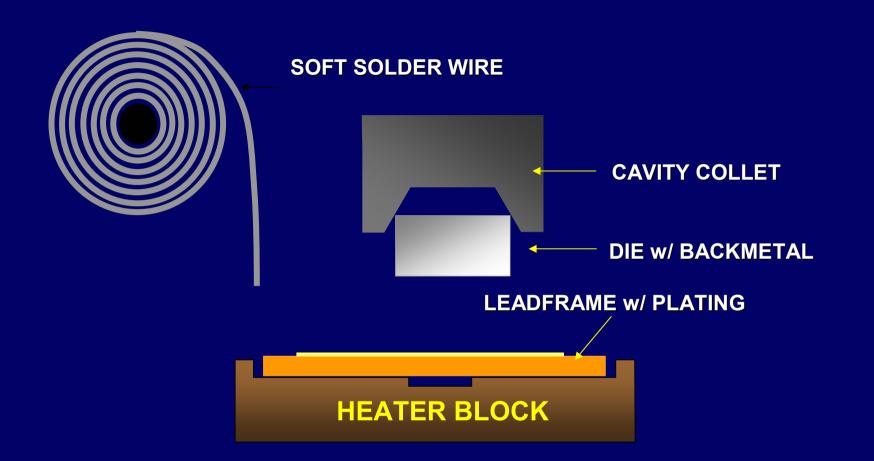
Cu Leadframe Die Attach Material Die or Microchip

Die Attach is a process of bonding the microchip on the leadframe.

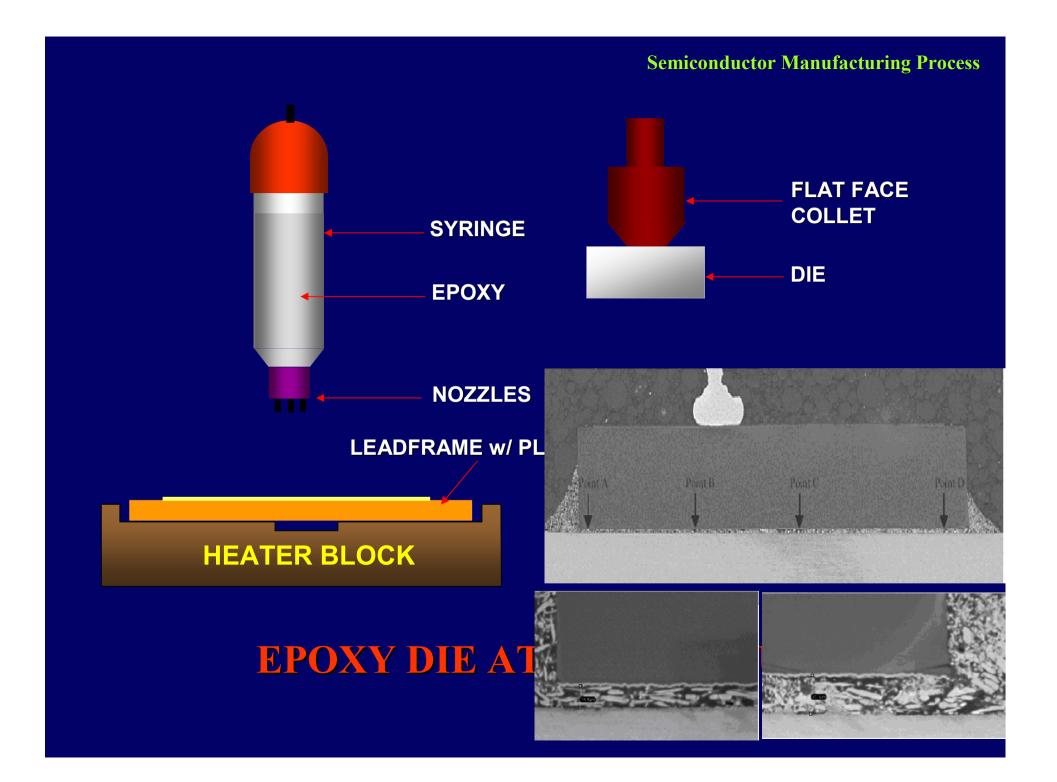


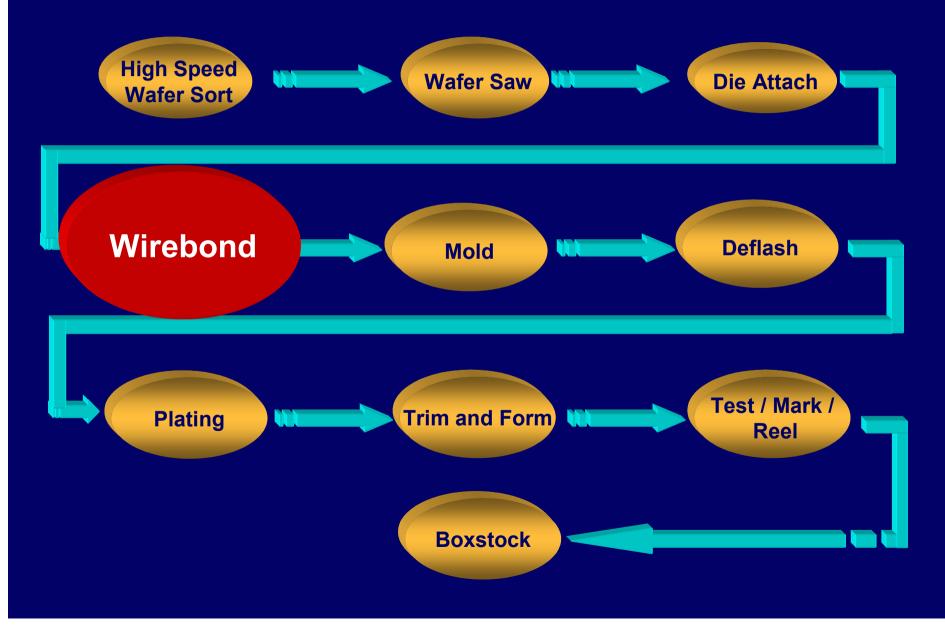
### **EUTECTIC DIE ATTACH SET-UP**

**Semiconductor Manufacturing Process** 



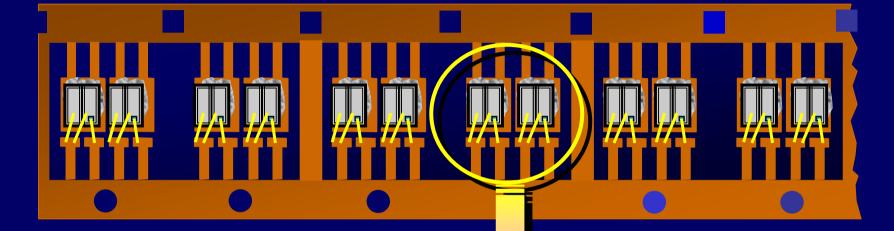
### SOFT SOLDER DIE ATTACH SET-UP





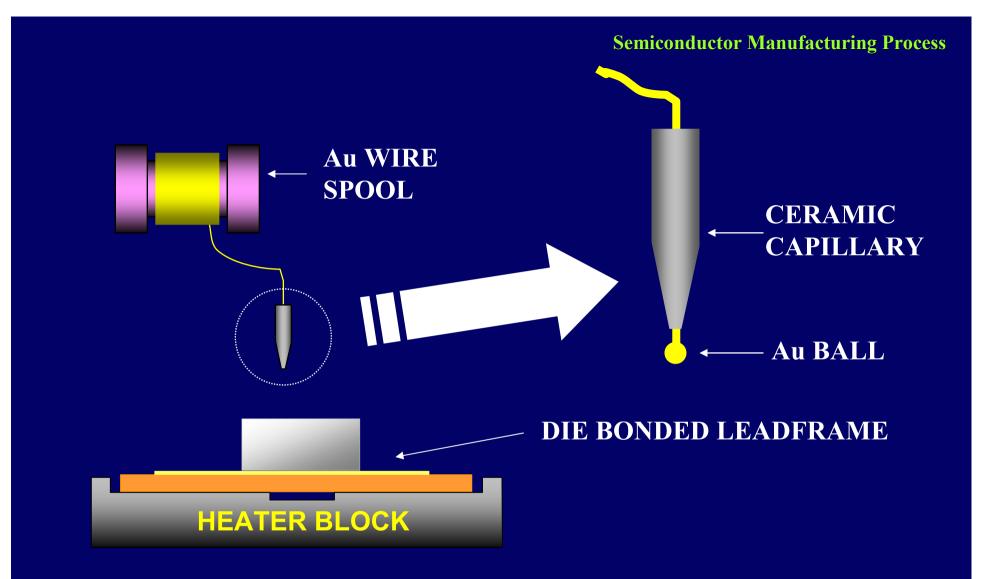
## **Thermosonic Wirebond**

**Semiconductor Manufacturing Process** 

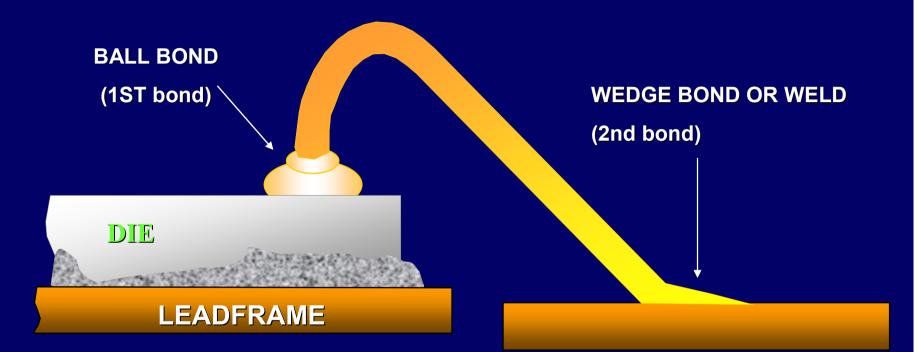


### 99.99% Gold wires

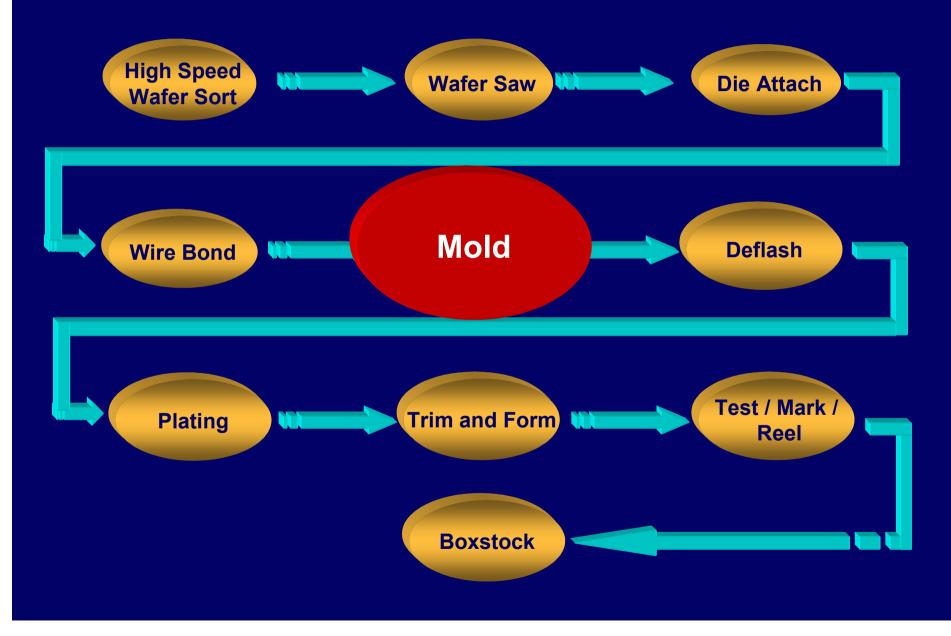
Thermosonic wire bonding employs heat and ultrasonic power to bond Au wire on die surface.



### **THERMOSONIC WIREBOND SET-UP**



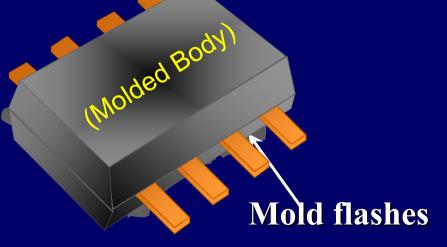
### **WIREBONDS**

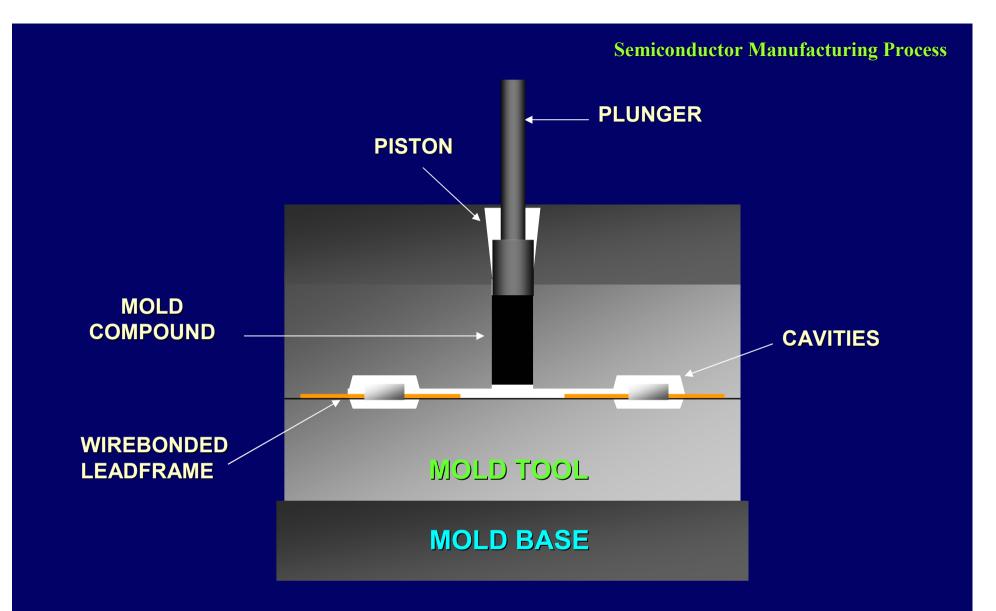


## **Transfer Molding**

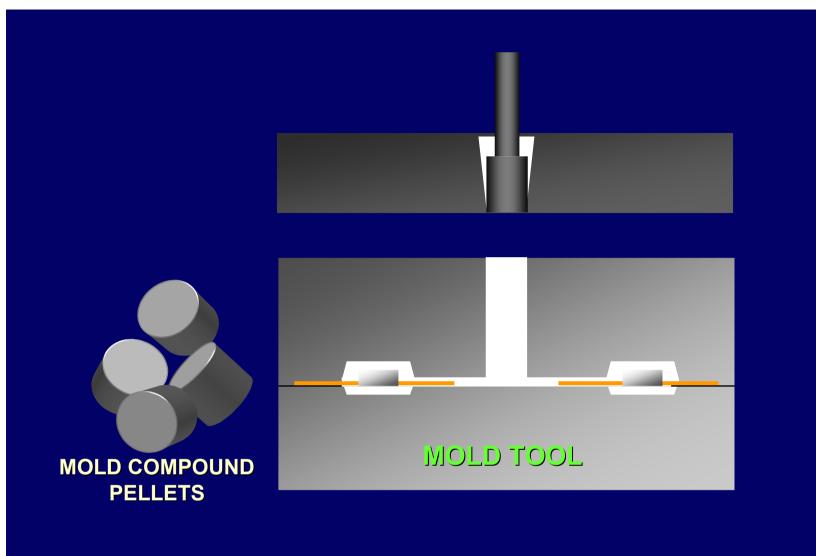
**Semiconductor Manufacturing Process** 

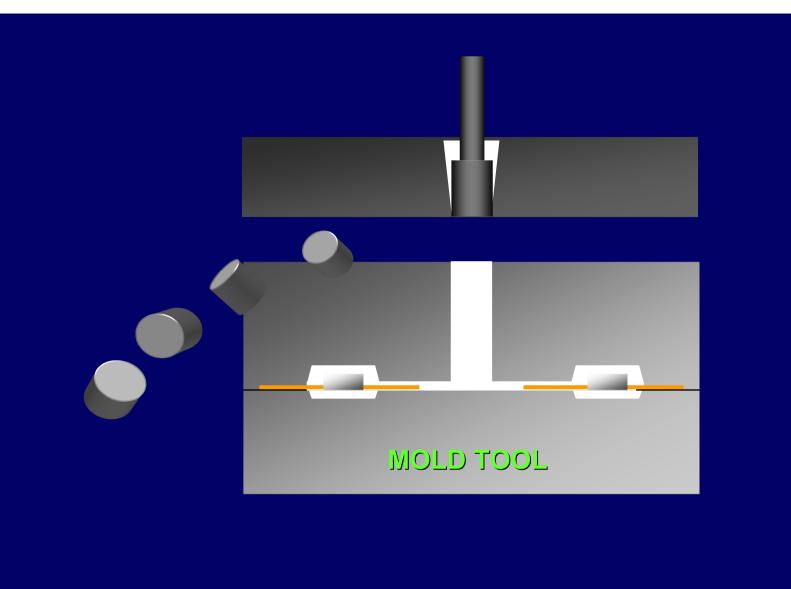
Transfer molding is a process of encapsulating the wirebonded microchip. It gives the package its distinct shape.

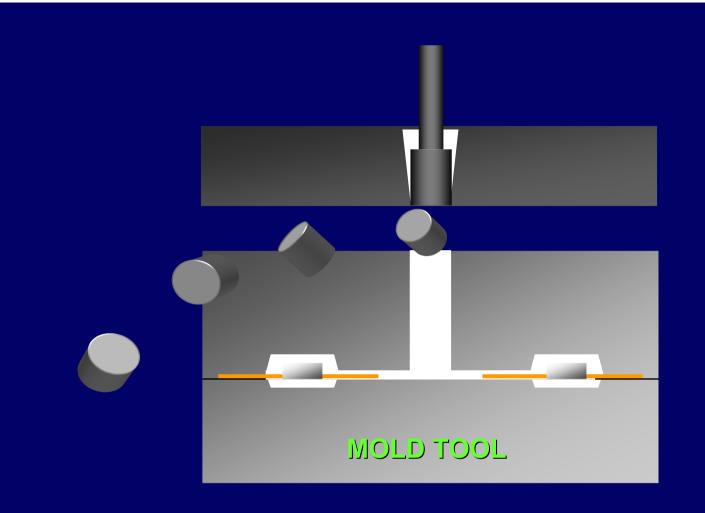


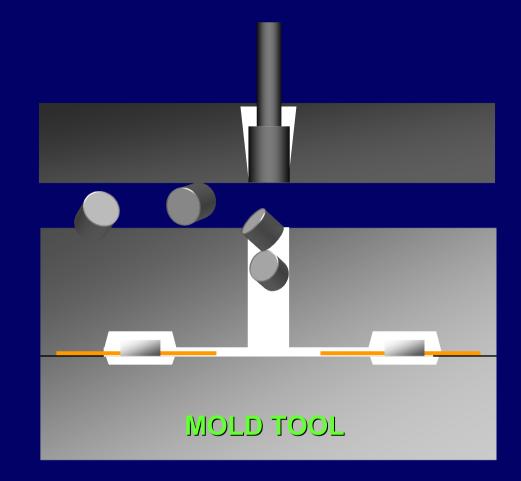


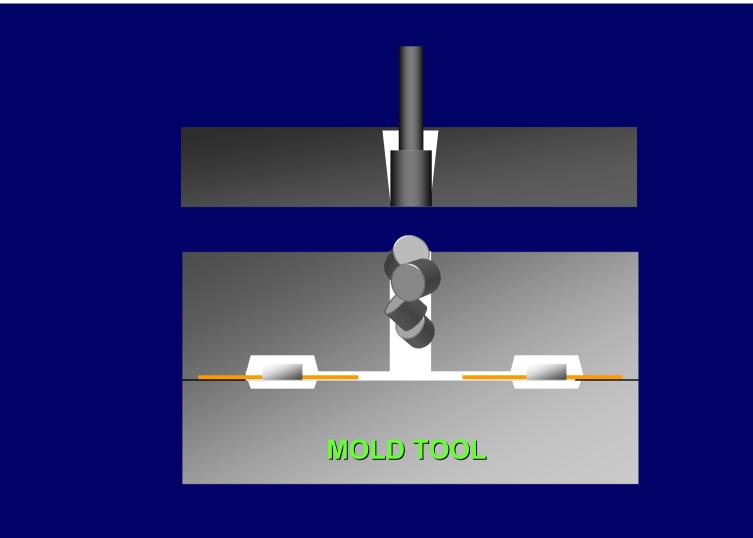
### **TRANSFER MOLDING SET-UP**

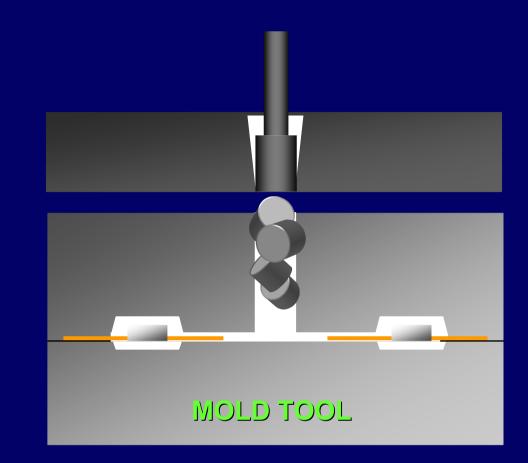


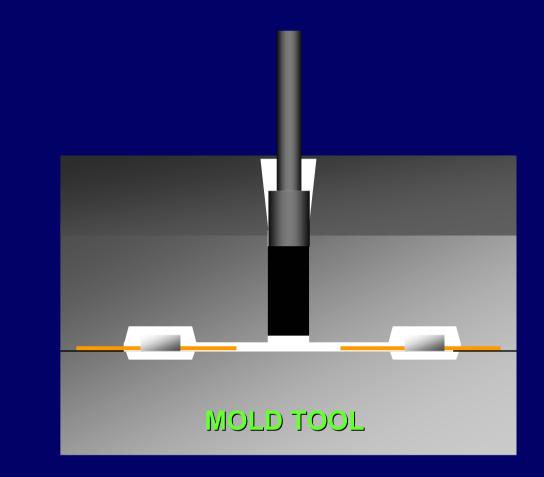


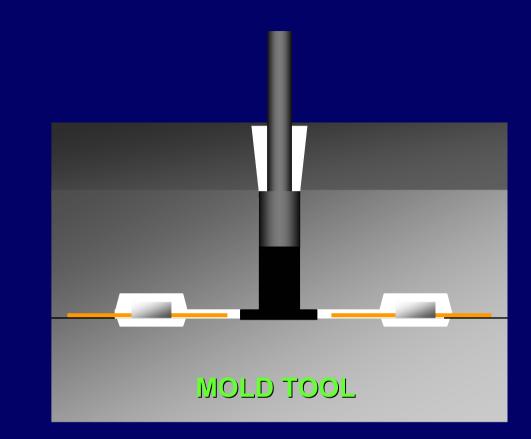


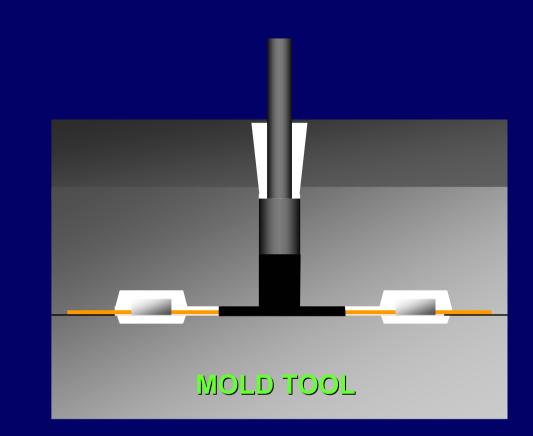


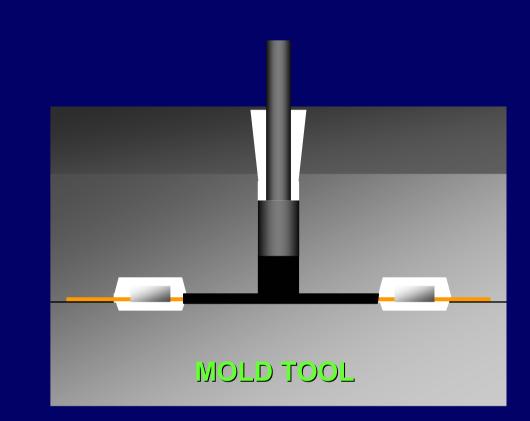


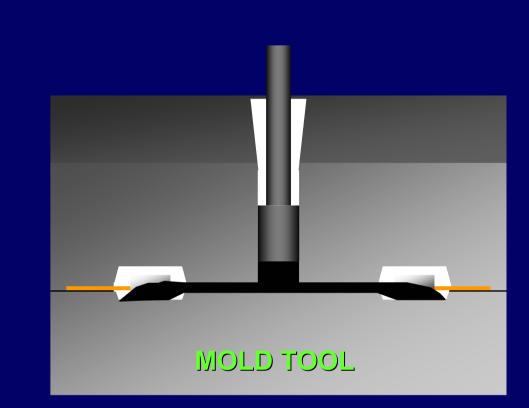


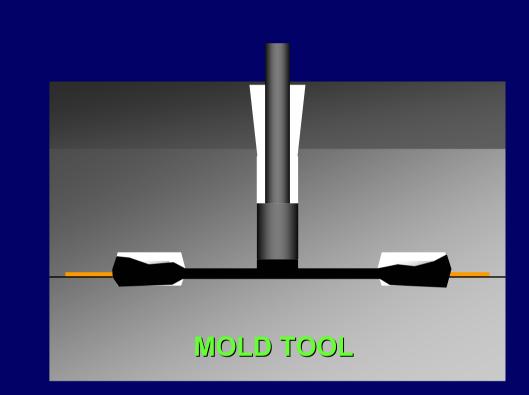


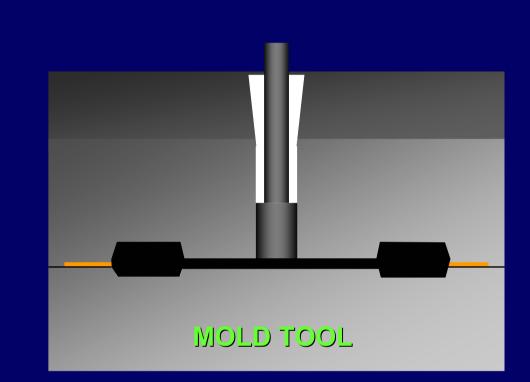


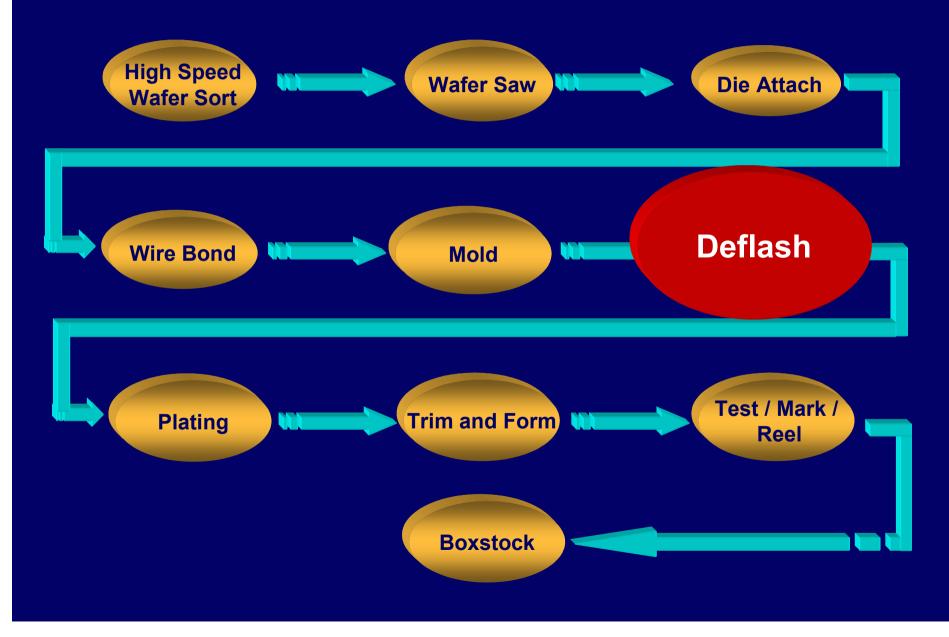








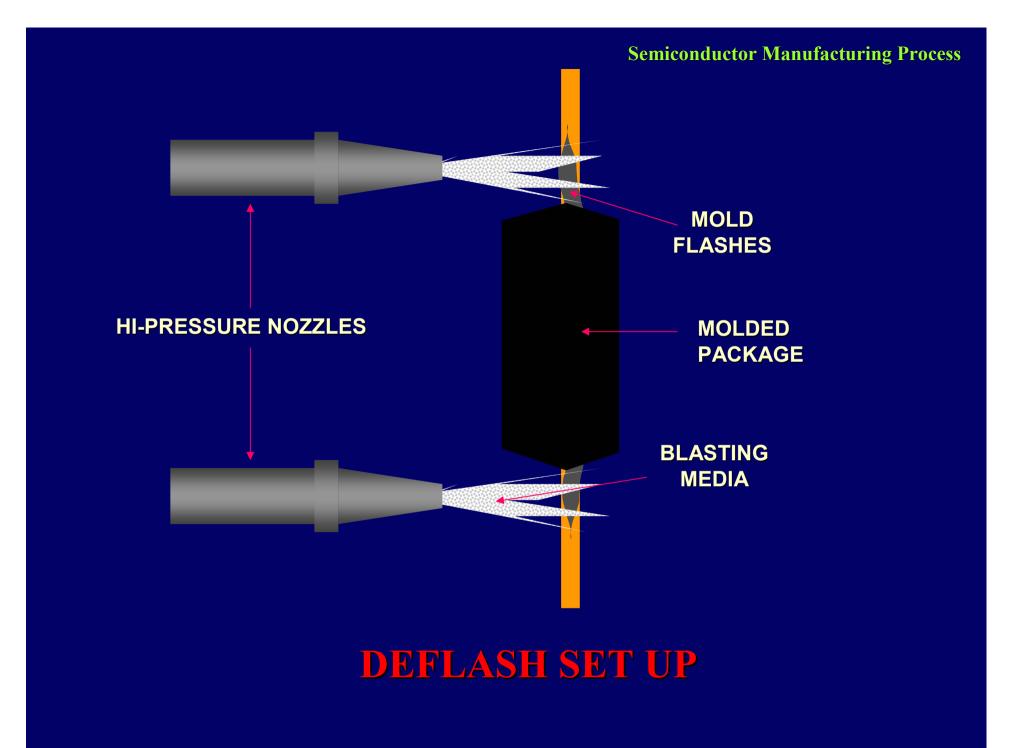


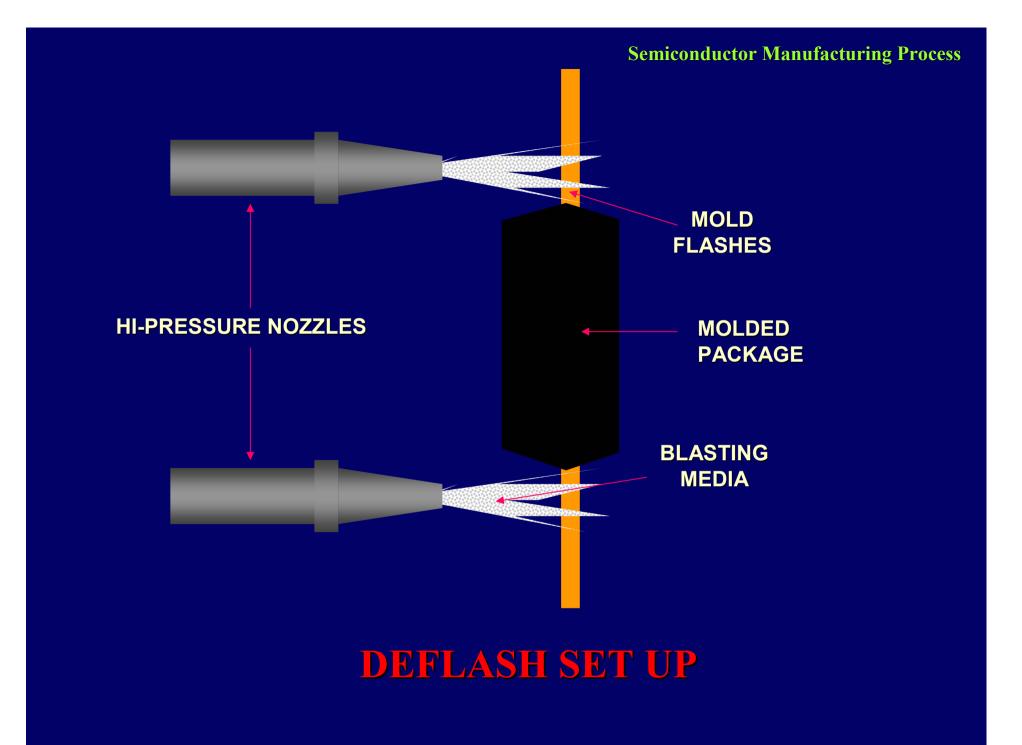


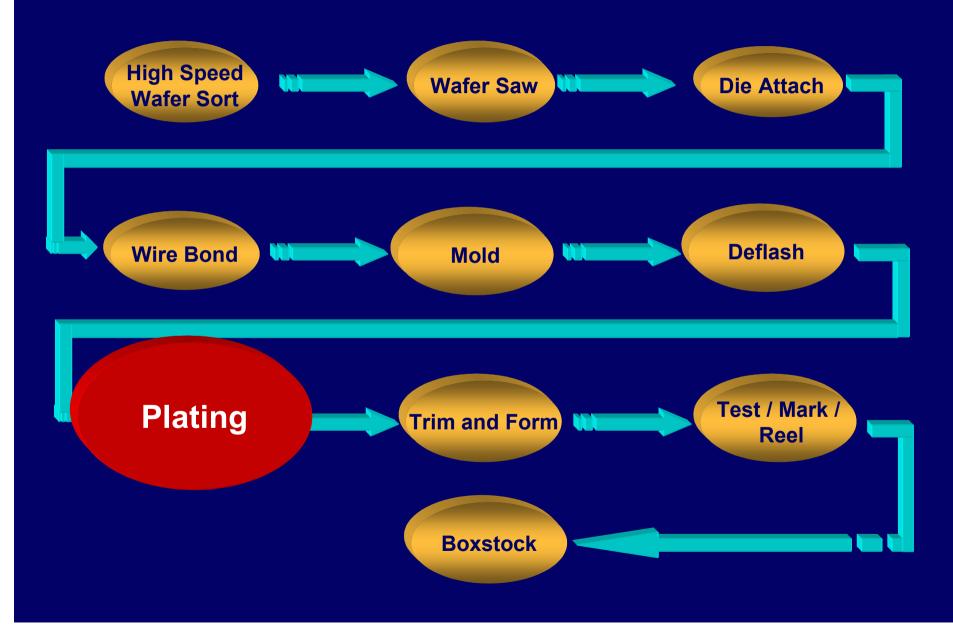


**Semiconductor Manufacturing Process** 

Deflashing is a mechanical process of removing the mold flashes and bleeds.





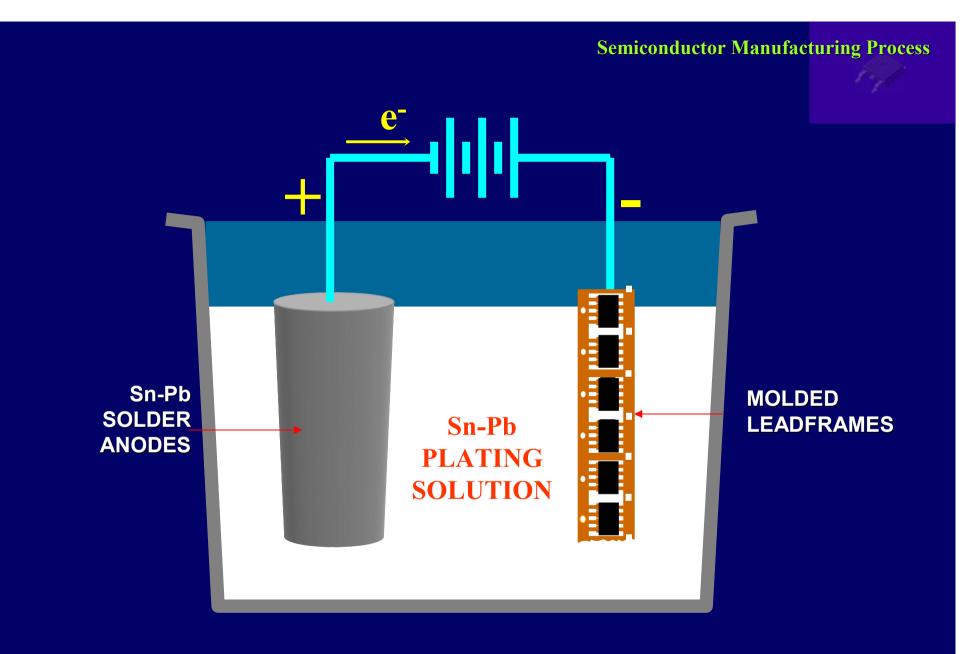


## **Solder Plating**

**Semiconductor Manufacturing Process** 

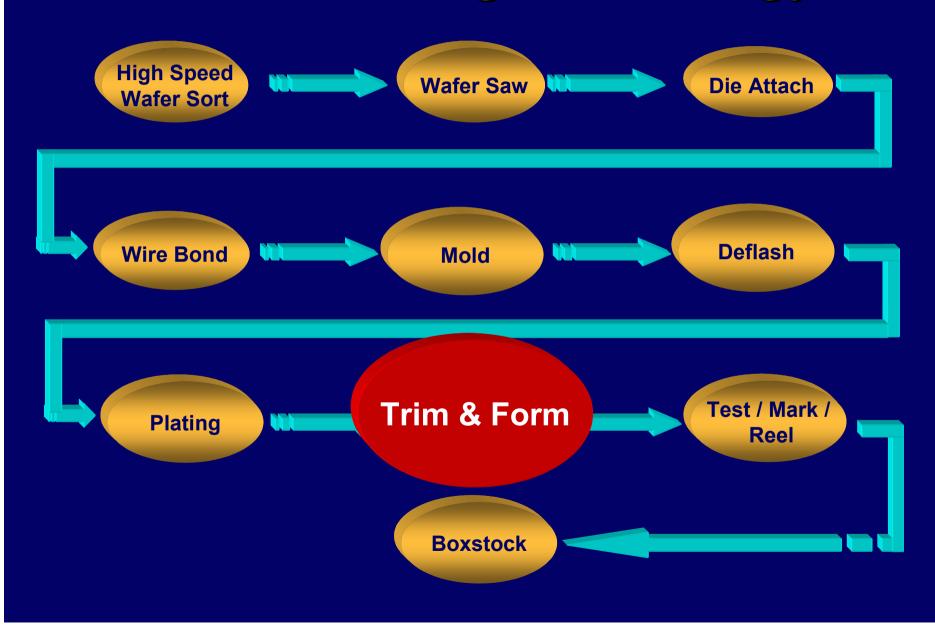
**Sn-Pb coated leads** 

Solder plating is the process of coating the leads with Sn-Pb solder by electrolysis.



#### **SOLDER PLATING SET UP**

## Manufacturing Technology



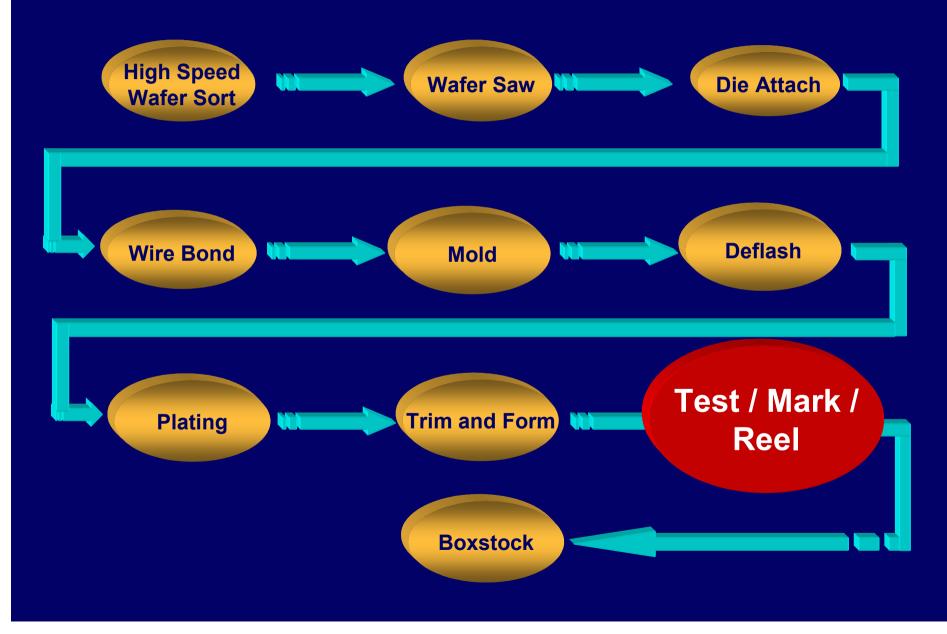
### **Trim and Form**

**Semiconductor Manufacturing Process** 

Trim & Form is a mechanical process of trimming and leadforming the leads.

Singulated packages/ units

## Manufacturing Technology



# **Electrical Testing Semiconductor Manufacturing Process Electrical testing process Electrically good units** sorts out fall-outs from electrically good units. R **Electrical Fall-outs**

### **Laser Marking**

Semiconductor Manufacturing Process

LA SER marking process im prints d evice name, da ecode & company log o on the package

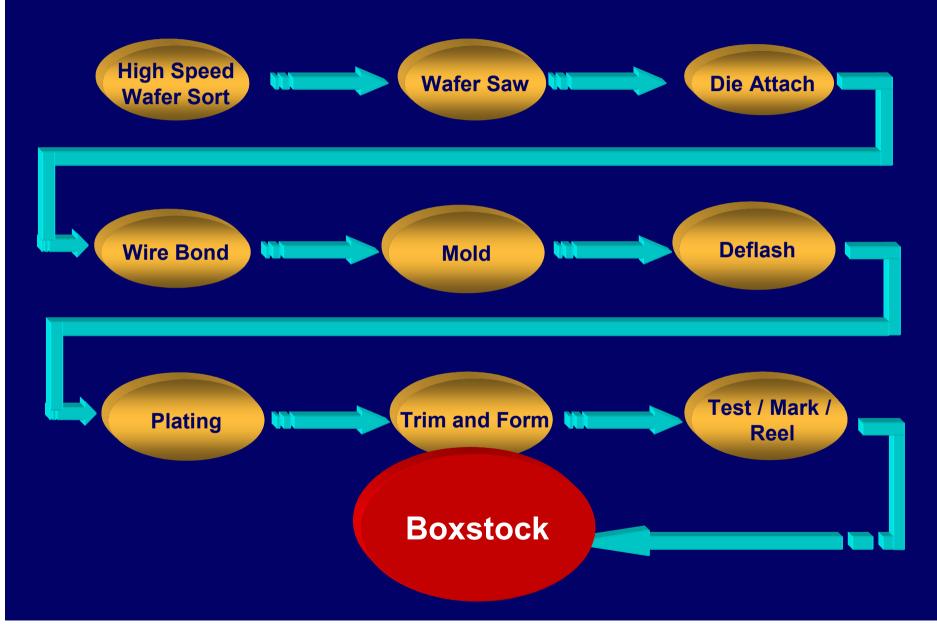
**F** 832 8304

## **Tape and Reel Packing**

Semiconductor Manufacturing Process

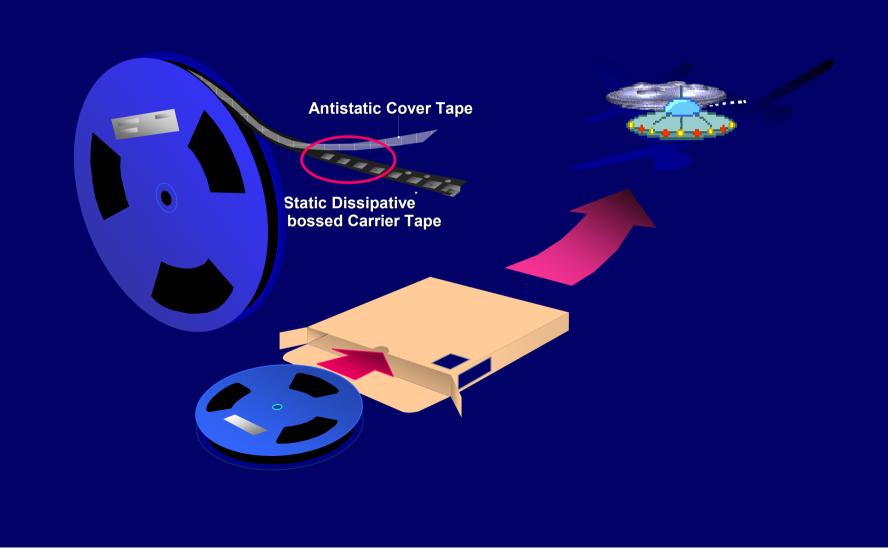
Tape & reel is the standard packing for surface mount packages/ devices.





## **Tape and Reel Packing**

Semiconductor Manufacturing Process



## Manufacturing Technology

