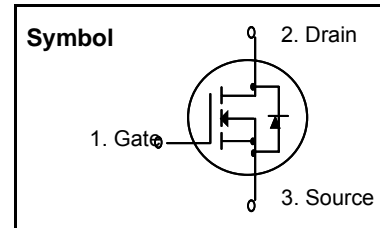


N-Channel MOSFET

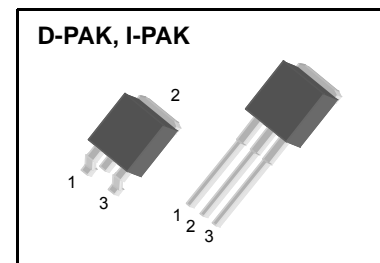
Features

- $R_{DS(on)}$ (Max 5.0 Ω)@ $V_{GS}=10V$
- Gate Charge (Typical 9.5nC)
- Improved dv/dt Capability, High Ruggedness
- 100% Avalanche Tested
- Maximum Junction Temperature Range (150°C)



General Description

This Power MOSFET is produced using Wisdom's advanced planar stripe, DMOS technology. This latest technology has been especially designed to minimize on-state resistance, have a high rugged avalanche characteristics. These devices are well suited for high efficiency switch mode power supplies, active power factor correction, electronic lamp ballasts based on half bridge topology.



Absolute Maximum Ratings

Symbol	Parameter	Value	Units
V_{DSS}	Drain to Source Voltage	650	V
I_D	Continuous Drain Current(@ $T_C = 25^\circ C$)	2.0	A
	Continuous Drain Current(@ $T_C = 100^\circ C$)	1.1	A
I_{DM}	Drain Current Pulsed (Note 1)	6.0	A
V_{GS}	Gate to Source Voltage	± 30	V
E_{AS}	Single Pulsed Avalanche Energy (Note 2)	120	mJ
E_{AR}	Repetitive Avalanche Energy (Note 1)	4.4	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)	4.5	V/ns
P_D	Total Power Dissipation(@ $T_C = 25^\circ C$)	44	W
	Derating Factor above 25 °C	0.35	W/°C
T_{STG}, T_J	Operating Junction Temperature & Storage Temperature	- 55 ~ 150	°C
T_L	Maximum Lead Temperature for soldering purpose, 1/8 from Case for 5 seconds.	300	°C

Thermal Characteristics

Symbol	Parameter	Value			Units
		Min.	Typ.	Max.	
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	-	-	2.87	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient*	-	-	50	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	-	-	110	°C/W

* When mounted on the minimum pad size recommended (PCB Mount)

WFD/U2N60

Electrical Characteristics (T_C = 25 °C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
Off Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = 250uA	650	-	-	V
$\frac{\Delta BV_{DSS}}{\Delta T_J}$	Breakdown Voltage Temperature coefficient	I _D = 250uA, referenced to 25 °C	-	0.6	-	V/°C
I _{DSS}	Drain-Source Leakage Current	V _{DS} = 600V, V _{GS} = 0V	-	-	10	uA
		V _{DS} = 480V, T _C = 125 °C	-	-	100	uA
I _{GSS}	Gate-Source Leakage, Forward	V _{GS} = 30V, V _{DS} = 0V	-	-	100	nA
	Gate-source Leakage, Reverse	V _{GS} = -30V, V _{DS} = 0V	-	-	-100	nA
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250uA	2.0	-	5.0	V
R _{DS(on)}	Static Drain-Source On-state Resistance	V _{GS} = 10 V, I _D = 0.9A	-	4.0	5.0	Ω
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{GS} = 0 V, V _{DS} = 25V, f = 1MHz	-	320	420	pF
C _{oss}	Output Capacitance		-	35	46	
C _{rss}	Reverse Transfer Capacitance		-	4.5	6.0	
Dynamic Characteristics						
t _{d(on)}	Turn-on Delay Time	V _{DD} = 300V, I _D = 2.0A, R _G = 25Ω (Note 4, 5)	-	8	30	ns
t _r	Rise Time		-	23	60	
t _{d(off)}	Turn-off Delay Time		-	25	60	
t _f	Fall Time		-	28	70	
Q _g	Total Gate Charge	V _{DS} = 480V, V _{GS} = 10V, I _D = 2.0A (Note 4, 5)	-	9.5	13	nC
Q _{gs}	Gate-Source Charge		-	1.6	-	
Q _{gd}	Gate-Drain Charge(Miller Charge)		-	4.0	-	

Source-Drain Diode Ratings and Characteristics

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit.
I _S	Continuous Source Current	Integral Reverse p-n Junction Diode in the MOSFET	-	-	2	A
I _{SM}	Pulsed Source Current		-	-	6.0	
V _{SD}	Diode Forward Voltage	I _S = 1.8A, V _{GS} = 0V	-	-	1.4	V
t _{rr}	Reverse Recovery Time	I _S = 2.0A, V _{GS} = 0V, di _F /dt = 100A/us	-	230	-	ns
Q _{rr}	Reverse Recovery Charge		-	1.0	-	uC

※ NOTES

1. Repeatability rating : pulse width limited by junction temperature
2. L = 68mH, I_{AS} = 1.8A, V_{DD} = 50V, R_G = 25Ω, Starting T_J = 25°C
3. I_{SD} ≤ 2A, di/dt ≤ 200A/us, V_{DD} ≤ BV_{DSS}, Starting T_J = 25°C
4. Pulse Test : Pulse Width ≤ 300us, Duty Cycle ≤ 2%
5. Essentially independent of operating temperature.

Typical Characteristics

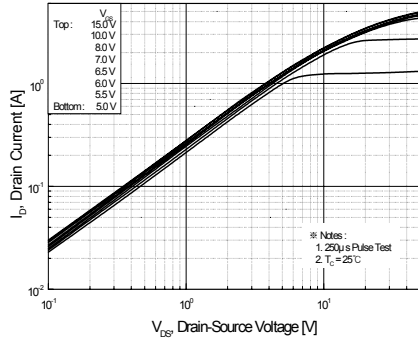


Figure 1. On-Region Characteristics

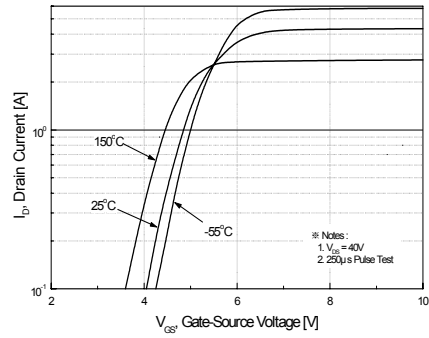


Figure 2. Transfer Characteristics

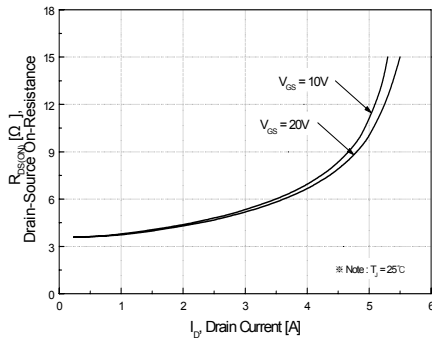


Figure 3. On-Resistance Variation vs Drain Current and Gate Voltage

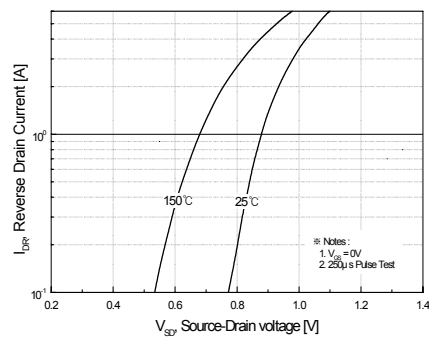


Figure 4. Body Diode Forward Voltage Variation with Source Current

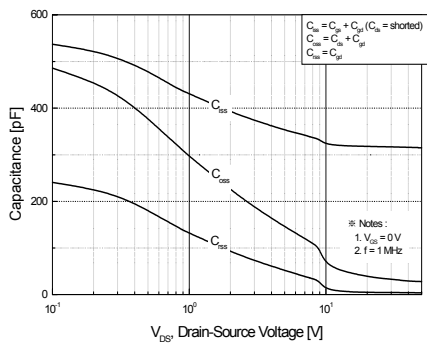


Figure 5. Capacitance Characteristics

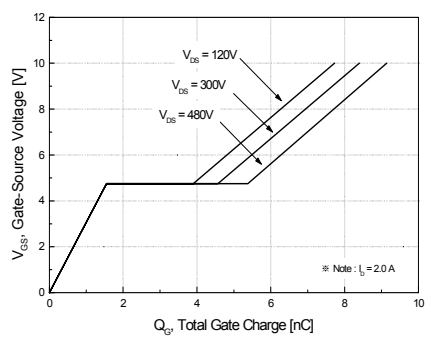


Figure 6. Gate Charge Characteristics

Typical Characteristics (Continued)

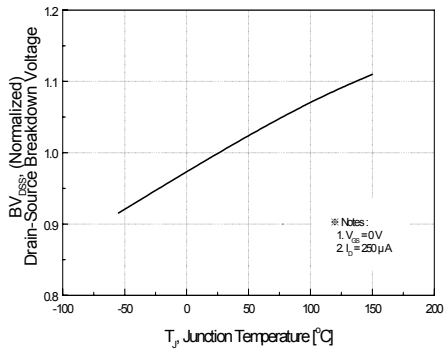


Figure 7. Breakdown Voltage Variation vs Temperature

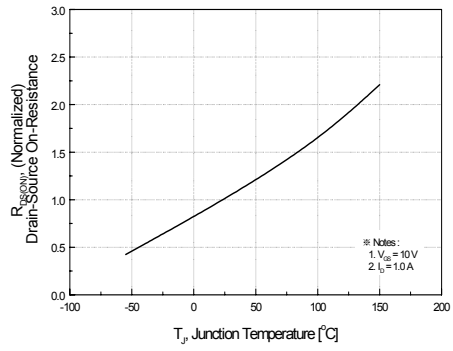


Figure 8. On-Resistance Variation vs Temperature

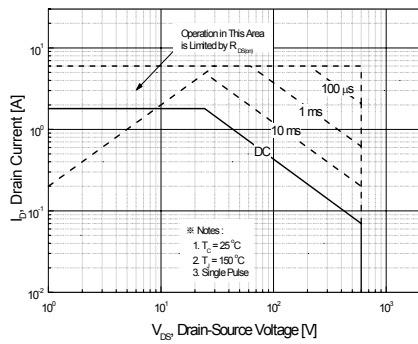


Figure 9. Maximum Safe Operating Area

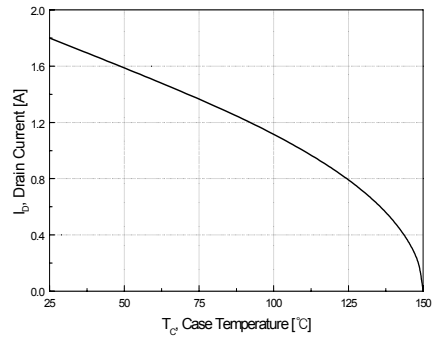


Figure 10. Maximum Drain Current vs Case Temperature

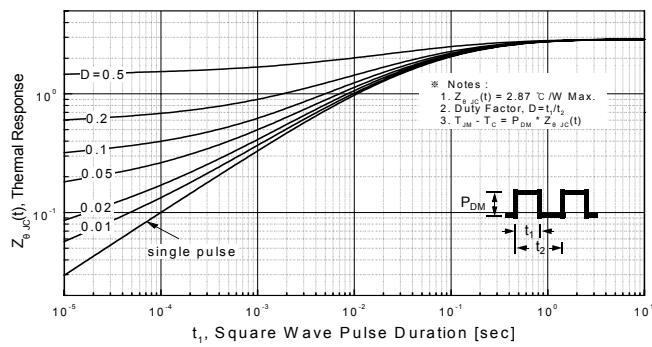
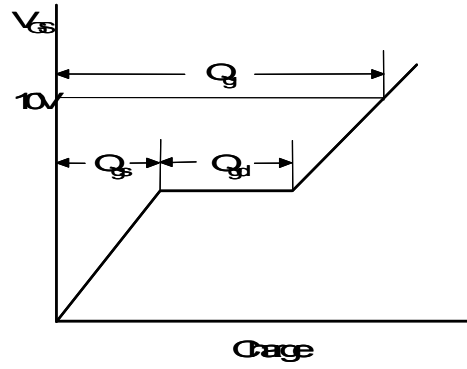
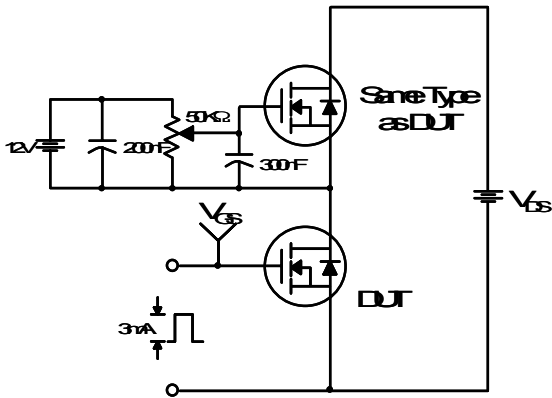
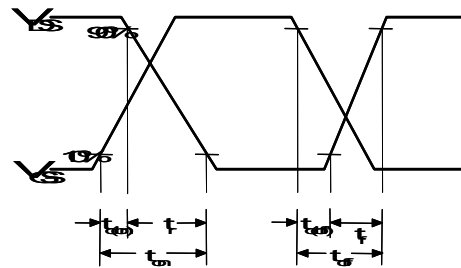
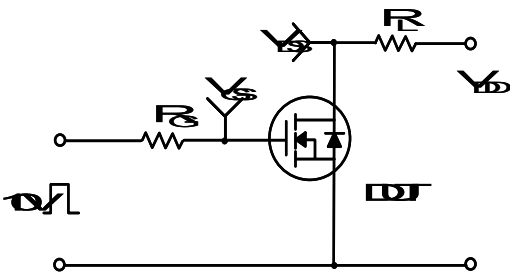


Figure 11. Transient Thermal Response Curve

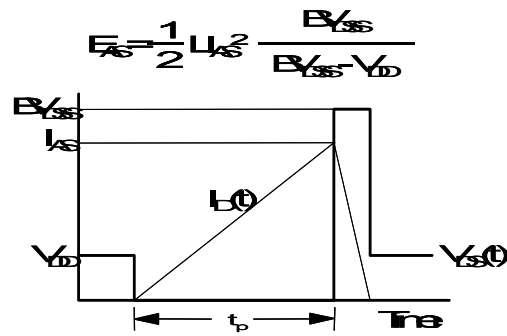
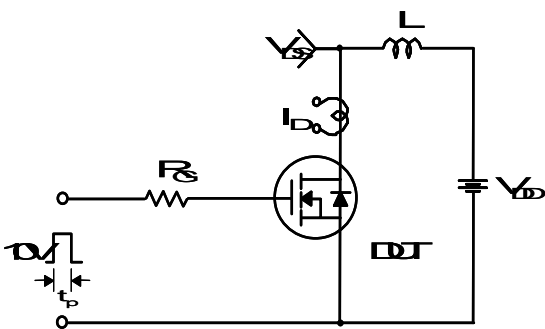
Gate Charge Test Circuit & Waveform



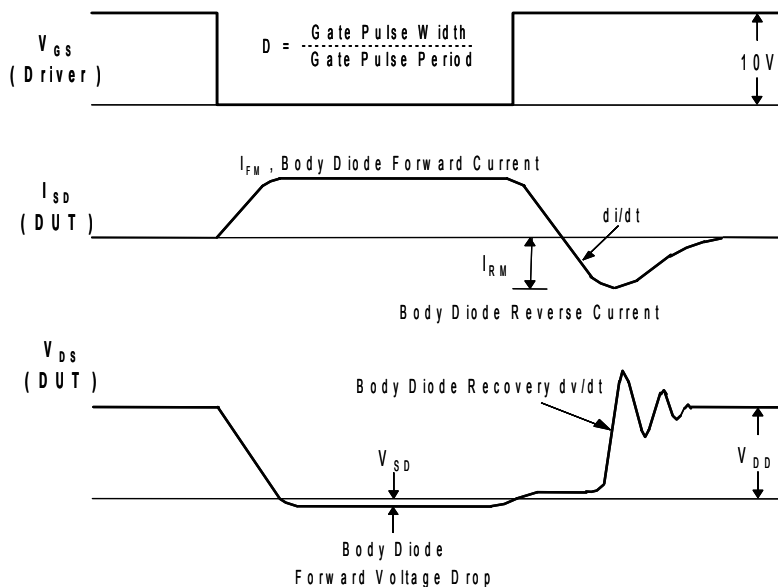
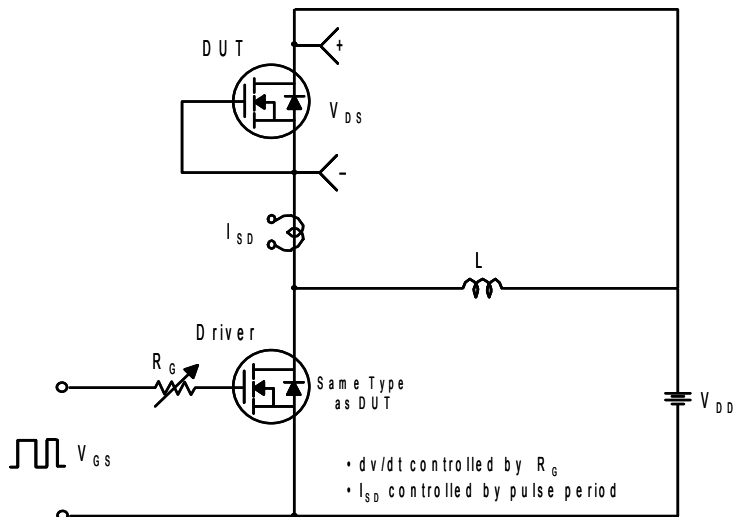
Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching Test Circuit & Waveforms

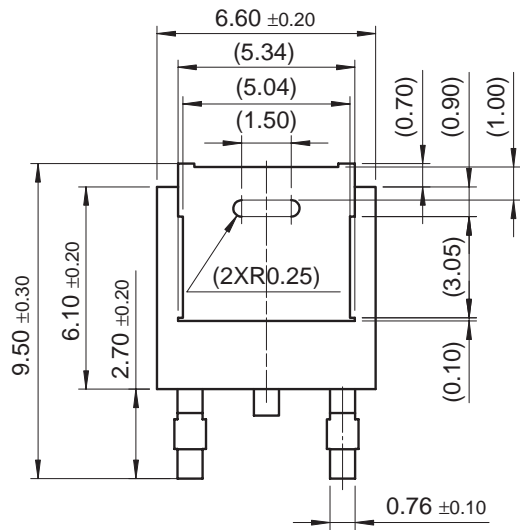
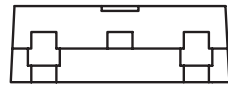
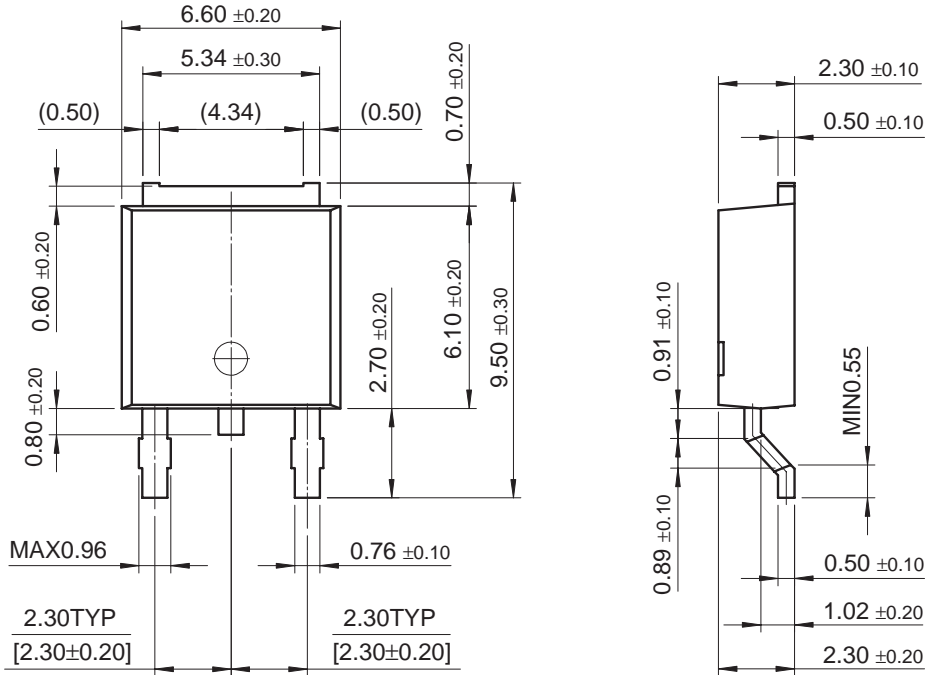


Peak Diode Recovery dv/dt Test Circuit & Waveforms



Package Dimensions

DPAK



Package Dimensions (Continued)

IPAK

