

200KHz, 1A PWM Buck DC/DC Converter

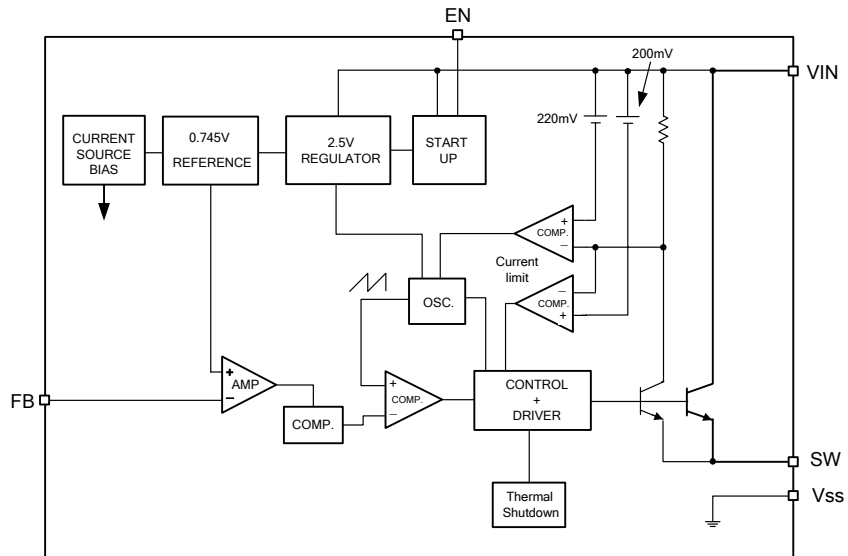
❖ **GENERAL DESCRIPTION**

The AX3023 series are monolithic IC designed for a step-down DC/DC converter, and own the ability of driving a 1A load without additional transistor. It saves board space. The external shutdown function can be controlled by logic level and then come into standby mode. The internal compensation makes feedback control having good line and load regulation without external design. Regarding protected function, thermal shutdown is to prevent over temperature operating from damage, and current limit is against over current operating of the output switch. If the AX3023's V_{FB} is down below 0.5V, the switching frequency will be reduced. The AX3023 operates at a switching frequency of 200KHz thus allow smaller sized filter components. Other features include a guaranteed +3% tolerance on output voltage under specified input voltage and output load conditions, The chips are available in SOT23-5L and SOP-8L packages.

❖ **FEATURES**

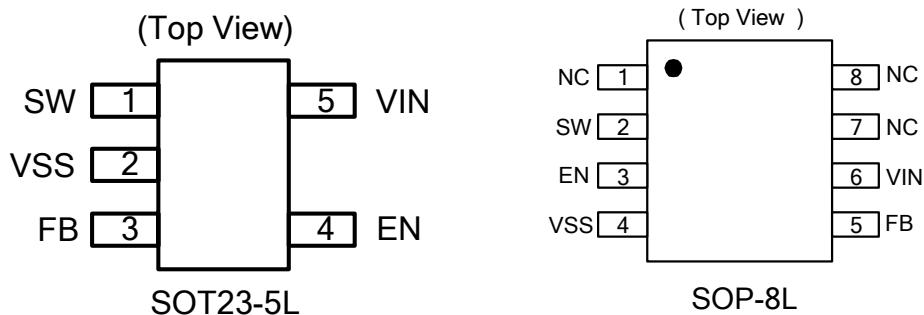
- Output voltage: adjustable output version.
- Adjustable version output voltage range: $0.745V$ to $26V \pm 3\%$.
- Fixed switching frequency is 200KHz
- Thermal-shutdown and current-limit protection.
- ON/OFF shutdown control input.
- Short Circuit Protect (SCP).
- Operating voltage can be up to 28V.
- Output load current: 1A.
- SOT23-5L and SOP-8L packages.
- Low power standby mode.
- Built-in switching transistor on chip.

❖ BLOCK DIAGRAM



❖ PIN ASSIGNMENT

The packages of AX3023 are SOT23-5L and SOP-8L; the pin assignment is given by:



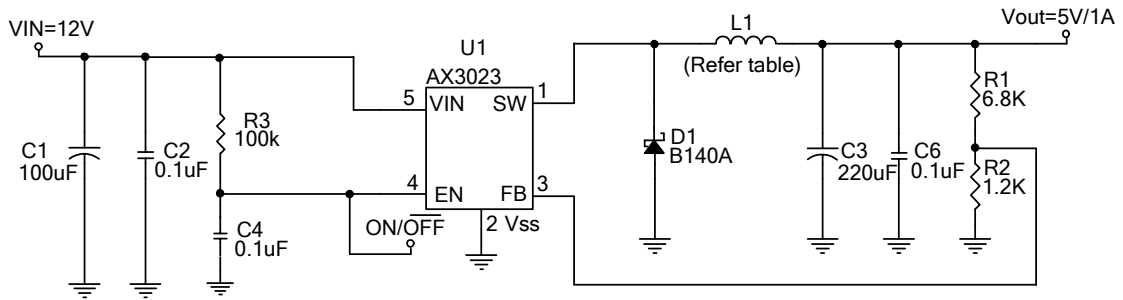
Name	Description
VIN	Operating voltage input
SW	Switching output
FB	Output voltage feedback control
EN	ON/OFF Shutdown
VSS	Ground pin
NC	No connect Pin

❖ **Electrical Characteristics** (Unless otherwise specified, $T_a=25^{\circ}\text{C}$, $V_{IN}=12\text{V}$, $V_{OUT}=3.3\text{V}$ ILOAD = 0.3A)

Characteristics	Symbol	Conditions	Min	Typ	Max	Units
Feedback Voltage	V_{FB}	$I_{OUT}=0.3\text{A}$	0.723	0.745	0.767	V
Quiescent Current	I_Q	$V_{FB}=1.2\text{V}$ force driver off	-	3.5	6	mA
Feedback bias current	I_{FB}	$I_{OUT}=0.1\text{A}$	-	-10	-50	nA
Shutdown supply Current	I_{SD}	$V_{EN}=0\text{V}$	-	2	10	uA
Oscillator frequency	F_{OSC}		140	200	260	KHz
Oscillator frequency of short circuit protect	F_{SCP}	(Adjustable) When $V_{FB}<0.5\text{V}$	-	50	-	KHz
Max. Duty Cycle (ON)	DC	$V_{FB}=1.2\text{V}$ force driver off	-	0	-	%
Min. Duty Cycle (OFF)		$V_{FB}=0\text{V}$ force driver on	-	100	-	
Current limit	I_{CL}	Peak current, No outside circuit $V_{FB}=0\text{V}$ force driver on	1.2	-	-	A
Saturation voltage	V_{SAT}	$I_{OUT}=1\text{A}$, No outside circuit $V_{FB}=0\text{V}$ force driver on	-	1.2	1.5	V
SW pin=0V	SW pin leakage current	$V_{IN}=28\text{V}$, No outside circuit $V_{FB}=1.0\text{V}$ force driver off	-	-	-200	uA
SW pin=-0.8V			-	-5	-	mA
EN pin logic input threshold voltage	V_{IH}	High (regulator ON)	-		2.0	V
	V_{IL}	Low (regulator OFF)	0.5		-	
EN pin logic input current	I_H	$V_{EN}=2.5\text{V}$ (ON)	-	20	-	uA
EN pin input current	I_L	$V_{EN}=0.3\text{V}$ (OFF)	-	-5	-	
Thermal shutdown Temp	TSD		-	145	-	$^{\circ}\text{C}$

❖ Application Circuit

Adjustable Output Voltage Version



$$V_{out} = V_{FB} \times \left(1 + \frac{R1}{R2}\right), \quad V_{FB} = 0.745V, \quad R2 = 0.75K \sim 4K$$

Table 1 Resistor select for output voltage setting

V _{OUT}	R2	R1
5V	1.2K	6.8K
3.3V	2.4K	8.2K
2.5V	2K	4.7K
1.8V	3.3K	4.7K
1.5V	2K	2K
1.3V	2K	1.5K
1.2V	2K	1.2K

L1 recommend value (V _{IN} =12V, I _{OUT} =1A,)				
V _{OUT}	1.8 V	2.5V	3.3V	5V
L1 Value	33uH	33uH	47uH	47uH

❖ Function Descriptions

Pin Functions

V_{IN}

This is the positive input supply for the IC switching regulator. A suitable input bypass capacitor must be presented at this pin to minimize voltage transients and to supply the switching currents needed by the regulator.

V_{SS}

Circuit ground.

SW

Internal switch. The voltage at this pin switches between $(+V_{IN} - V_{SAT})$ and approximately $-0.5V$, with a duty cycle of approximately V_{OUT} / V_{IN} . To minimize coupling to sensitive circuitry, the PC board copper area connected to this pin should be minimized.

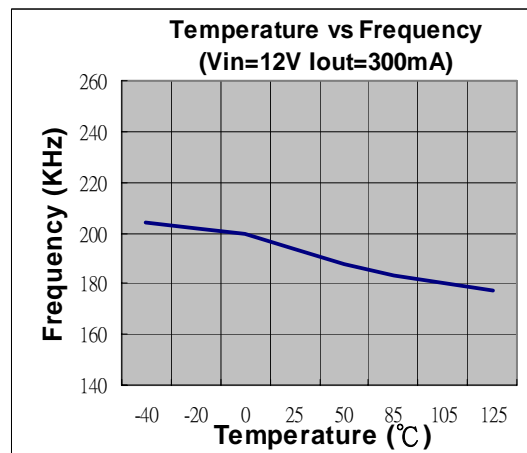
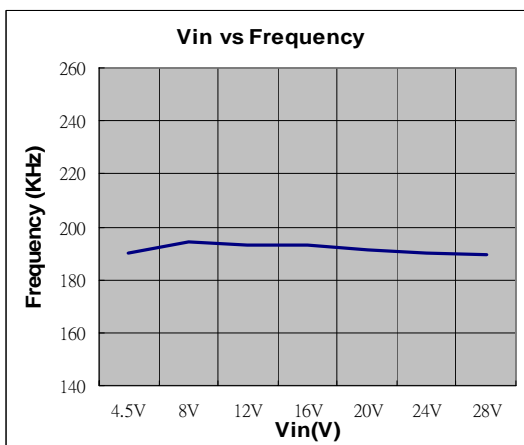
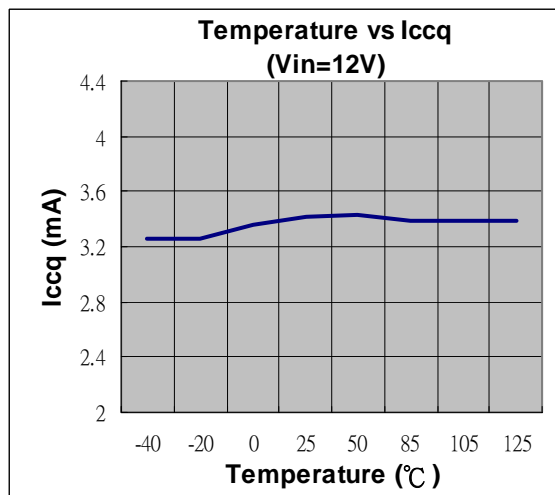
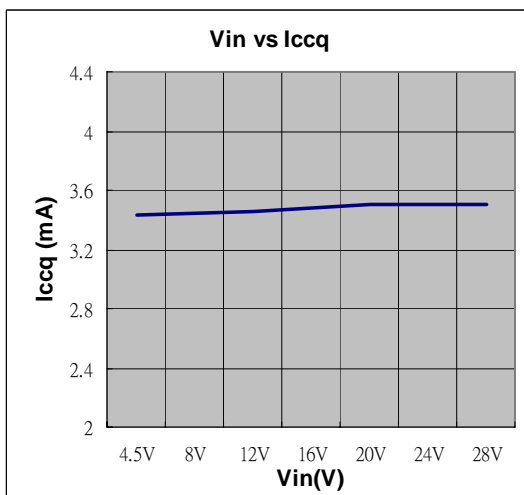
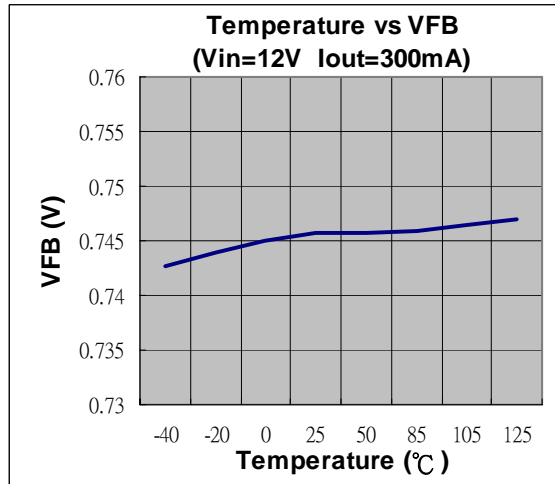
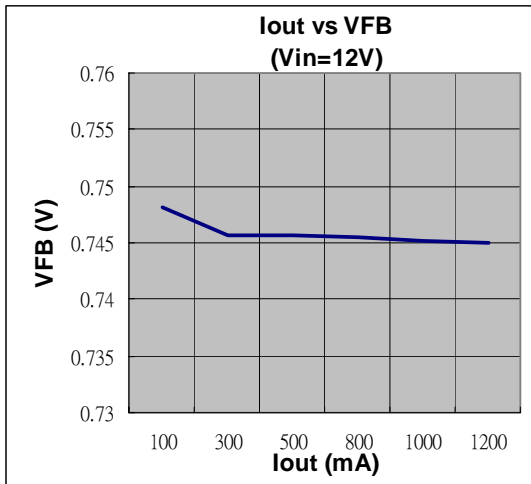
FB

Senses the regulated output voltage to complete the feedback loop.

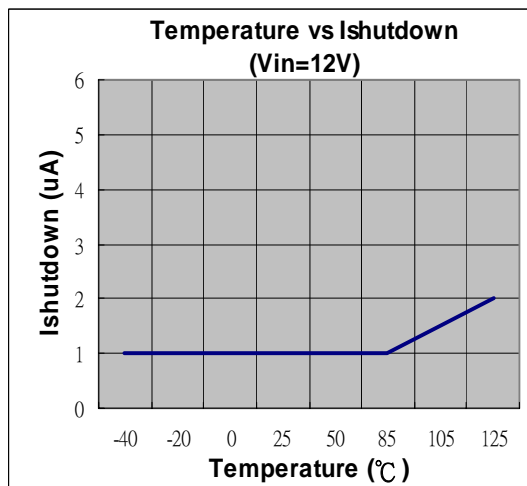
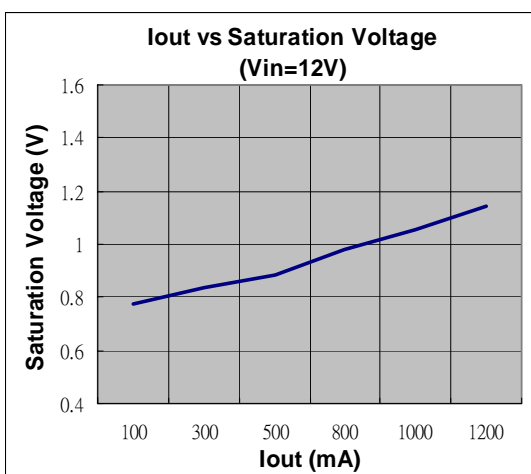
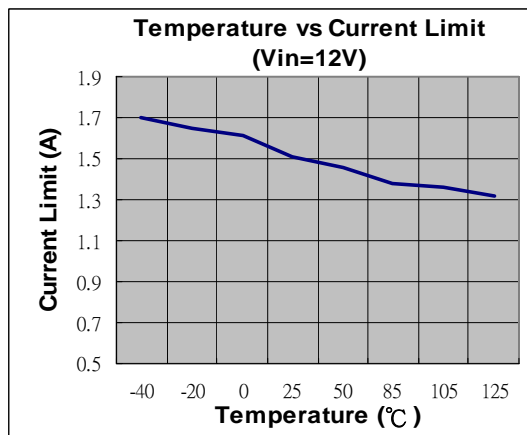
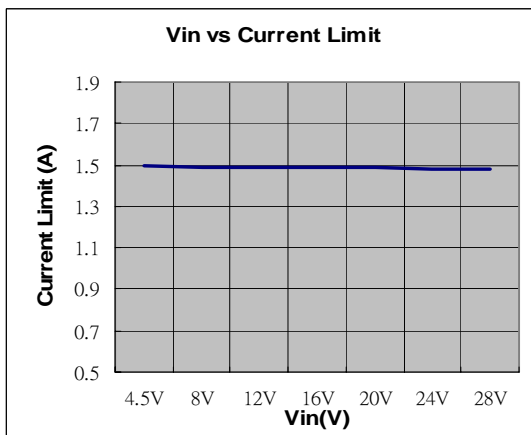
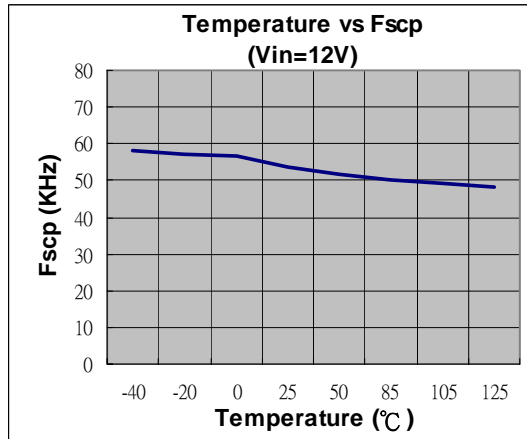
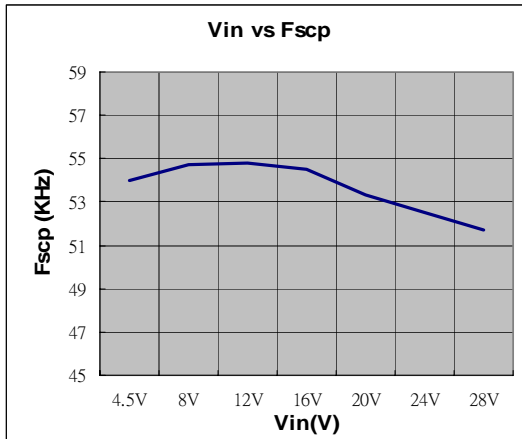
EN

Allows the switching regulator circuit to be shutdown using logic level signals thus dropping the total input supply current to approximately 10uA. Pulling this pin below a threshold voltage of approximately 0.5V shuts the regulator down, and pulling this pin above 2.0V (up to a maximum of V_{IN}) turns the regulator on.

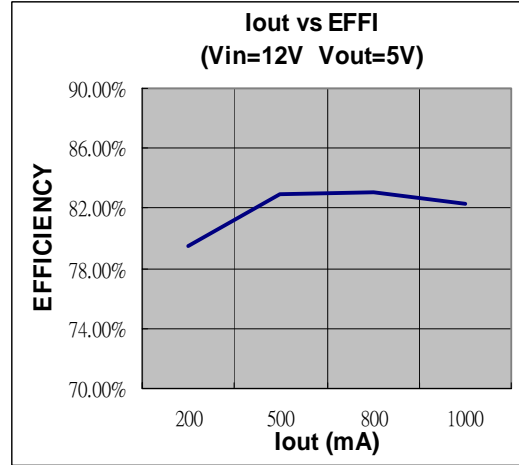
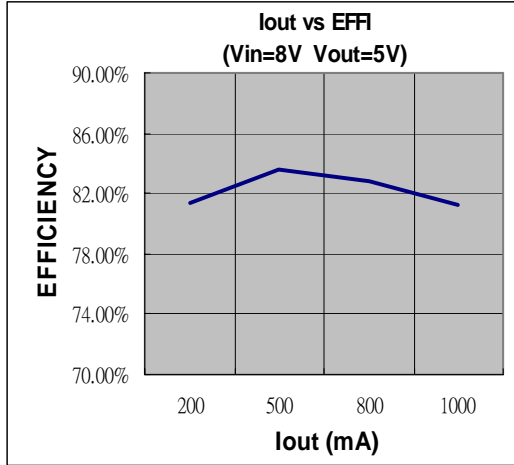
❖ Typical Characteristics



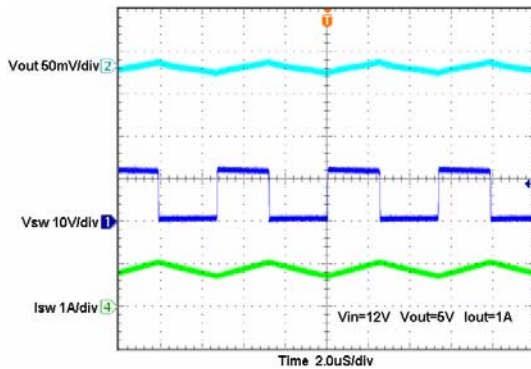
❖ Typical Characteristics



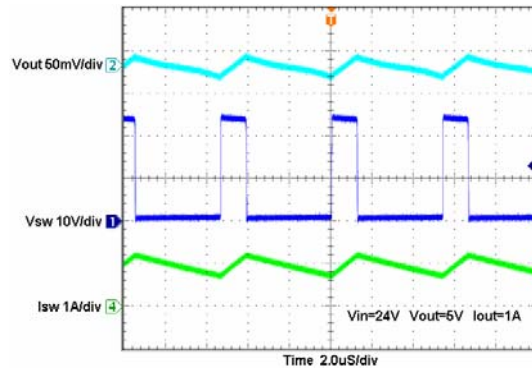
❖ Typical Characteristics



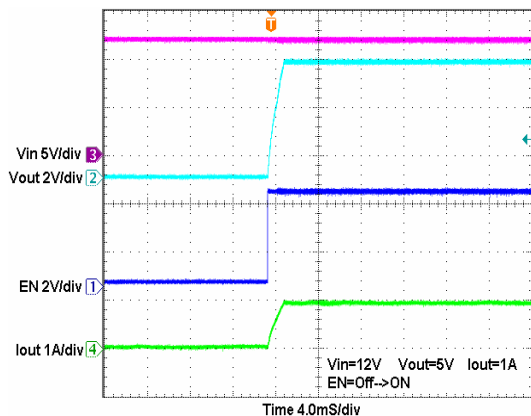
Output Ripple (12V→5V @1A)



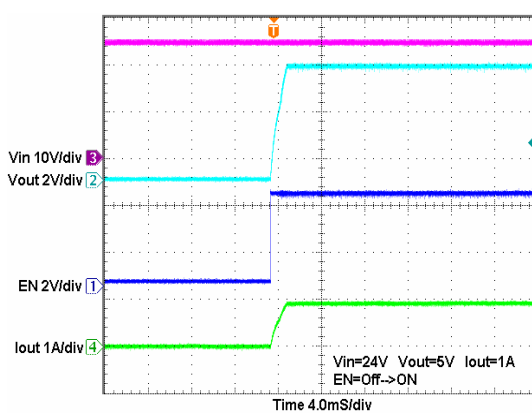
Output Ripple (24V→5V @1A)



EN Off→ON Test (12V→5V @1A)

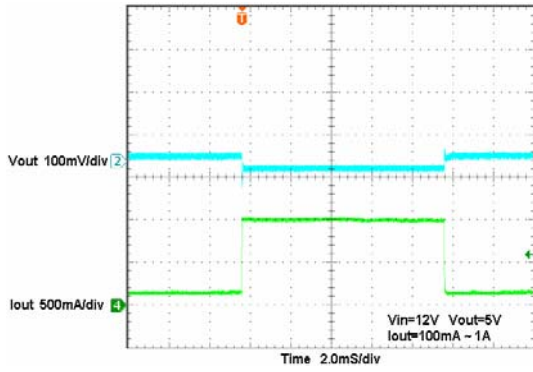


EN Off→ON Test (24V→5V @1A)

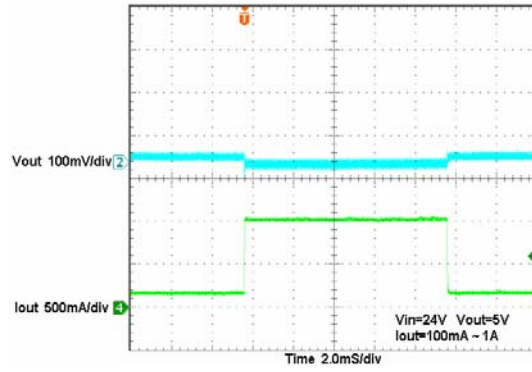


❖ **Typical Characteristics**

Load Transient (12V→5V @0.1~1A)

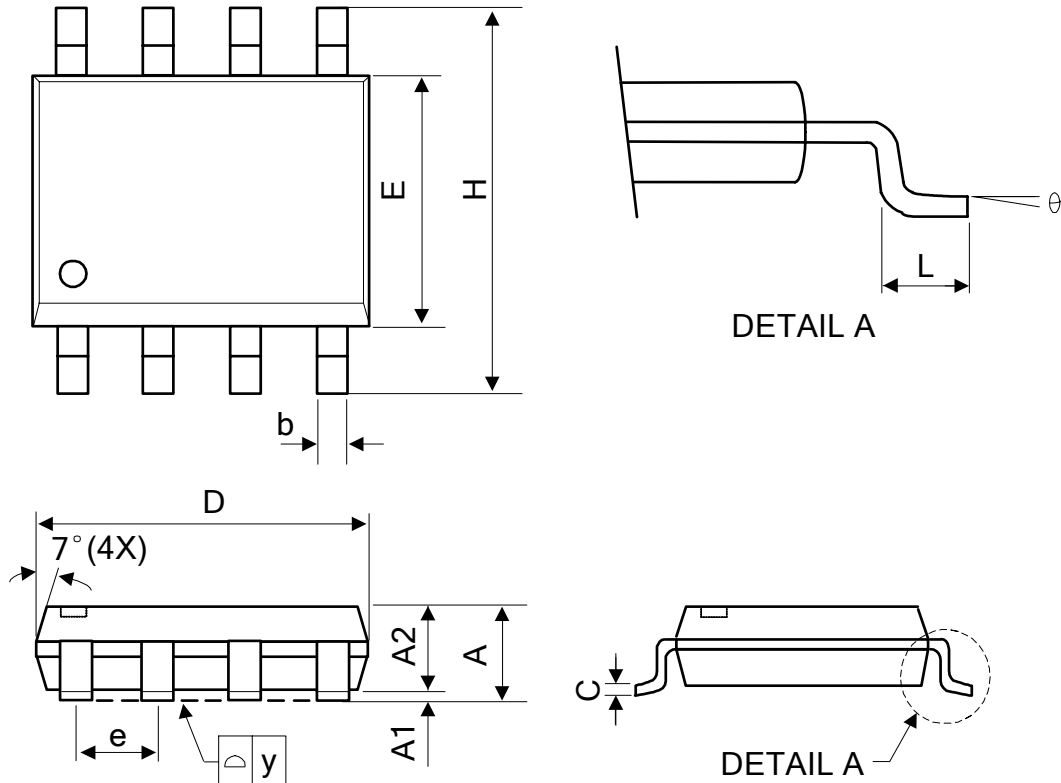


Load Transient (24V→5V @0.1~1A)



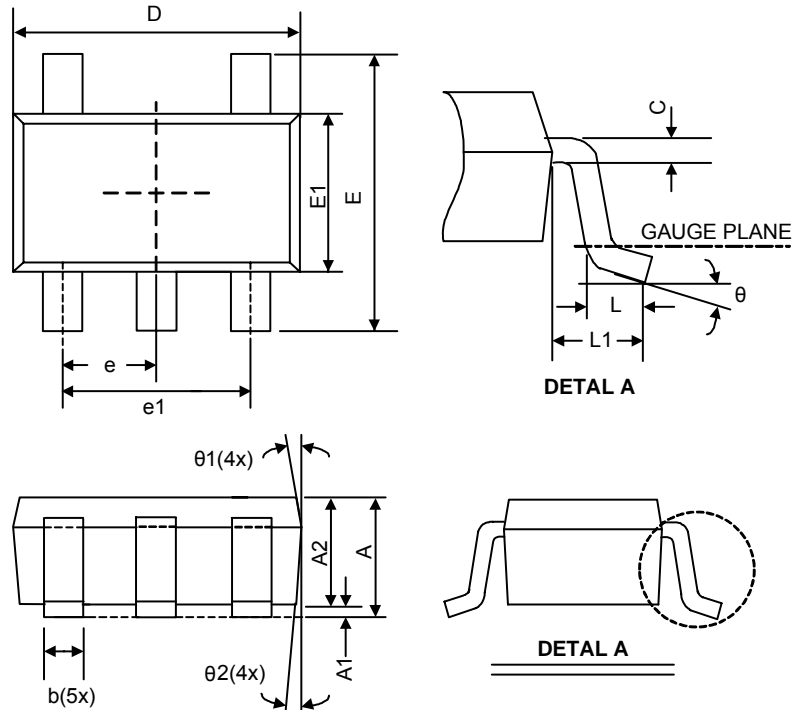
❖ Package Outlines

(1) SOP-8L



Symbol	Dimensions In Millimeters			Dimensions In Inches		
	Min.	Nom.	Max.	Min.	Nom.	Max.
A	1.40	1.60	1.75	0.055	0.063	0.069
A1	0.10	-	0.25	0.040	-	0.100
A2	1.30	1.45	1.50	0.051	0.057	0.059
C	0.19	0.20	0.25	0.0075	0.008	0.010
D	4.80	4.90	5.00	0.189	0.193	0.197
E	3.80	3.90	4.00	0.150	0.154	0.157
H	5.79	5.99	6.20	0.228	0.236	0.244
L	0.38	0.71	1.27	0.015	0.028	0.050
b	0.33	0.41	0.51	0.013	0.016	0.020
e	1.27 TYP			0.050 TYP		
y	-	-	0.10	-	-	0.004
θ	0°	-	8°	0°	-	8°

(2) SOT23-5L



Symbol	Dimensions in Millimeters			Dimensions in Inches		
	Min.	Nom.	Max.	Min.	Nom.	Max.
A	1.05	-	1.35	0.041	-	0.053
A1	0.05	-	0.15	0.002	-	0.006
A2	1.00	1.10	1.20	0.039	0.043	0.047
b	0.30	-	0.50	0.012	-	0.020
C	0.08	-	0.22	0.003	-	0.009
D	2.80	2.90	3.00	0.110	0.114	0.118
E1	1.50	1.60	1.70	0.059	0.063	0.067
E	2.60	2.80	3.00	0.102	0.110	0.118
L	0.30	-	0.60	0.012	-	0.024
L1	0.50	0.60	0.70	0.020	0.024	0.028
e1	1.80	1.90	2.00	0.071	0.075	0.079
e	0.85	0.95	1.05	0.033	0.037	0.041
theta	0°	4°	8°	0°	4°	8°
theta1	5°	10°	15°	5°	10°	15°
theta2	5°	10°	15°	5°	10°	15°