

## PWM Control 3A Step-Down Converter

### ❖ GENERAL DESCRIPTION

AX3111 consists of step-down switching regulator with PWM control. These devise include a reference voltage source, oscillation circuit, error amplifier, internal PMOS and etc.

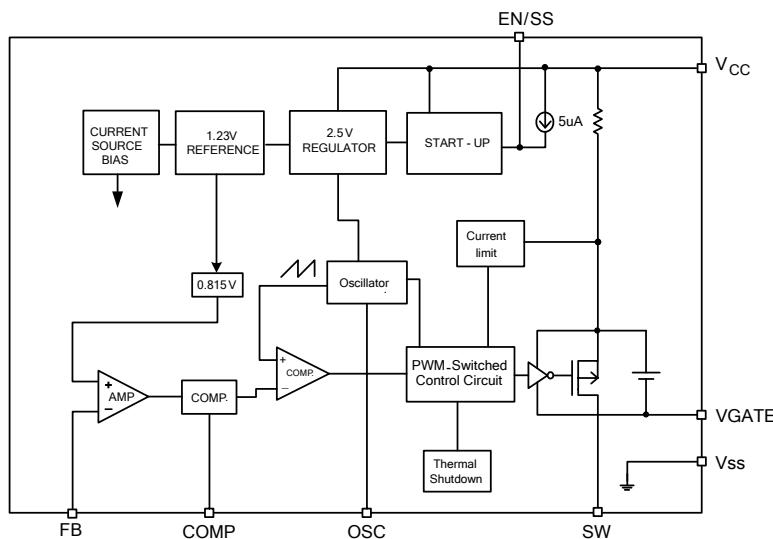
AX3111 provides low-ripple power, high efficiency, and excellent transient characteristics. The PWM control circuit is able to the duty ratio linearly form 0 up to 100%. This converter is build out soft start function that prevents overshoot and inrush current at startup. An over current protect function and short circuit protect function are built inside, and when OCP or SCP happens, the operation frequency will be reduced. The operating frequency is decided by outside resistance. An external compensation is easily to system stable; the low ESR output capacitor can be used.

With the addition of an internal P-channel Power MOS, a coil, capacitors, and a diode connected externally, these ICs can function as step-down switching regulators. They serve as ideal power supply units for portable devices when coupled with the SOP-8L with exposed pad and DFN-10L packages, providing such outstanding features as low current consumption. Since this converter can accommodate an input voltage up to 40V, it is also suitable for the operation via an AC adapter.

### ❖ FEATURES

- Input voltage : 8V to 40V
- Output voltage :  $V_{FB}$  to 38V
- Duty ratio : 0% to 100% PWM control
- Oscillation frequency range is 50K~380KHz by outside resistance setting
- Enable with Soft-Start function
- Current Limit, Short Circuit Protect (SCP) and Thermal Shutdown protection
- Built-in internal SW P-channel MOS.
- SOP-8L-EP and DFN-10L Pb-Free packages.

### ❖ Block Diagram



### ❖ PIN ASSIGNMENT

The packages of AX3111 are SOP-8L-EP and DFN-10L; the pin assignment is given by:

( Top View )		Name	Description
EN/SS	1	OSC	ON/OFF Shutdown and Soft-start pin
VSS	2	COMP	GND pin
VGATE	3	FB	Driver GATE clamping pin. The pin must connect a 1uF capacitor to VCC
VCC	4	SW	IC power supply pin
SOP8L-EP		SW	Switch pin. Connect external inductor & diode here.
EN/SS	1	OSC	Frequency Set Pin. The pin connect a resistance to GND.
VSS	2	COMP	
VGATE	3	FB	
VCC	4	SW	
VCC	5	SW	
DFN-10L (3*3)			

## ❖ ORDER/MARKING INFORMATION

Order Information	
<b>AX3111 X X</b>	
Package Type ES: SOP-8L-EP Z1: DFN-10L(3*3)	Packing Blank : Tube A : Taping
Top Marking (SOP-8L-EP)	Top Marking (DFN-10L)
Logo $\leftarrow$ <b>AX</b> 3111 $\rightarrow$ Part number X X X X X $\rightarrow$ ID code: internal WW: 01~52 Year: 08 = 2008	3111 $\rightarrow$ Part Number X X X X X $\rightarrow$ ID code: internal WW: 01~52 Year: 08 = 2008

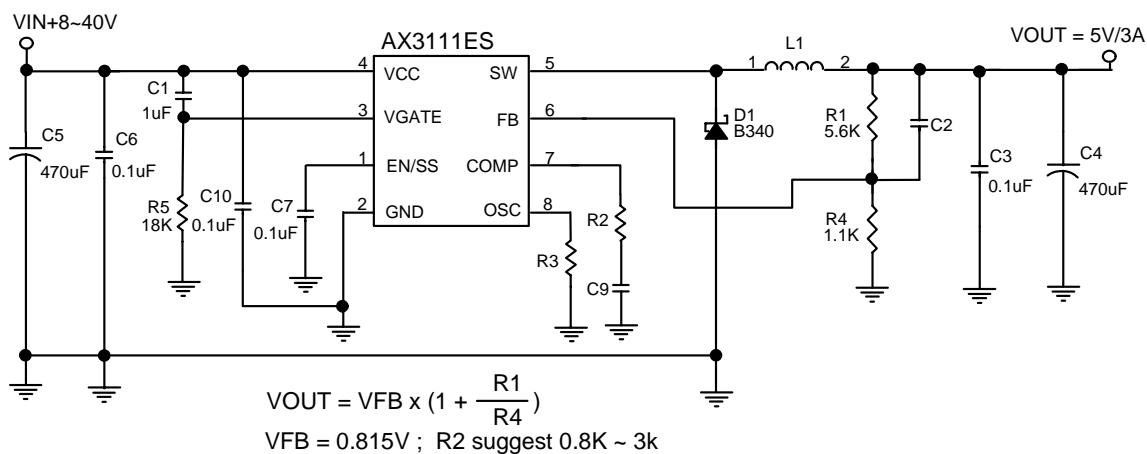
## ❖ Absolute Maximum Ratings (at Ta=25°C)

Characteristics	Symbol	Rating	Unit
VCC Pin Voltage	V <sub>CC</sub>	V <sub>SS</sub> - 0.3 to V <sub>SS</sub> + 45	V
Feedback Pin Voltage	V <sub>FB</sub>	V <sub>SS</sub> - 0.3 to 6	V
EN/SS Pin Voltage	V <sub>EN/SS</sub>	V <sub>SS</sub> - 0.3 to 6	V
OSC Pin Voltage	V <sub>OSC</sub>	V <sub>SS</sub> - 0.3 to 3	V
COMP Pin Voltage	V <sub>COMP</sub>	V <sub>SS</sub> - 0.3 to 6	V
VGATE Pin Voltage	V <sub>GATE</sub>	V <sub>SS</sub> - 0.3 to V <sub>CC</sub>	V
Switch Pin Voltage	V <sub>SW</sub>	V <sub>SS</sub> - 0.3 to V <sub>CC</sub> + 0.3	V
Power Dissipation	PD	Internally limited	mW
Storage Temperature Range	T <sub>ST</sub>	-65 to +150	°C
Operating Junction Temperature Range	T <sub>OJP</sub>	-40 to +125	°C
Operating Supply Voltage	V <sub>OP</sub>	8 to 40	V
Thermal Resistance from Junction to case	θ <sub>JC</sub>	SOP8=15, DFN10=15	°C/W
Thermal Resistance from Junction to ambient	θ <sub>JA</sub>	SOP8=40, DFN10=45	°C/W

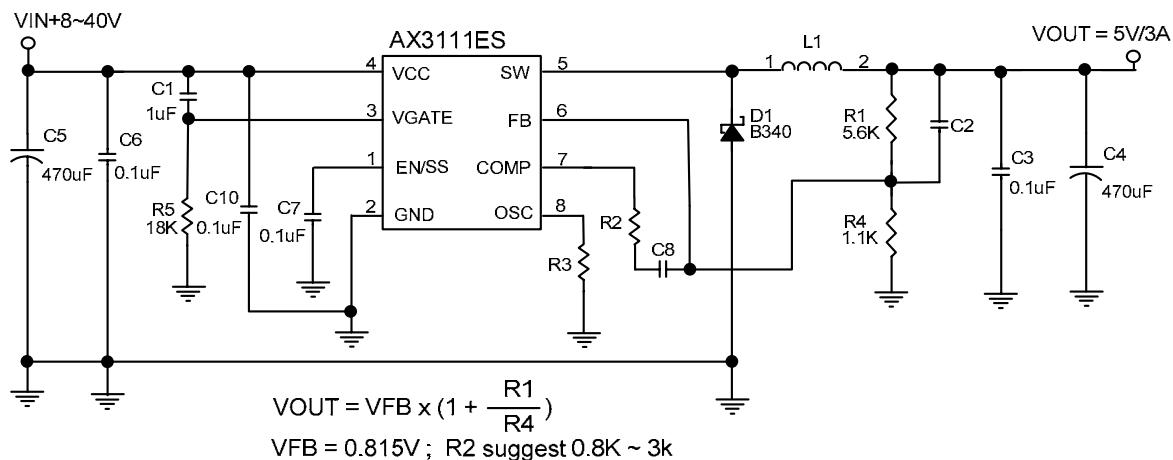
Note : θ<sub>JA</sub> is measured with the PCB copper area(need connect to Exposed Pad) of approximately 1.5 in<sup>2</sup>(Multi-layer).

**❖ Electrical Characteristics ( $V_{CC} = 12V$ ,  $T_a=25^\circ C$ , unless otherwise specified)**

Characteristics	Symbol	Conditions	Min	Typ	Max	Units
Feedback Voltage	$V_{FB}$	$V_{CC} = 10V \sim 30V$ $I_{OUT} = 0$ to $2A$ $T_j = -20^\circ C \sim 125^\circ C$	0.800	0.815	0.830	V
Quiescent Current	$I_{CCQ}$	$V_{FB}=1.2V$ force driver off	-	3	6	mA
Feedback Bias Current	$I_{FB}$	$I_{OUT}=0.1A$	-	0.1	0.5	uA
Shutdown Supply Current	$I_{SD}$	$V_{EN/SS} = 0V$	-	150	300	uA
Switch Current	$I_{SW}$		3.5	-	-	A
Adjustable frequency range	$F_{osc}$		50	-	380	KHz
Short frequency	$F_{osc1}$	$V_{CC} = 10V \sim 30V$	45	50	55	KHz
EN/SS Pin Shutdown Logic input threshold voltage	$V_{ENL}$		-	-	0.8	V
EN/SS Pull high Current	$I_{EN/SS}$	$V_{EN/SS}=0V$	-	5	-	uA
Internal MOSFET $R_{DS(on)}$	$R_{DS(on)}$	$V_{CC}=12V$ , $V_{FB}=0V$	-	130	180	mΩ
Efficiency	$EFFI$	$V_{CC} = 12V$ , $V_{OUT} = 5V$ , $I_{OUT} = 2A$	-	91	-	%
		$V_{CC} = 30V$ , $V_{OUT} = 5V$ , $I_{OUT} = 2A$	-	87	-	%

**❖ Application Circuit**
**(1) Frequency = 50K~250KHz**


(2) Frequency = 250K~380KHz



Compensation Table							
COUT ESR Range	Frequency (Hz)	VIN Range	L1	R2 ( $\Omega$ )	C9	C8	C2
30m~80m $\Omega$	50K~120K	8~40V	68u~100u	470	10n	Open	8.2n
	120K~180K		47u~68u	1K	4.7n	Open	3.3n
	180K~250K		33u~47u	short	1.8n	Open	330p
	250K~380K		22u~33u	270	Open	3.3n	1n
80m~300m $\Omega$	50K~120K	8~40V	68u~100u	330	10n	Open	8.2n
	120K~180K		47u~68u	1K	4.7n	Open	3.3n
	180K~250K		33u~47u	270	3.3n	Open	1n
	250K~380K		22u~33u	270	Open	3.3n	1n

## ❖ Function Descriptions

### EN/SS

This pin can be supplied shutdown or soft start function. It is inside pull high function. For normal application, the pin must be connected a capacitor to ground. There is a 5uA current to charge this capacitor, vary the different capacitor value to control soft start time. Allow the switching regulator circuit to be shutdown pulling this pin below a 0.8V threshold voltage; the shutdown supply current is approximately 150uA.

### OSC

External frequency set pin. The pin connects a resistance (R3) to reduce system frequency. This converter's frequency can be set from 50K to 380KHz, please refer the below table to set frequency.

T=Room Temperature					
R3 ( $\Omega$ )	10M	360K	180K	100K	52K
Frequency (Hz)	52K	120K	180K	250K	380K

### COMP

Compensation pin. For EL output capacitor application, the COMP pin connects R2 and C8 to ground for all condition; please refer the compensation table.

## ❖ Application Information

### Setting the Output Voltage

Application circuit item shows the basic application circuit with adjustable output version. The external resistor sets the output voltage according to the following equation:

$$V_{out} = 0.815V \times \left(1 + \frac{R1}{R4}\right)$$

Table 1 Resistor select for output voltage setting

V <sub>OUT</sub>	R4	R1
5V	1.1K	5.6K
3.3V	2.7K	8.2K
2.5V	1.3K	2.7K
1.8V	1.5K	1.8K
1.5V	1.3K	1.1K
1.2V	1K	0.47K

### Inductor Selection

For most designs, the different frequency can be reducing the inductor value; The AX3111 is suggested 22 $\mu$ H to 100 $\mu$ H for 380K to 50KHz frequencies. Please refer the below table to design.

L1 recommend value (V <sub>IN</sub> =8~40V, V <sub>OUT</sub> =5V, I <sub>OUT</sub> =3A)				
Frequency (Hz)	50K~120K	120K~180K	180K~250K	250K~380K
L1 Value (H)	68u~100u	47u~68u	33u~47u	22u~33u

Where is inductor Ripple Current. Large value inductors lower ripple current and small value inductors result in high ripple currents. Choose inductor ripple current approximately 20% of the maximum load current 3A,  $\Delta I_L = 0.6A$ . The DC current rating of the inductor should be at least equal to the maximum load current plus half the ripple current to prevent core saturation ( $3A + 0.3A$ ).

### Input Capacitor Selection

This capacitor should be located close to the IC using short leads and the voltage rating should be approximately 1.5 times the maximum input voltage. The RMS current rating requirement for the input capacitor of a buck regulator is approximately 1/2 the DC load current. A low ESR input capacitor sized for maximum RMS current must be used. A  $470\mu F$  low ESR capacitor for most applications is sufficient.

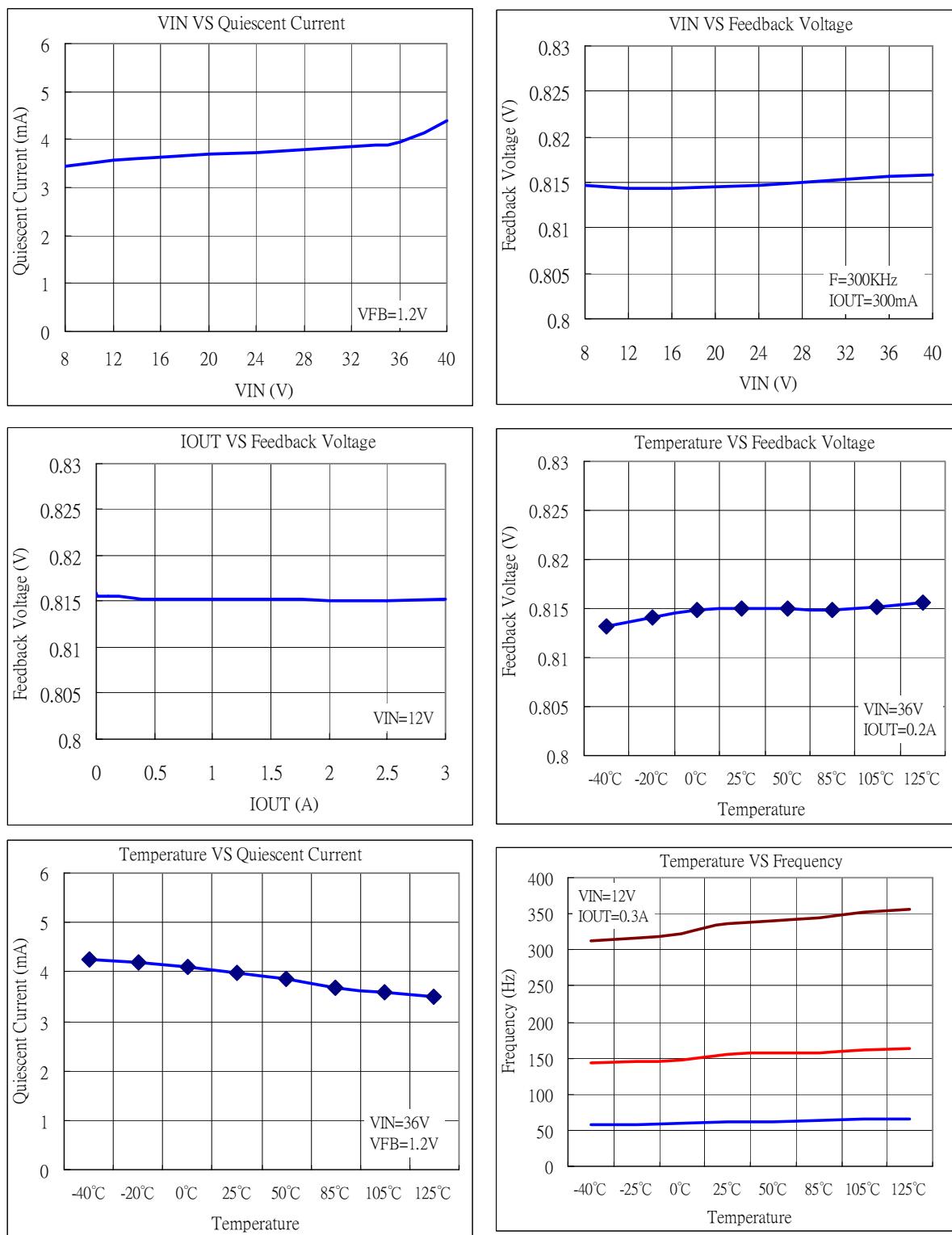
### Output Capacitor Selection

The output capacitor is required to filter the output and provide regulator loop stability. The important capacitor parameters are; the 100 KHz Equivalent Series Resistance (ESR), the RMS ripples current rating, voltage rating, and capacitance value. For the output capacitor, the ESR value is the most important parameter. The ESR can be calculated from the following formula.

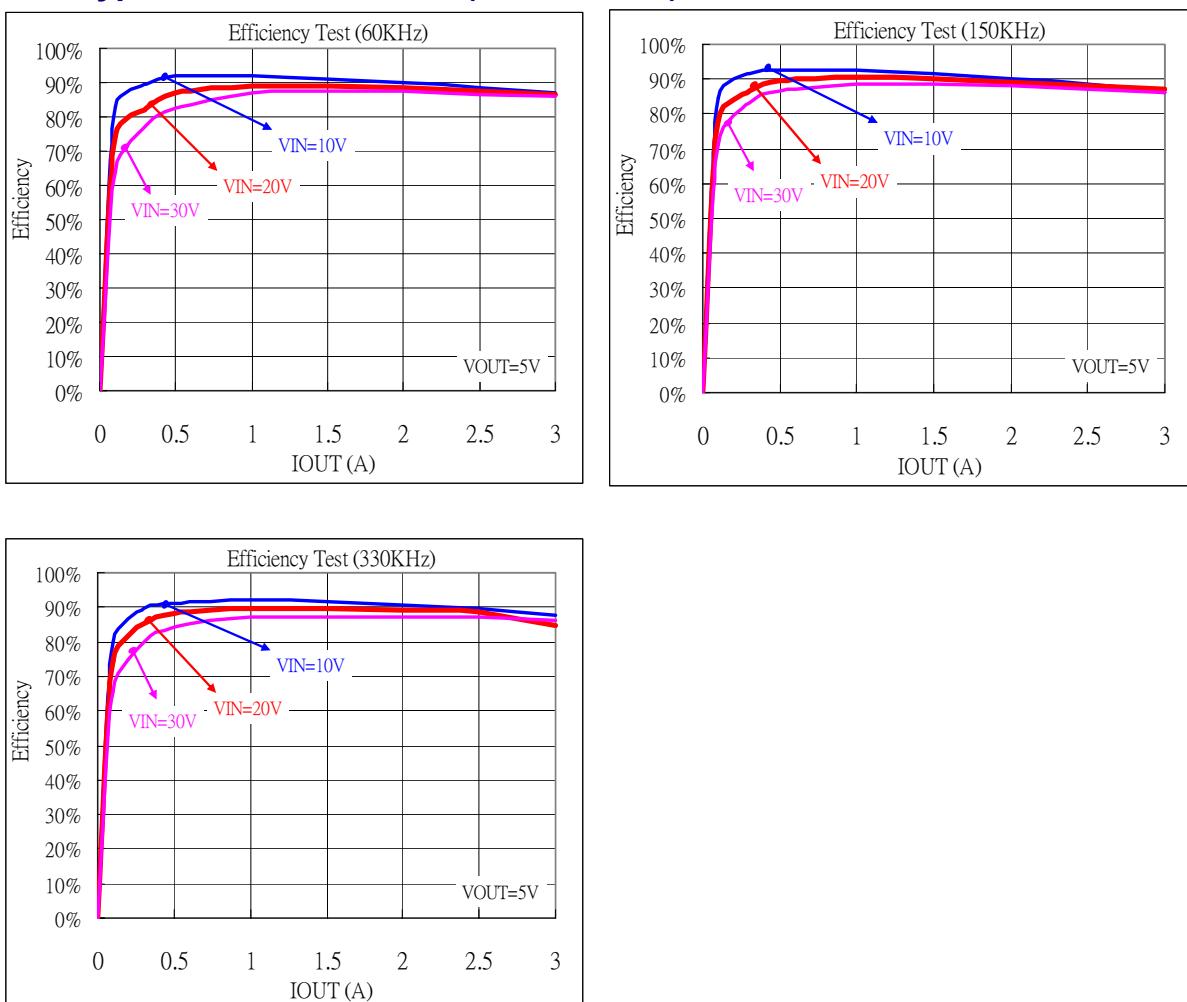
$$V_{RIPPLE} = \Delta I_L \times ESR = 0.6A \times 80m\Omega = 48mV$$

An aluminum electrolytic capacitor's ESR value is related to the capacitance and its voltage rating. In most case, higher voltage electrolytic capacitors have lower ESR values. Most of the time, capacitors with much higher voltage ratings may be needed to provide the low ESR values required for low output ripple voltage. It is recommended to replace this low ESR capacitor by using a  $470\mu F$  low ESR values  $< 80m\Omega$ .

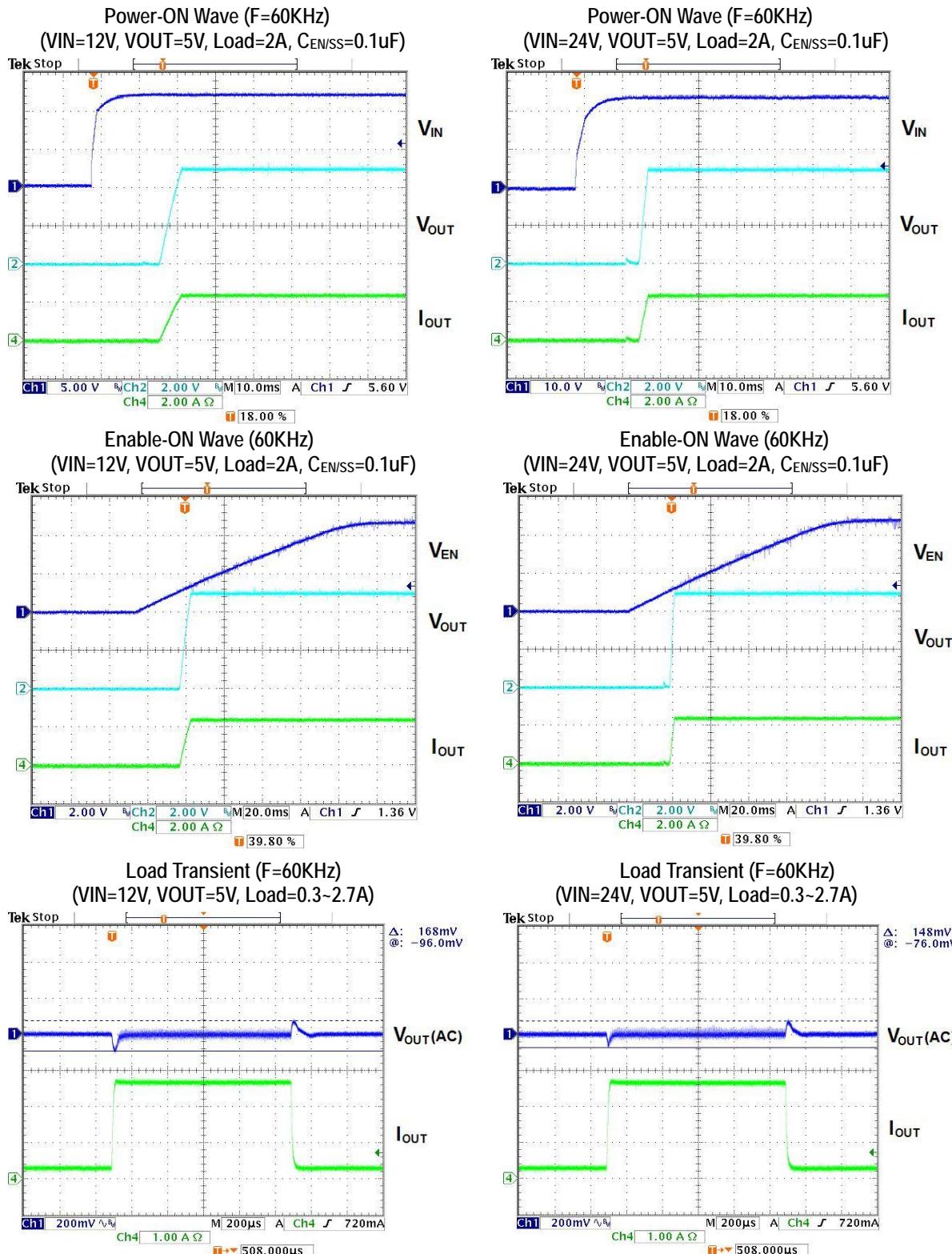
### ❖ Typical Characteristics



### ❖ Typical Characteristics (Continuous)

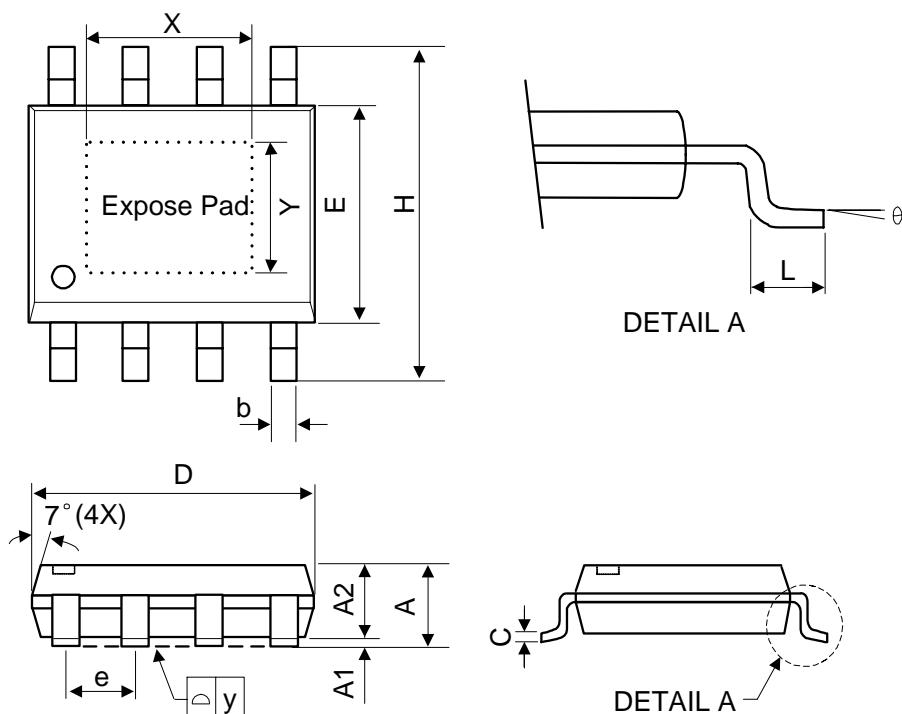


### ◆ Typical Characteristics (Continuous)

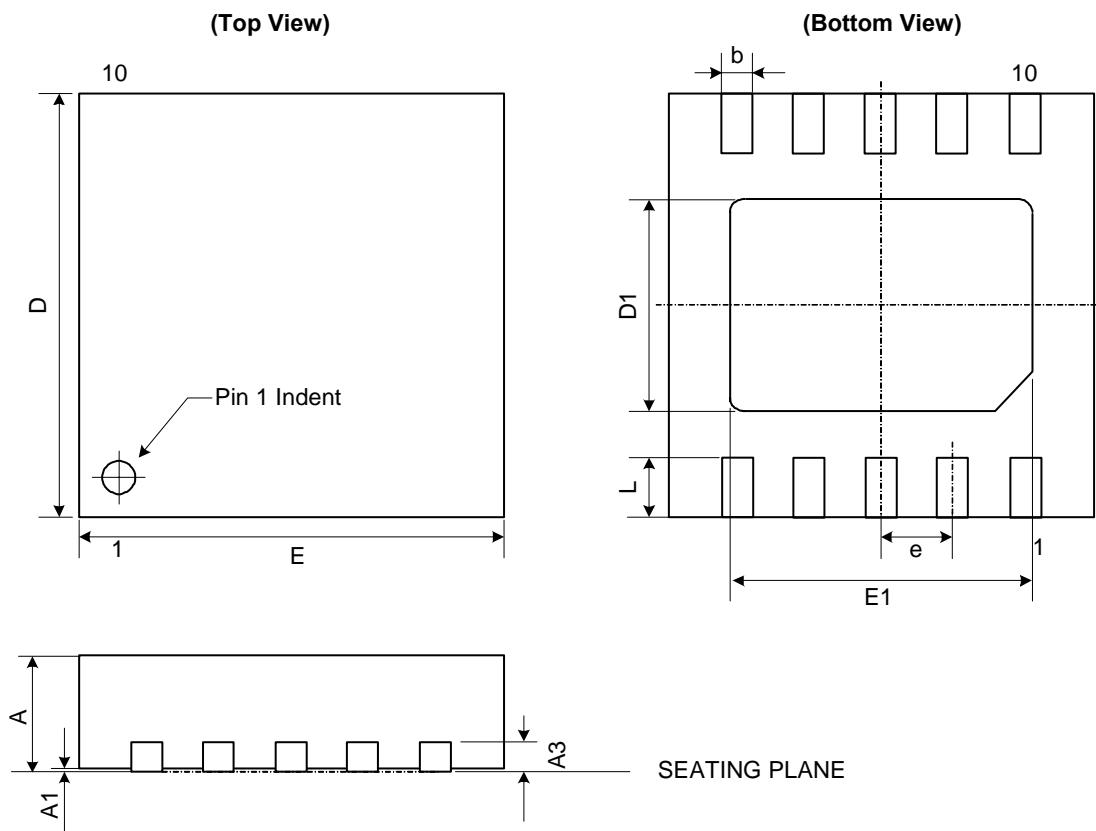


## ❖ Package Outlines

### (1) SOP-8L-EP



Symbol	Dimensions In Millimeters			Dimensions In Inches		
	Min.	Nom.	Max.	Min.	Nom.	Max.
A	1.40	1.60	1.75	0.055	0.063	0.069
A1	0.10	-	0.25	0.040	-	0.100
A2	1.30	1.45	1.50	0.051	0.057	0.059
C	0.19	0.20	0.25	0.0075	0.008	0.010
D	4.80	4.90	5.00	0.189	0.193	0.197
E	3.80	3.90	4.00	0.150	0.154	0.157
H	5.79	5.99	6.20	0.228	0.236	0.244
L	0.38	0.71	1.27	0.015	0.028	0.050
b	0.33	0.41	0.51	0.013	0.016	0.020
e	1.27 TYP			0.050 TYP		
y	-	-	0.10	-	-	0.004
X	-	2.34	-	-	0.092	-
Y	-	2.34	-	-	0.092	-
θ	0°	-	80	0°	-	80

**(2) DFN-10L (3\*3\*0.8mm)**


Symbol	Dimensions In Millimeters			Dimensions In Inches		
	Min.	Nom.	Max.	Min.	Nom.	Max.
A	0.70	0.75	0.80	0.027	0.029	0.032
A1	0.00	0.02	0.05	0.000	0.001	0.002
A3	0.20 REF.			0.008 REF.		
b	0.18	0.25	0.30	0.007	0.010	0.012
D	2.90	3.00	3.10	0.114	0.118	0.122
D1	1.45	1.60	1.75	0.057	0.063	0.069
E	2.90	3.00	3.10	0.114	0.118	0.122
E1	2.20	2.45	2.70	0.087	0.096	0.106
L	0.30	0.40	0.50	0.012	0.016	0.020
e	0.50 BSC.			0.020 BSC.		