

#### **Descriptions**

The DW8502 is an instant On/Off LED driver for high power LED applications. At DW8502 output stage, one regulated current port is designed to provide a uniform and constant current sink for driving LEDs within a large range of V<sub>F</sub> variations. DW8502 easily provides users a consistent current source. User may adjust the output current from up to 2.5A through an external resistor ,R<sub>S</sub>, which gives users flexibility in controlling the light intensity of LEDs. In addition, users can precisely adjust LED brightness from 0% to 100% via output enable (EN) with Pulse Width Modulation. DW8502 also guarantees that LEDs can be cascaded to maximum 40V at the output port.

#### Features

- Constant output current invariant to supply and load voltage change
- 5V to 40V supply voltage
- Up to 2.5A adjustable regulated output current
- Built-in thermal derating circuit
- Available PWM dimming control
- · Output current adjusted through an external resistor
- TO-263 Package

#### **Ordering Information**

Device	Marking	Package	Operating Temp
DW8502	DW8502 XXXXXX YWW	TO-263	-35 <sup>0</sup> C ~ +85 <sup>0</sup> C

### **Package Information**



Package	Size		
TO-263-5L	10.16x15.35x4.57(mm)		

### **Applications**

- LED light bulbs
- Signage and decorative LED lighting
- · General lighting of flat panel displays
- RGB backlighting LED driver
- Current stabilizer with DC/DC or AC/DC
- Automotive lighting
- General purpose constant current source

## Typical Application Circuit





# DW8502 High Power LED Driver

## **Pin Connection**



#### VDD RS GND EN OUT

# **Pin Description**

Pin	Name	Description			
1	VDD	Supply voltage input			
2	RS	Output current set input. Connect a resistor from RS to GND to set the LED bias current			
3	GND	Ground			
4	EN	Output stage enable control pin. High enable the OUT pin. Dimming control is possible by PWM (Pulse width modulation). Typically, It operates under 30kHz.			
5	OUT	Output pin. Sink current is decided by the current on $R_{SET}$ connected to $RS$			
6	HS	Heat sink, normally connected GND			



### **Absolute Maximum Ratings**

Characteri	Symbol	Value	Unit	
Supply voltage	V <sub>DD</sub>	41	V	
Enable voltage	V <sub>EN</sub>	41	V	
Output voltage	V <sub>OUT</sub>	23	V	
Reference voltage	Vrs	5	V	
Package thermal resistance	TO-263-5L	$\Theta_{JA}$	TBD	°C/W
Operating temperature	T <sub>OPR</sub>	-35~+85	Ĉ	
Storage Temperature	T <sub>STG</sub>	-55~+150	Ĵ	

**Note** 1. θja is measured in the convection at Ta=25 °C on a high effective thermal conductivity test board(4 Layers, 2S2P) of JEDEC 51-7 thermal measurement standard.

# **Recommended Operation Conditions**

Characteristics	Symbol	Min.	Тур.	Max.	Unit
Supply voltage	V <sub>DD</sub>	5	-	40	V
Enable voltage	V <sub>EN</sub>	-	-	40	V
Output sink current	Ι <sub>ουτ</sub>	-	-	2.5	A

High Power LED Driver

## **Electrical Characteristics**

V<sub>DD</sub> = 24V, EN= 0~24V, Ta = -35 ℃~+85 ℃, unless otherwise specified. Typical values are at T<sub>A</sub>=+25 ℃

Characteristics	Symbol	Conditions	Min.	Тур.	Max.	Unit
Input supply voltage	V <sub>DD</sub>		5	-	40	V
Output linearity voltage	$V_{\text{OUT}\_\text{LINE}}$	$V_{DD}$ =24V, I <sub>SET</sub> = 300mA,	-	-	3	V
Output current	I <sub>OUT</sub>		-	-	1.5	А
	$I_{QON}$	EN = 24V	0.8	1	1.5	mA
Quiescent Current	$I_{Q_{OFF}}$	EN = 0V	85	120	250	uA
EN input leakage current	I <sub>EN_LIK</sub>		30	45	60	uA
Input high voltage	V <sub>IH</sub>		2	-	-	V
Input low voltage	V <sub>IL</sub>		-	-	0.8	V
LED output drop-out voltage	V <sub>DROP</sub>	V <sub>DD</sub> =40V , I <sub>SET</sub> =300mA	240	270	300	V
Thermal derating	T <sub>D</sub>		-	140	-	Ĵ
Thermal derating hysteresis	T <sub>DHYS</sub>		-	15	-	Ĉ
Rset Voltage	$V_{\text{SET}}$		0.59	0.61	0.63	V
		<b>2Κ</b> Ω	285	300	315	mA
	Rost	1ΚΩ	570	600	630	mA
	INSET	<b>600</b> Ω	950	1000	1050	mA
		<b>400</b> Ω	1425	1500	1575	mA

Note2 : Output dropout voltage : 90% x IOUT

DW8502 High Power LED Driver

# **Block Diagram**

DONGWOON

ANATECH



### **Application notice**

#### **Setting Output Current**

lout [mA] = (610[mV]/Rset[ohm])X 1000

<b>Rset(k</b> Ω)	Iout(mA)		
4	152		
2	300		
1.22	500		
0.68	900		
0.61	1000		
0.406	1500		
0.305	2000		

#### **Power sequence**

There is an electrostatic diode between VDD and EN. When dimming control, It must input EN signal after inputs VDD. ( $t \ge 1ms$ ) If not use Dimming control, EN connect to VDD.



13 ~

DW8502 **High Power LED Driver** 

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**Typical Applications** 

 $O^{V_{DD}}$ 

\* LED VF = 3.3V, 1W Power LED



12

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C1=2.2ul

Figure 6. R<sub>VALLAST</sub> Application

Figure 5. PWM Typical Application



# Package Dimension (TO-263-5L 10.16 x 15.35 x 4.57)

