
RICOH**Li-ION/POLYMER 1CELL PROTECTOR**

R5478Nxxxx SERIES

OUTLINE

The R5478Nxxxx Series are high voltage CMOS-based protection ICs for over-charge/discharge of rechargeable one-cell Lithium-ion (Li+) / Lithium polymer excess load current, further include a short circuit protector for preventing large external short circuit current and Excess charge/discharge-current.

Each of these ICs is composed of three voltage detectors, a reference unit, a delay circuit, a short circuit protector, an oscillator, a counter, and a logic circuit. When the over-charge voltage crosses the detector threshold from a low value to a high value, the output of C_{OUT} pin switches to low level after internal fixed delay time. The conditions to release over-charge detector are different by mask options. In terms of so called "Latch function" version, after detecting over-charge, the detector can be reset and the output of C_{OUT} becomes "H" when a kind of load is connected to V_{DD} after a charger is disconnected from the battery pack, and the cell voltage becomes lower than over-charge detector threshold. If a charger is continue to be connected to the battery pack, even the cell voltage becomes lower than over-charge detector threshold, over-charge state is not released. On the other hand, in terms of so called, "Released by voltage level" version, after detecting over-charge, when the cell voltage reaches the released voltage from over-charge, the output of C_{OUT} becomes "H"

The output of D_{OUT} pin, the output of Over-discharge detector and Excess discharge-current detector, switches to "L" level after internally fixed delay time, when discharged voltage crosses the detector threshold from a high value to a value lower than V_{DET2}.

The conditions to release over-discharge detector are also different by mask options. In terms of so called "Latch function" version, after detecting over-discharge voltage, connect a charger to the battery pack, and when the battery supply voltage becomes higher than over-discharge detector threshold, VD2 is released and the voltage of D_{OUT} pin becomes "H" level. On the other hand, in terms of so called "Released by voltage level" version, in case that the charger is not connected, when the cell voltage becomes equal released voltage from over-discharge, over-discharge detector is released. In case that a charger is connected, and when the cell voltage becomes the released voltage from over-discharge, the over-discharge detector is released.

An excess discharge-current and short circuit state can be sensed and cut off through the built in excess current detector, VD3, with D_{OUT} being enabled to low level. Once after detecting excess discharge-current or short circuit, the VD3 is released and D_{OUT} level switches to high by detaching a battery pack from a load system.

After detecting over-discharge, supply current will be kept extremely low by halting internal circuits' operation.

When the output of C_{OUT} is "H", if V- pin level is set at V_{SS}-2V, the delay time of detector can be shortened. Especially, the delay time of over-charge detector can be reduced into approximately 1/57. Therefore, testing time of protector circuit board can be reduced. Output type of C_{OUT} and D_{OUT} are CMOS. 6-pin, SOT-23-6 is available.

FEATURES

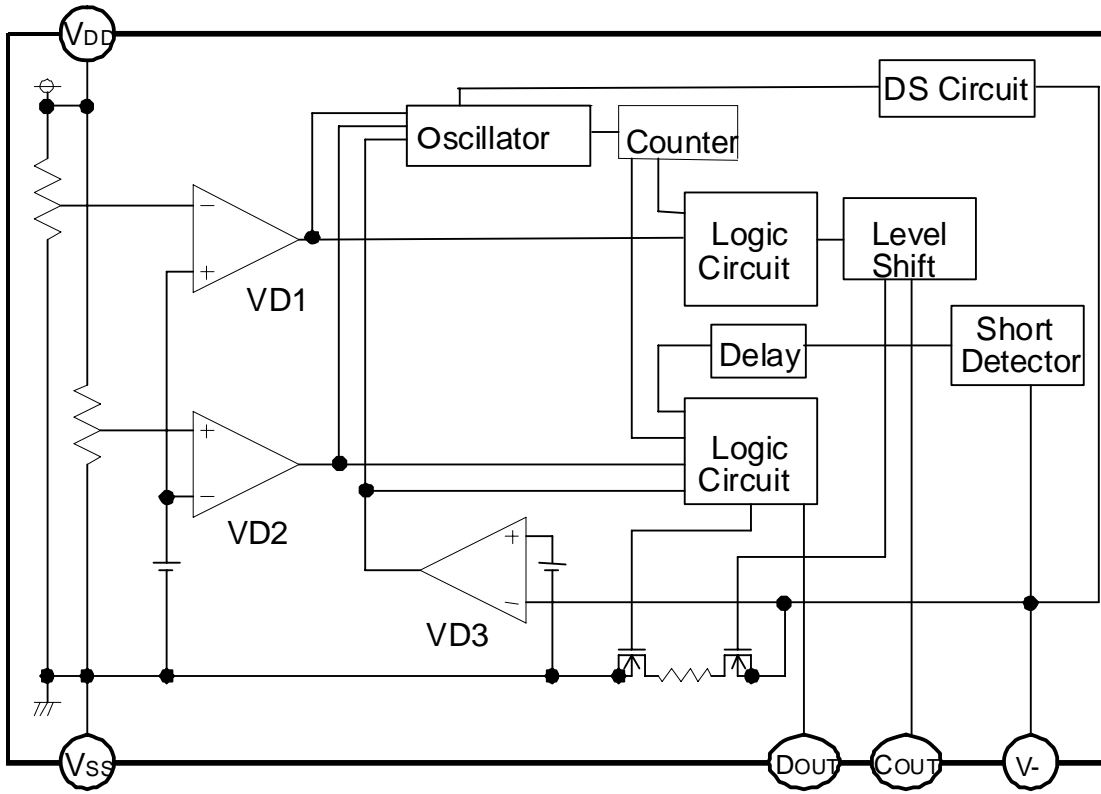
- Manufactured with High Voltage Tolerant Process ...Absolute Maximum Rating 30V
- Low supply currentSupply current (At normal mode) Typ. 3.0μA
 - C version: (detecting over-discharge) Max. 0.1μA
 - D/J version: (detecting over-discharge) Typ. 1.2μA/Max. 2.0μA
- High accuracy detector thresholdOver-charge detector (Topt=25°C) ±25mV
 - (Topt=-5 to 55°C) ±30mV
 - Over-discharge detector ±2.5%
 - Excess discharge-current detector ±15mV
- Variety of detector thresholdOver-charge detector threshold
 - C version: 4.2V-4.5V step of 0.005V
 - D/J version: 3.65V
 - Over-discharge detector threshold 2.1V-3.0V step of 0.100V
 - Excess discharge-current threshold 0.05V-0.20V step of 0.005V
 - Short Detector Threshold Fixed at 0.8V
- Internal fixed Output delay time.....Over-charge detector Output Delay 1s
 - (Select among the options) Over-discharge detector Output Delay 20ms
 - Excess discharge-current detector Output Delay 12ms(C Type) / 6ms(E Type)
 - Short Circuit detector Output Delay 300μs(C Type) / 200μs(E Type)
- Output Delay Time Shortening Function..At COUT is “H”, if V- level is set at -2V, the Output Delay time of all items except short-circuit can be reduced. (Delay Time for over-charge becomes about 1/57 of normal state.)
- Conditions for release over-charge detector With Latch function (C version)
 - With Released voltage ((D/J version)
- Conditions for Release over-discharge detector Latch Type: (C version)
 - Released Voltage (D/J version)
- Ultra Small packageSOT-23-6

APPLICATIONS

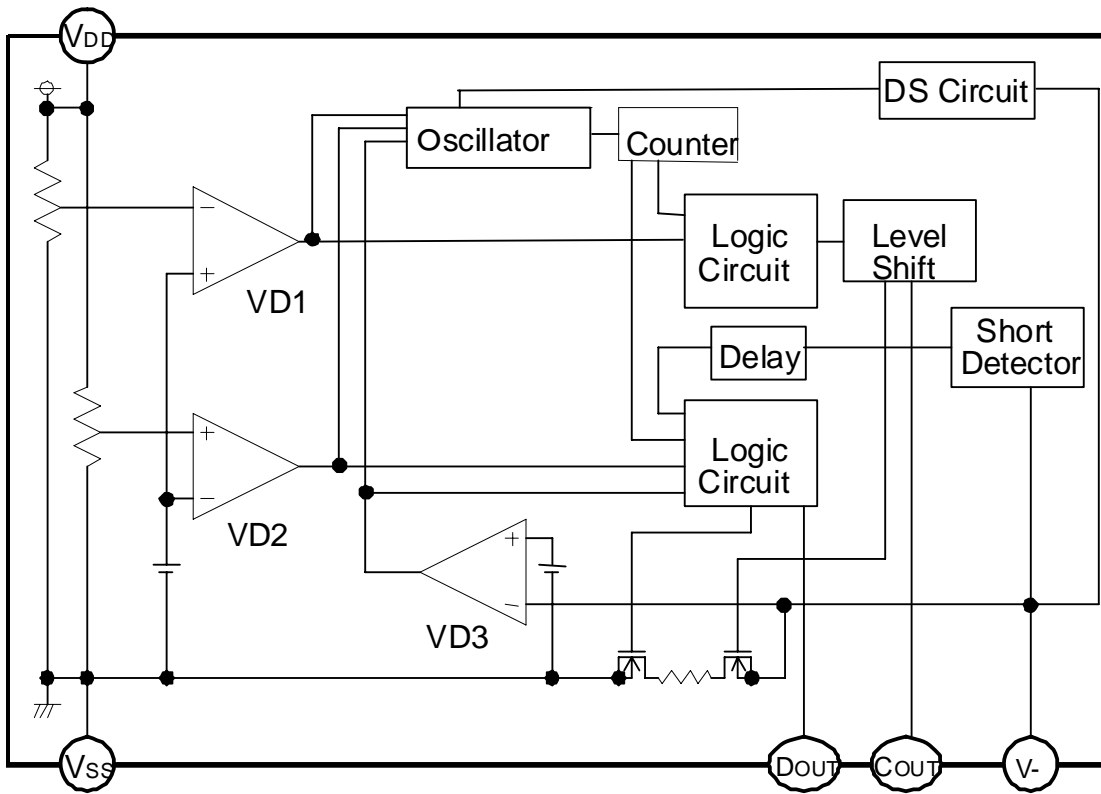
- Li+ / Li Polymer protector of over-charge, over-discharge, excess-current for battery pack
- High precision protectors for cell-phones and any other gadgets using on board Li+ / Li Polymer battery

BLOCK DIAGRAMS

C version



D/J version



SELECTION GUIDE

In the R5478xxxxx Series, three of the input threshold for over-charge, over-discharge, and excess discharge current detectors can be designated.

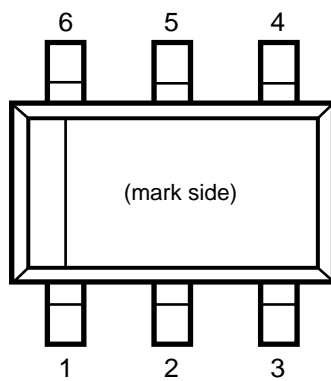
Part Number is designated as follows:

R5478N xxx x x-xx ←Part Number
 ↑ ↑ ↑ ↑ ↑
 a b c d e

Code	Contents
a	Package Type N: SOT-23-6
b	Serial Number for the R5478 Series designating input four threshold for over-charge, over-discharge, and excess discharge-current detectors.
c	Designation of Output delay option of over-charge, and excess discharge-current.
d	Designation of version symbols C version: With Latch function after Over-charge and Over-discharge D version: Released by voltage level from Over-charge and Over-discharge J version: Released by voltage level from Over-charge and Over-discharge Over-charge level = 3.65V Only
e	Taping Type: TR (refer to Taping Specification)

PIN CONFIGURATIONS

SOT-23-6



PIN DESCRIPTION

Pin No.	Symbol	Description
1	D _{OUT}	Output of over-discharge detection, CMOS output
2	V-	Pin for charger negative input
3	C _{OUT}	Output of over-charge detection, CMOS output
4	NC	No Connection
5	V _{DD}	Power supply pin, the substrate voltage level of the IC.
6	V _{SS}	Vss pin. Ground pin for the IC

ABSOLUTE MAXIMUM RATINGSV_{SS}=0V

Symbol	Item	Ratings	Unit
V _{DD}	Supply voltage	-0.3 to 12	V
V-	Input Voltage V- pin	V _{DD} -30 to V _{DD} +0.3	V
V _{C_{OUT}}	Output voltage C _{OUT} pin	V _{DD} -30 to V _{DD} +0.3	V
V _{D_{OUT}}	D _{OUT} pin	V _{SS} -0.3 to V _{DD} +0.3	V
P _D	Power dissipation	150	mW
T _{opt}	Operating temperature range	-40 to 85	°C
T _{stg}	Storage temperature range	-55 to 125	°C

*Note: Exposure to the condition exceeded Absolute Maximum Ratings may cause the permanent damages and affects the reliability and safety of both device and systems using the device.

The functional operations cannot be guaranteed beyond specified values in the Recommended conditions.

ELECTRICAL CHARACTERISTICSUnless otherwise specified, T_{opt}=25°C

*R5478N1xxEC

Symbol	Item	Conditions	Min.	Typ.	Max.	Unit
V _{DD1}	Operating input voltage	Voltage defined as V _{DD} -V _{SS}	1.5		5.0	V
V _{st}	Minimum operating Voltage for 0V charging ^{*Note 1}	Voltage defined as V _{DD} -V ₋ , V _{DD} -V _{SS} =0V			1.8	V
V _{DET1}	Over-charge threshold	Detect rising edge of supply voltage R1=330Ω R1=330Ω (T _{opt} =-5 to 55°C) ^{*Note3}	V _{DET1} -0.025 V _{DET1} -0.030	V _{DET1} V _{DET1}	V _{DET1} +0.025 V _{DET1} +0.030	V V
t _{VDET1}	Output delay of over-charge	V _{DD} =3.6V to 4.4V	0.7	1.0	1.3	s
t _{VREL1}	Output delay of release from over-charge	V _{DD} =4.5V to 3.6V	11	16	21	ms
V _{DET2}	Over-discharge threshold	Detect falling edge of supply voltage	V _{DET2} ×0.975	V _{DET2}	V _{DET2} ×1.025	V
t _{VDET2}	Output delay of over-discharge	V _{DD} =3.6V to 2.2V	14	20	26	ms
t _{VREL2}	Output delay of release from over-discharge	V _{DD} =3V V ₋ =3V to 0V	0.7	1.2	1.7	ms
V _{DET3}	Excess discharge-current threshold	Detect rising edge of 'V-' pin voltage	V _{DET3} -0.015	V _{DET3}	V _{DET3} +0.015	V
t _{VDET3}	Output delay of excess discharge-current	V _{DD} =3.0V, V ₋ =0V to 0.4V	4	6	8	ms
t _{VREL3}	Output delay of release from excess discharge-current	V _{DD} =3.0V, V ₋ =3V to 0V	0.7	1.2	1.7	ms
V _{short}	Short protection voltage	V _{DD} =3.0V	0.50	0.75	0.95	V
T _{short}	Output Delay of Short protection	V _{DD} =3.0V, V ₋ =0V to 3V	150	200	300	μs
R _{short}	Reset resistance for Excess discharge-current protection	V _{DD} =3.6V, V ₋ =1V	25	50	75	kΩ
V _{DS}	Delay Shortening Mode input voltage	V _{DD} =4.4V	-2.6	-2.0	-1.4	V
V _{OL1}	Nch ON voltage of COUT	I _{ol} =50μA, V _{DD} =4.5V		0.4	0.5	V
V _{OH1}	Pch ON voltage of COUT	I _{oh} =-50μA, V _{DD} =3.9V	3.4	3.7		V
V _{OL2}	Nch ON voltage of DOUT	I _{ol} =50μA, V _{DD} =2.0V		0.2	0.5	V
V _{OH2}	Pch ON voltage of DOUT	I _{oh} =-50μA, V _{DD} =3.9V	3.4	3.7		V
I _{DD}	Supply current	V _{DD} =3.9V, V ₋ =0V		3.0	7.0	μA
I _S	Standby current	V _{DD} =2.0V			0.1	μA

*Note: We compensate for this characteristic related to temperature by laser-trim, however, this specification is guaranteed by design, not production tested.

*R5478N1xxCD

Unless otherwise specified, T_{opt}=25°C

Symbol	Item	Conditions	Min.	Typ.	Max.	Unit
VDD1	Operating input voltage	Voltage defined as VDD-VSS	1.5		5.0	V
Vst	Minimum operating Voltage for 0V charging ^{*Note 1}	Voltage defined as VDD-V-, VDD-VSS=0V			1.8	V
VDET1	Over-charge threshold	Detect rising edge of supply voltage R1=330Ω R1=330Ω (T _{opt} =-5 to 55°C) ^{*Note3}	VDET1-0.025 VDET1-0.030	VDET1 VDET1	VDET1+0.025 VDET1+0.030	V V
VREL1	Released Voltage from Over-charge	Detect falling edge of supply voltage	VREL1-0.05	VREL1	VREL1+0.05	V
tVDET1	Output delay of over-charge	VDD=3.6V to 4.4V	0.7	1.0	1.3	s
tVREL1	Output delay of release from over-charge	VDD=4.5V to 3.6V	11	16	21	ms
VDET2	Over-discharge threshold	Detect falling edge of supply voltage	VDET2×0.975	VDET2	VDET2×1.025	V
VREL2	Released Voltage from Over-discharge	Detect rising edge of supply voltage	VREL2×0.975	VREL2	VREL2×1.025	V
tVDET2	Output delay of over-discharge	VDD=3.6V to 2.2V	14	20	26	ms
tVREL2	Output delay of release from over-discharge	VDD=3V V-=3V to 0V	0.7	1.2	1.7	ms
VDET3	Excess discharge-current threshold	Detect rising edge of 'V-' pin voltage	VDET3-0.015	VDET3	VDET3+0.015	V
tVDET3	Output delay of excess discharge-current	VDD=3.0V, V-=0V to 0.4V	8	12	16	ms
tVREL3	Output delay of release from excess discharge-current	VDD=3.0V, V-=3V to 0V	0.7	1.2	1.7	ms
Vshort	Short protection voltage	VDD=3.0V	0.50	0.75	0.95	V
Tshort	Output Delay of Short protection	VDD=3.0V, V-=0V to 3V	230	300	500	μs
Rshort	Reset resistance for Excess discharge-current protection	VDD=3.6V, V-=1V	25	50	75	kΩ
VDS	Delay Shortening Mode input voltage	VDD=4.4V	-2.6	-2.0	-1.4	V
VOL1	Nch ON voltage of COUT	I _{ol} =50μA, VDD=4.5V		0.4	0.5	V
VOH1	Pch ON voltage of COUT	I _{oh} =-50μA, VDD=3.9V	3.4	3.7		V
VOL2	Nch ON voltage of DOUT	I _{ol} =50μA, VDD=2.0V		0.2	0.5	V
VOH2	Pch ON voltage of DOUT	I _{oh} =-50μA, VDD=3.9V	3.4	3.7		V
IDD	Supply current	VDD=3.9V, V-=0V		3.0	7.0	μA
IS	Standby current	VDD=2.0V		1.2	2.0	μA

*Note: We compensate for this characteristic related to temperature by laser-trim, however, this specification is guaranteed by design, not production tested.

*R5478N1xxCJ

Unless otherwise specified, T_{opt}=25°C

Symbol	Item	Conditions	Min.	Typ.	Max.	Unit
V _{DD1}	Operating input voltage	Voltage defined as V _{DD} -V _{SS}	1.5		5.0	V
V _{st}	Minimum operating Voltage for 0V charging ^{*Note 1}	Voltage defined as V _{DD} -V ₋ , V _{DD} -V _{SS} =0V			1.8	V
V _{DET1}	Over-charge threshold	Detect rising edge of supply voltage R1=330Ω R1=330Ω (T _{opt} =-5 to 55°C) ^{*Note3}	V _{DET1} -0.025 V _{DET1} -0.030	V _{DET1} V _{DET1}	V _{DET1} +0.025 V _{DET1} +0.030	V V
V _{REL1}	Released Voltage from Over-charge	Detect falling edge of supply voltage	V _{REL1} -0.05	V _{REL1}	V _{REL1} +0.05	V
tV _{DET1}	Output delay of over-charge	V _{DD} =3.6V to 4.4V	0.7	1.0	1.3	s
tV _{REL1}	Output delay of release from over-charge	V _{DD} =4.5V to 3.6V	11	16	21	ms
V _{DET2}	Over-discharge threshold	Detect falling edge of supply voltage	V _{DET2} ×0.975	V _{DET2}	V _{DET2} ×1.025	V
V _{REL2}	Released Voltage from Over-discharge	Detect rising edge of supply voltage	V _{REL2} ×0.975	V _{REL2}	V _{REL2} ×1.025	V
tV _{DET2}	Output delay of over-discharge	V _{DD} =3.6V to 2.2V	14	20	26	ms
tV _{REL2}	Output delay of release from over-discharge	V _{DD} =3V V ₋ =3V to 0V	0.7	1.2	1.7	ms
V _{DET3}	Excess discharge-current threshold	Detect rising edge of 'V-' pin voltage	V _{DET3} -0.015	V _{DET3}	V _{DET3} +0.015	V
tV _{DET3}	Output delay of excess discharge-current	V _{DD} =3.0V, V ₋ =0V to 0.4V	8	12	16	ms
tV _{REL3}	Output delay of release from excess discharge-current	V _{DD} =3.0V, V ₋ =3V to 0V	0.7	1.2	1.7	ms
V _{short}	Short protection voltage	V _{DD} =3.0V	0.50	0.75	0.95	V
T _{short}	Output Delay of Short protection	V _{DD} =3.0V, V ₋ =0V to 3V	230	300	500	μs
R _{short}	Reset resistance for Excess discharge-current protection	V _{DD} =3.6V, V ₋ =1V	25	50	75	kΩ
V _{DS}	Delay Shortening Mode input voltage	V _{DD} =4.4V	-2.6	-2.0	-1.4	V
V _{OL1}	Nch ON voltage of COUT	I _{ol} =50μA, V _{DD} =4.5V		0.4	0.5	V
V _{OH1}	Pch ON voltage of COUT	I _{oh} =-50μA, V _{DD} =3.9V	3.4	3.7		V
V _{OL2}	Nch ON voltage of DOUT	I _{ol} =50μA, V _{DD} =2.0V		0.2	0.5	V
V _{OH2}	Pch ON voltage of DOUT	I _{oh} =-50μA, V _{DD} =3.9V	3.4	3.7		V
I _{DD}	Supply current	V _{DD} =3.9V, V ₋ =0V		3.0	7.0	μA
I _S	Standby current	V _{DD} =2.0V		1.2	2.0	μA

*Note: We compensate for this characteristic related to temperature by laser-trim, however, this specification is guaranteed by design, not production tested.

OPERATION

• VD1 / Over-Charge Detector

The VD1 monitors V_{DD} pin voltage while charge the battery pack. When the V_{DD} voltage crosses over-charge detector threshold V_{DET1} from a low value to a value higher than the V_{DET1} , the VD1 can detect over-charge and an external charge control Nch MOSFET turn off with C_{OUT} pin being at "L" level.

In terms of B /C version, to reset the VD1 making the C_{OUT} pin level to "H" again after detecting over-charge, in such conditions that a time when the V_{DD} voltage is down to a level lower than over-charge voltage, by connecting a kind of loading to V_{DD} after disconnecting a charger from the battery pack. Output voltage of C_{OUT} pin becomes "H", and it makes an external Nch MOSFET turn on, and charge cycle is available. In other words, once over-charge is detected, even if the supply voltage becomes low enough, if a charger is continuously connected to the battery pack, recharge is not possible. Therefore this over-charge detector has no hysteresis. To judge whether or not load is connected, the built-in excess-discharge current detector is used. In other words, by connecting some load, V_{-} pin voltage becomes equal or more than excess-discharge current detector threshold, and reset the over-charge detecting state.

In terms of D version, after detecting over-charge, if V_{DD} pin voltage is lower than released from over-charge, even if a charger is connected, over-charge detector is released. Further, in case that V_{DD} pin level is lower than detector threshold and higher than released voltage from over-charge, if a charger is removed and some load is connected, over-charge detector is also released.

After detecting over-charge with the V_{DD} voltage of higher than V_{DET1} , connecting system load to the battery pack makes load current allowable through parasitic diode of external charge control FET.

The C_{OUT} level would be "H" when the V_{DD} level is down to a level below the V_{DET1} by continuous drawing of load current.

Internal fixed output delay times for over-charge detection and release from over-charge exist. Even when the V_{DD} pin level becomes equal or higher level than V_{DET1} if the V_{DD} voltage would be back to a level lower than the V_{DET1} within a time period of the output delay time, VD1 would not output a signal for turning off the charge control FET. Besides, after detecting over-charge, while the V_{DD} is lower than over-charge detector, even if a charger is removed and a load is connected, if the voltage is recovered within output delay time of release from over-charge, over-charge state is not released.

A level shifter incorporated in a buffer driver for the C_{OUT} pin makes the "L" level of C_{OUT} pin to the V_{-} pin voltage and the "H" level of C_{OUT} pin is set to V_{DD} voltage with CMOS buffer.

• VD2 / Over-Discharge Detector

The VD2 is monitoring a V_{DD} pin voltage. When the V_{DD} voltage crosses the over-discharge detector threshold V_{DET2} from a high value to a value lower than the V_{DET2} , the VD2 can detect an over-discharge and the external discharge control Nch MOSFET turns off with the D_{OUT} pin being at "L" level.

In terms of C version, to reset the VD2 with the D_{OUT} pin level being "H" again after detecting over discharge, it is necessary to connect a charger to the battery pack. When the V_{DD} voltage stays under over-discharge detector threshold V_{DET2} , charge-current can flow through parasitic diode of an external discharge control MOSFET, then after the V_{DD} voltage comes up to a value larger than V_{DET2} , then, D_{OUT} becomes "H" and discharging process would be able to advance through ON state MOSFET for discharge control.

Connecting a charger to the battery pack makes the D_{OUT} level being "H" instantaneously when the V_{DD} voltage is higher than V_{DET2} .

In terms of B/D version, released operation by connecting a charger is same as C version. However, without a charger, if V_{DD} pin voltage is equal or more than the released voltage from over-discharge, D_{OUT} pin becomes "H" immediately.

When a cell voltage equals to zero, if the voltage of a charger is equal or more than 0V-charge minimum voltage (V_{st}), C_{OUT} pin becomes "H" and a system is allowable to charge.

An output delay time for over-discharge detection is fixed internally. When the V_{DD} level is down to a equal or lower level than V_{DET2} if the V_{DD} voltage would be back to a level higher than the V_{DET2} within a time period of the output delay time, VD2 would not output a signal for turning off the discharge control FET. Output delay time for release from over-discharge is also set.

After detecting over-discharge by VD2, supply current would be reduced and be into standby by halting unnecessary circuits and consumption current of IC itself is made as small as possible.

The output type of D_{OUT} pin is CMOS having "H" level of V_{DD} and "L" level of V_{SS} .

- **VD3 /Excess discharge-current Detector, Short Circuit Protector**

Both of the excess current detector and short circuit protection can work when the both of control FETs are in "ON" state.

When the V- pin voltage is up to a value between the short protection voltage V_{short} / V_{DD} and excess discharge-current threshold V_{DET3} , VD3 operates and further soaring of V- pin voltage higher than V_{short} makes the short circuit protector enabled. This leads the external discharge control Nch MOSFET turns off with the D_{OUT} pin being at "L" level.

An output delay time for the excess discharge-current detector is internally fixed.

A quick recovery of V- pin level from a value between V_{short} and V_{DET3} within the delay time keeps the discharge control FET staying "H" state. Output delay time for Release from excess discharge-current detection is also set.

When the short circuit protector is enabled, the D_{OUT} would be "L" and the delay time is also set.

The V - pin has a built-in pull-down resistor to the V_{SS} pin, that is, the resistance to release from excess-discharge current.

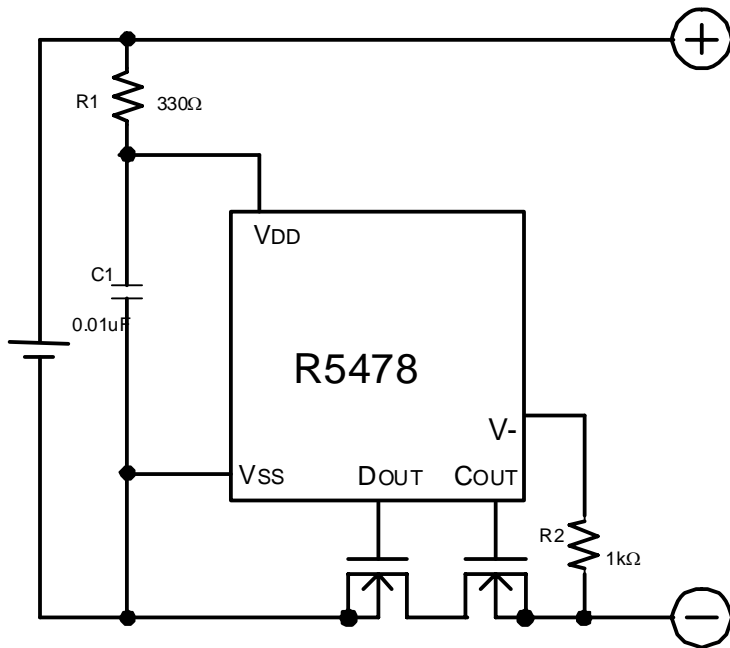
After an excess discharge-current or short circuit protection is detected, removing a cause of excess discharge-current or external short circuit makes an external discharge control FET to an "ON" state automatically with the V- pin level being down to the V_{SS} level through built-in pulled down resistor. The reset resistor of excess discharge-current is off at normal state. Only when detecting excess discharge-current or short circuit, the resistor is on.

Output delay time of excess discharge-current is set shorter than the delay time for over-discharge detector. Therefore, if V_{DD} voltage would be lower than V_{DET2} at the same time as the excess discharge-current is detected, the R5478xxxxxx is at excess discharge-current detection mode. By disconnecting a load, VD3 is automatically released from excess discharge-current.

- **DS (Delay Shorten) function**

Output delay time of over-charge, over-discharge, and release from those detecting modes can be shorter than those setting value by forcing equal or less than the delay shortening mode voltage to V- pin.

TYPICAL APPLICATION



APPLICATION HINTS

R1 and C1 will stabilize a supply voltage to the R5478xxxxxx. A recommended R1 value is less than 1kΩ.

A larger value of R1 leads higher detection voltage, makes some errors because of some conduction current may flow in the R5478xxxxxx. To stabilize the operation, the value of C1 should be equal 0.01μF.

R1 and R2 can operate also as parts for current limit circuit against reverse charge or applying a charger with excess charging voltage to the R5478xxxxxx, battery pack. Small value of R1 and R2 may cause over-power consumption rating of power dissipation of the R5478xxxxxx. Thus, the total value of 'R1+R2' should be equal or more than 1kΩ.

On the other hand, if large value of R2 is set, release from over-discharge by connecting a charger might not be possible. Recommended R2 value is equal or less than 10kΩ.

The typical application circuit diagram is just an example. This circuit performance largely depends on the PCB layout and external components. In the actual application, fully evaluation is necessary.

Over-voltage and the over current beyond the absolute maximum rating should not be forced to the protection IC and external components.

We are making our continuous effort to improve the quality and reliability of our products, but semiconductor products are likely to fail with certain probability. In order prevent any injury to persons or damages to property resulting from such failure, customers should be careful enough to incorporate safety measures in their design, such as redundancy feature, fire-containment feature and fail-safe feature.

We do not assume any liability or responsibility for any loss or damage arising from misuse or inappropriate use of the products.

R5478N Series

Code	Vdet1	Vrel1	Vdet2	Vrel2	Vdet3	tVdet1	tVdet2	tVdet3	tShort	0V Charge	Package
	(V)	(V)	(V)	(V)	(V)	(s)	(ms)	(ms)	(μ s)		
R5478N128EC	4.280	-	2.800	-	0.050	1	20	6	200	OK	SOT-23-6
R5478N176EC	4.280	-	2.300	-	0.130	1	20	6	200	OK	SOT-23-6
R5478N101CD	4.250	4.050	2.500	3.000	0.200	1	20	12	300	OK	SOT-23-6
R5478N102CD	4.350	4.150	2.500	3.000	0.200	1	20	12	300	OK	SOT-23-6
R5478N110CD	4.280	4.080	2.300	3.000	0.125	1	20	12	300	OK	SOT-23-6
R5478N173CD	4.200	4.100	2.800	2.900	0.100	1	20	12	300	OK	SOT-23-6
R5478N204CD	4.200	3.900	2.500	3.000	0.200	1	20	12	300	OK	SOT-23-6
R5478N205CD	4.200	3.900	3.000	3.200	0.200	1	20	12	300	OK	SOT-23-6
R5478N215CD	4.200	4.100	2.500	3.000	0.100	1	20	12	300	OK	SOT-23-6
R5478N216CD	4.170	4.170	2.500	3.000	0.100	1	20	12	300	OK	SOT-23-6
R5478N217CD	4.325	4.125	2.300	3.000	0.200	1	20	12	300	OK	SOT-23-6
R5478N218CD	4.250	4.050	2.800	3.000	0.150	1	20	12	300	OK	SOT-23-6
R5478N184CJ	3.650	3.650	2.500	3.000	0.200	1	20	12	300	OK	SOT-23-6

(as of 2010/11/29)

R5478 N 128 E C

Package Type
N : SOT23-6

Function Version

C : Over-Charge = Latch

D : Over-Charge = Auto-Release

J : Over-Charge = Auto-Release

Vdet1=3.65V only

Over-Discharge = Latch

Over-Discharge = Auto Release

Over-Discharge = Auto Release

Delay Time Version

Voltage Version

Code	Return from Over-Charge	Return from Over-Discharge	tVdet1 (s)	tVdet2 (ms)	tVdet3 (ms)	tShort (μ s)
R5478N xxx EC	Latch	Latch	1	20	6	200
R5478N xxx CD	Auto-Release	Auto-Release	1	20	12	300
R5478N xxx CJ	Auto-Release	Auto-Release	1	20	12	300