

#### **Novel Low Cost Green-Power PWM Controller**

#### **Features**

- Low Cost, PWM&PFM&CRM
- Low Start-up Current (about 10μA)
- Low Operating Current (about 2mA)
- Current Mode Operation
- Under Voltage Lockout (UVLO)
- Built-in Synchronized Slope Compensation
- Programmable PWM Frequency
- Leading edge Blanking on Sense input
- Constant output power limiting for

- universal AC input
- Cycle-by-cycle current limiting
- Clamped gate output voltage 16.5V
- Over voltage protect 26.7V
- High-Voltage CMOS Process with ESD
- SOT-23-6L、SOP-8 & DIP-8 Pb-Free Packaging

#### **Applications**

- Switching AC/DC Adaptor
- Battery Charger

- Open Frame Switching Power Supply
- 384X Replacement

#### **General Description**

The TC1208 is a highly integrated low cost current mode PWM controller, which is ideal for small power current mode of offline AC-DC fly-back converter applications. Making use of external resistors, the IC changes the operating frequency and automatically enters the PFM/CRM under light-load/zero-load conditions. This can minimize standby power consumption and achieve green-power functions. With a very low start-up current, the TC1208 could use a large value start-up resistor (1.5Mohm). Built-in synchronized slope compensation enhances the stability of the system and avoids sub-harmonic oscillation. Dynamic peak limiting circuit minimizes output power change caused by delay time of the system over a universal AC input range. Leading

edge blanking circuit on current sense input could remove the signal glitch due to snubber circuit diode reverse recovery and thus greatly reduces the external component count and system cost in the design. Pulse-by-pulse current limiting ensures safe operation even during short-circuit.

The TC1208 offers more protection like OVP (Over Voltage Protection) and OCP (Over current protection). The TC1208's output driver is clamped to maximum 16.5V to protect the power MOSFET. Excellent EMI performance is achieved soft switching control at the totem pole gate driver output. TC1208 is offered in SOT-23-6, SOP-8 and DIP-8 packages.

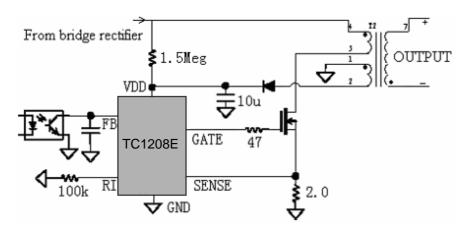
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## **TC12XX Series**

Part Number	PWM Frequency	Package	Description				
			PWM&PFM&CRM 、 Current Mode 、 Slope				
TC1208	External	DIP-8, SOP8	Compensation Leading-edge Blanking Gate Clamped, Over-voltage Protection, Over-current				
	Adjustable	SOT-23-6L					
			Protection、 Soft Drive.				
TC1209	External	DIP-8, SOP8	With All Functions of TC1208 , Gate soft				
101209	Adjustable	SOT-23-6L	Clamped				
	Estamal DID 9 COD9		With All Functions of TC1210、Frequency jitter、				
TC1210	External Adjustable	DIP-8, SOP8 SOT-23-6L	Soft start, Sense Fault Protect, OLP, SCP,				
	Adjustable SO1-23-6L		OTP				

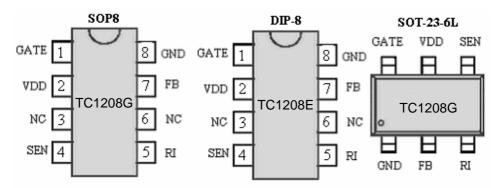
# **TYPICAL APPLICATION**



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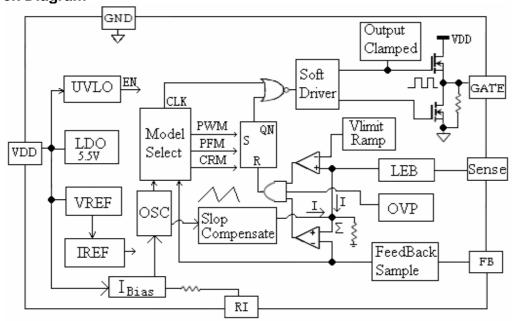
## **Pin Assignment**



# **Pin Descriptions**

Name	Description
GND	GND Pin
FB	Analog Input. Voltage feedback pin (same as the COMP pin in UC384X),
RI	This pin is to program the switching frequency. By connecting a resistor
Ni	to ground to set the switching frequency.
SEN	Current sense pin, connect to sense the MOSFET current.
VDD	Supply voltage pin.
GATE	Gate output to drive the external power MOSFET.

# **Block Diagram**



**Simplified Internal Circuit Architecture** 

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**Absolute Maximum Ratings** 

Symbol	Parameter	Rating	Unit	
$V_{DD}$	Supply voltage Pin Voltage		40	V
I <sub>OVP</sub>	VDD OVP maximal enter curre	ent	20	mA
V <sub>FB</sub>	Input Voltage to FB Pin		-0.3 to 6V	V
V <sub>SEN</sub>	Input Voltage to SEN Pin		-0.3 to 6V	V
P <sub>D</sub>	Power Dissipation	300	mW	
	ESD Capability, HBM Model	2000	V	
	ESD Capability, Machine Mod	ESD Capability, Machine Model		
TL	Lead Temperature	20 second SOT-23-6L	220	
''L	(Soldering) 10 second DIP-8		260	
T <sub>STG</sub>	Storage Temperature Range		-55 to + 150	

# RECOMMENDED OPERATION CONDITION

Symbol	Parameter	Min ~ Max	Unit
VDD	VDD Supply Voltage	11~20	V
RI	RI PIN Resistor Value	58~120	K ohm
T <sub>OA</sub>	Operation Ambient Temperature	-20~85	
Po	Output Power	0~60	W
F <sub>PWM</sub>	Frequency of PWM	48~100	kHz

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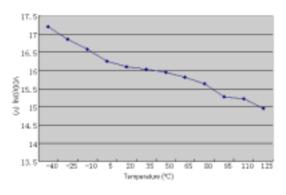
# **Electrical Characteristics (** $Ta=25^{\circ}C$ unless otherwise noted, $V_{DD}=15V$ .)

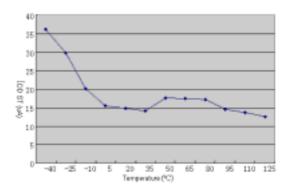
Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit				
Supply V	Supply Voltage (V <sub>DD</sub> Pin)									
I <sub>ST</sub>	Startup Current			10		μΑ				
		V <sub>FB</sub> =0V		2.8		mA				
I <sub>SS</sub>	Operating Current	V <sub>FB</sub> =3V		2.3		mA				
		V <sub>FB</sub> =Open		1.46		mA				
VDD <sub>ON</sub>	Turn-on Threshold Voltage			16.1		V				
VDD <sub>OFF</sub>	Turn-off Threshold Voltage			11.1		V				
VD <sub>CLAMP</sub>	VDD Clamp Voltage	I <sub>VDD</sub> =20mA		26.7		V				
Voltage F	Feedback (FB Pin)									
I <sub>FB</sub>	Short Circuit Current	V <sub>FB</sub> =0V		2.8		mA				
$V_{FB}$	Open Loop Voltage	V <sub>FB</sub> =Open		4.8		V				
I <sub>PFM</sub>	Enter PFM, FB current			0.92		mA				
I <sub>CRM</sub>	Enter CRM, FB current			1.5		mA				
Current S	Sensing (SEN Pin)									
V <sub>TH_L</sub>	Minimum Voltage Lever		0.83	0.87		V				
$V_{TH\_H}$	Maximum Voltage Lever			1.0	1.05	V				
$T_PD$	Delay to Output			300		ns				
R <sub>CS</sub>	Input Impedance			50		ΚΩ				
Oscillato	r (RI Pin)									
Fosc	Normal Frequency	RI=100Kohm	53	58	63	KHz				
F <sub>PFM</sub>	PFM Frequency	RI=100Kohm		11.6		KHZ				
$DC_{MAX}$	Maximum Duty Cycle	RI=100Kohm		75		%				
F <sub>TEMP</sub>	Frequency Temp. Stability	-30-85		5		%				
T <sub>BLANK</sub>	Leading-Edge Blanking Time			300		nS				
GATE Drive Output (GATE Pin)										
V <sub>OL</sub>	Output Low Level	V <sub>DD</sub> =15V, I <sub>O</sub> =20mA			1	V				
V <sub>OH</sub>	Output High Level	V <sub>DD</sub> =15V, I <sub>O</sub> =20mA	8			V				
T <sub>R</sub>	Rising Time	C <sub>L</sub> =1000pF		450		ns				
$T_F$	Falling Time	C <sub>L</sub> =1000pF		130		ns				
$VG_{CLAMP}$	Output Clamp Voltage	VDD=20V		16.5		V				

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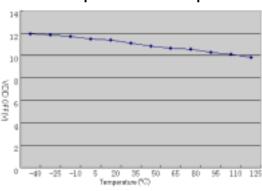


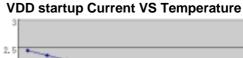
#### **TYPICAL CHARACTERISTICS**

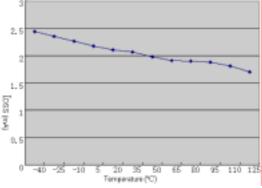




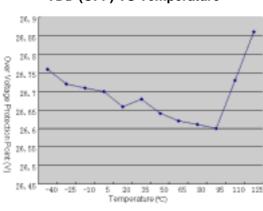
#### **VDD startup Current VS Temperature**



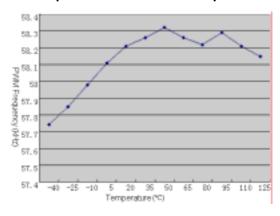




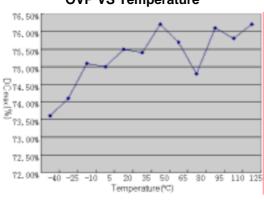
#### **VDD (OFF) VS Temperature**



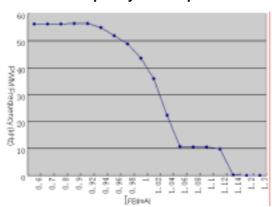
#### **VDD Operation Current VS Temperature**



## **OVP VS Temperature**



**PWM frequency VS Temperature** 

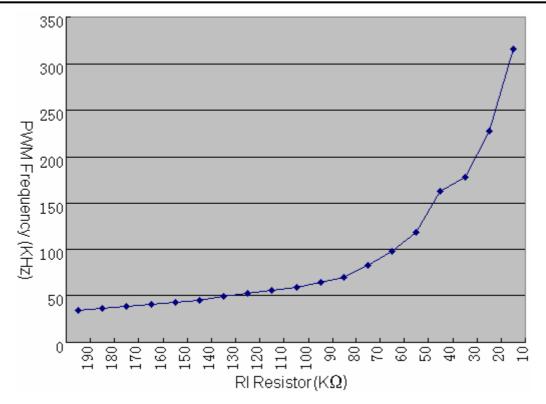


**Duty cycle VS Temperature** 

Fosc VS FB Current

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Fosc VS RI pin resistor

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# OPERATION DESCRIPTION Current Model

Compared to voltage model control, current model control has a current feedback loop. When the voltage of the sense resistor peak current of the primary winding reaches the internal setting value V<sub>TH</sub>, comparator reverse, register reset and power MOSFET cut-off. So that to detect and modulate the peak current cycle by cycle could control the output of the power supply. The current feedback has a good linear modulation rate and a fast input and output dynamic impact avoid the pole that the output filter inductance brings and the second class system descends to first class and so it widens the frequency range and optimizes overload protection and short circuit protection.

# Startup Current and Under Voltage Lockout

The startup current of TC1208 is set to be very low so that a large value startup resistor can therefore be used to minimize the power loss. For AC to DC adaptor with universal input range design, a 1.5 M $\Omega$ , 1/8 W startup resistor and a 10uF/25V VDD hold capacitor could be used.

The turn-on and turn-off threshold of the TC1208 is designed to 16.1V/11.1V. During startup, the hold-up capacitor must be charge to 16.1V through the startup resistor. The hysteresis is implemented to prevent the shutdown from the voltage dip during startup.

#### **Internal Bias and OSC Operation**

A resistor connected between RI pin and GND pin set the internal constant current source to charge or discharge the internal fixed cap. The charge time and discharge time determine the internal clock speed and the switching frequency. Increasing the resistance will reduce the value of the input current and reduce the switching frequency. The relationship between RI pin and PWM switching frequency follows the below equation within the RI allowed range.

$$F_{OSC} = \frac{5800}{RI(K\Omega)}(kHz)$$

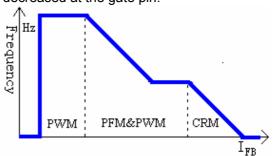
For example, a  $100k\Omega$  resistor RI could generate a 50uA constant current and a 58kHz PWM switching frequency. The

suggested operating frequency range of TC1208 is within 48KHz to 100KHz.

#### **Green Power Operation**

The power dissipation of switching mode power supply is very important in zero load or light load condition. The major dissipation result from conduction loss, switching loss and consume of the control circuit. However, all of them related to the switching frequency. There are many difference topologies has been implemented in different pam. The basic operation theory of all these approaches intended to reduce the switching frequency under light-load or no-load condition.

TC1208's green power function adapts PWM, PFM and CRM combining modulation. When RI resistor is 100k, the PWM frequency is 58kHz in medium or heavy load operation. Through modifying the pulse width, TC1208 could control output voltage. The current of FB pin increases when the load is in light condition and the internal mode controller enters PFM&PWM when the feedback current is over 0.92mA. The operation frequency of oscillator is to descend gradually. The invariable frequency of oscillator is 11.6kHz when the feedback current is over 1.05mA. To decrease the standby consumption of the power supply, PAM-TS introduces the Cycle Reset Mode technology; If the feedback current were over 1.1mA, mode controller of TC1208 would reset internal register all the time and cut off the gate pin, while the output voltage is lower than the set value, it would set register, gate pin operating again. Although the frequency of the internal OSC is invariable, the register would reset some pulses so that the practical frequency is decreased at the gate pin.



**TC1208 Green-Power Function** 

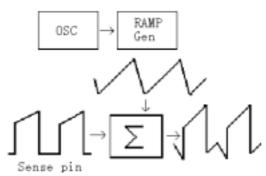
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# Internal Synchronized Slop Compensation

Although there are more advantages of the current mode control than conventional voltage mode control, there are still several drawbacks of peak-sensing current-mode the open Especially converter. instability when it operates in higher than 50% of the duty-cycle. TC1208 is introduced an internal slope compensation adding voltage ramp to the current sense input voltage for PWM generation to solve this problem. It improves the close loop stability greatly at CCM, prevents the sub-harmonic oscillation and thus reduces the output ripple voltage.

$$V_{SLOP} = 0.33 \times \frac{DUTY}{DUTY_{MAX}} = 0.4389 \times DUTY$$



# **Current Sensing & Dynamic peak limiting**

The current flowing by the power MOSFET comes in to being a voltage  $V_{\text{SENSE}}$  on the sense pin cycle by cycle, which compares to the internal reference voltage, controls the reverse of the internal register, limits the peak current IMAX of the primary of the

transformer. The energy 
$$E = \frac{1}{2} \times L \times I_{MAX}^{2}$$

deposited by the transformer. So adjusting the R<sub>SENSE</sub> can set the Max output power of the power supple mode. The current flowing by the power MOSFET has an extra value

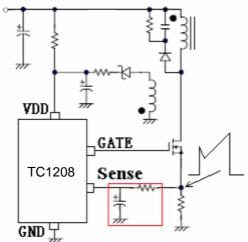
$$\Delta I = \frac{V_{IN}}{L_P} \times T_D$$
 due to the system delay T

that the current detected from the sense pin to power MOSFET cut off in the TC1208 (Among these,  $V_{\text{IN}}$  is the primary winding voltage of the transformer and  $L_{\text{P}}$  is the

primary wind inductance.  $V_{\it IN}$  ranges from 85VAC to 264VAC. To guarantee the output power is a constant for universal input AC voltage, there is a dynamic peak limit circuit to compensate the system delay T that the system delay brings on.

#### Leading-edge Blanking (LEB)

Each time the power MOSFET is switched on, a turn-on spike will inevitably occur at the sense pin, which would disturb the internal signal from the sampling of the R<sub>SENSE</sub>. There is a 300n sec leading edge blanking time built in to avoid the effect of the turn-on spike and the power MOSFET cannot be switched off during this time. So that the conventional external RC filtering on sense input is no longer required.



#### **Over Voltage Protection (OVP)**

There is a 26.7V over-voltage protection circuit in the TC1208 to improve the credibility and extend the life of the pam. The GATE is to shutdown immediately when the voltage of the VDD is over 26.7V and the voltage of VDD is to descend rapidly.

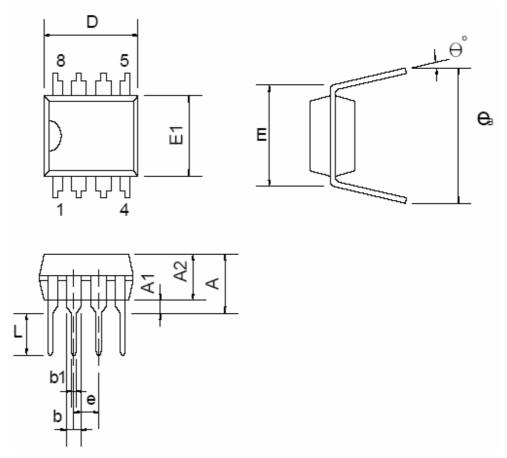
#### Gate Driver & Soft Clamped

TC1208' output designs a totem pole to drive a periphery power MOSFET. The dead time is introduced to minimize the transfixion current when the output is drove. The NMOS is shut off when the other NMOS is turned on. The clamp technology is introduced to protect the periphery power MOSFET from breaking down.

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# PACKAGE DEMENSIONS DIP-8L



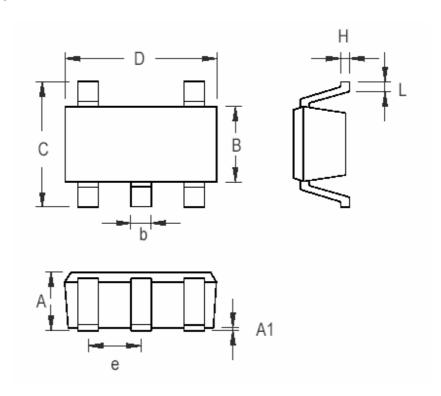
# **Dimensions**

Symbol	Millimeters			Inches		
	Min.	Тур.	Max.	Min.	Тур.	Max.
Α			5.334			0.210
A1	0.381			0.015		
A2	3.175	3.302	3.429	0.125	0.130	0.135
b		1.524			0.060	
b1		0.457			0.018	
D	9.017	9.271	10.160	0.355	0.365	0.400
E		7.620			0.300	
E1	6.223	6.350	6.477	0.245	0.250	0.255
е		2.540			0.100	
L	2.921	3.302	3.810	0.115	0.130	0.150
eB	8.509	9.017	9.525	0.335	0.355	0.375
θ°	0°	7°	15°	0°	7°	15°

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# SOT-23-6L

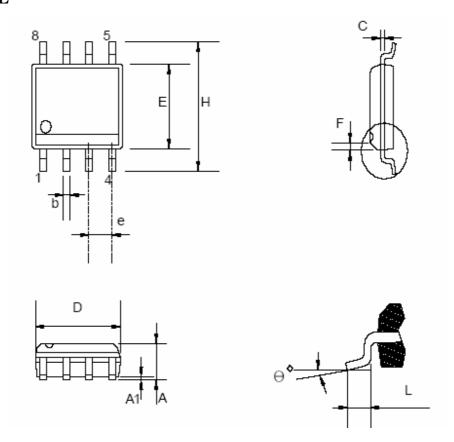


Symbol	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min	Max	Min	Max	
А	0.700	1.000	0.028	0.039	
A1	0.000	0.100	0.000	0.004	
В	1.397	1.803	0.055	0.071	
b	0.300	0.559	0.012	0.022	
С	2.591	3.000	0.102	0.118	
D	2.692	3.099	0.106	0.122	
е	0.838	1.041	0.033	0.041	
Н	0.080	0.254	0.003	0.010	
L	0.300	0.610	0.012	0.024	

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# SOP-8L



# **Dimensions DISCLAIMERS**

Symbol		Millimeter		Inch		
	Min.	Тур.	Max.	Min.	Тур.	Max.
Α	1.346		1.752	0.053		0.069
A1	0.101		0.254	0.004		0.010
b		0.406			0.016	
С		0.203			0.008	
D	4.648		4.978	0.183		0.196
Е	3.810		3.987	0.150		0.157
е	1.016	1.270	1.524	0.040	0.050	0.060
F		0.381X45 °			0.015X45 °	
Н	5.791		6.197	0.228		0.244
L	0.406		1.270	0.016		0.050
θ°	0°		8°	0 °		8 °

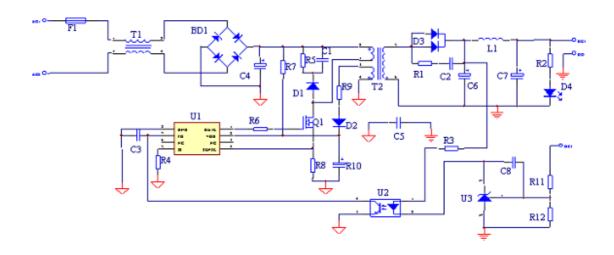
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# **REFERENCE CIRCUIT 1**

# TC1208 drives a periphery Power MOSFET.

# 5V/6A 30W Power Supply



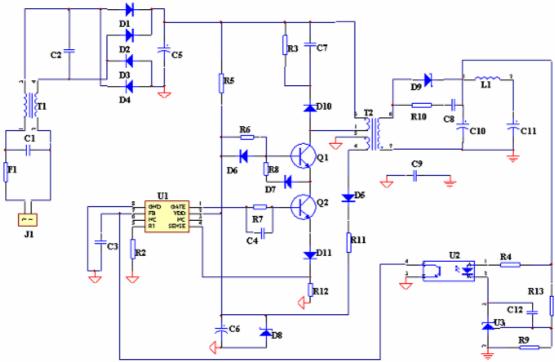
Quantity	Part Number	Description	Quantity	Part Number	Description
1	C1	102/1000V	1	F1	0.25A
1	C2	103/1000V	1	L1	10uH
1	C3	104	1	R1	20R
1	C4	33uF/400V	2	R2、R3	1K
1	C5	223/1000V	1	R4	100K
1	C7	103/1000V	1	R5	100K/0.5W
1	C8	102/100V	1	R6	47R
2	C6、C7	1000 uF/25V	1	R7	1.5M
1	C8	104	1	R8	0R51
1	BD1	KBL406	1	R9	20R
1	D1	FR107	2	R11、R12	3.3K
1	D2	1N4007	1	U1	TC1208
1	D3	Y2010D	1	U2	PC817
1	D4	LED	1	U3	TL431
1	T1	20mH	1	Q1	2N60
1	T2	El-22			

**Notice: Q1 is a Power MOSFET** 

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# REFERENCE CIRCUIT 2 TC1208 drives a periphery Power BJT (5V/3A 15W Power Supply)



Quantity	Part Number	Description	Quantity	Part Number	Description
	C1、C2	104/400V	1	R2	100K
1	C3	472	1	R3	100K/0.5W
1	C4	102	1	R4	1K
1	C5	33uF/400V	1	R5	1.5M
1	C6	10uF/50V	2	R6、R7	220R/1W
1	C7	103/1000V	1	R8	220R
1	C8	102/100V	1	R9	20K
1	C9	223/1000V	2	R10、R11	20R
2	C10、C11	220uF/16V	1	R12	1R2/1W
1	C12	1.0uF/16V	1	R13	20K
5	D1、D2、D3、 D4、D5	1N4007	1	R14	2K2
2	D6、D7	1N4148	1	U1	TC1208
1	D8	18V	1	U2	PC817
1	D9	SB560	1	U3	TL431
2	D10、D11	FR107	1	U4	LM358
1	T1	20mH	1	Q1	13003
1	T2	El-22	1	Q2	9013
1	F1	0.25A			

**Notice: Q1 is a Power BJT** 

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