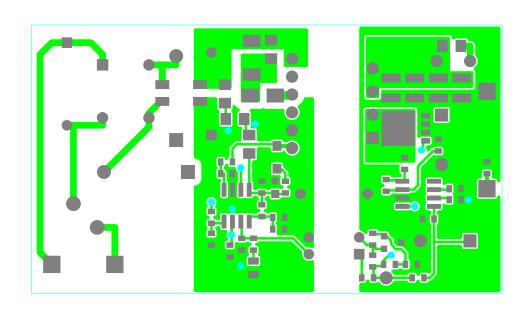
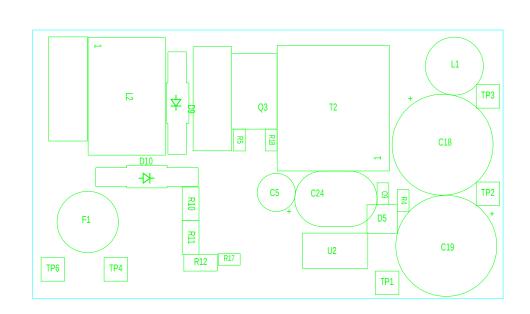


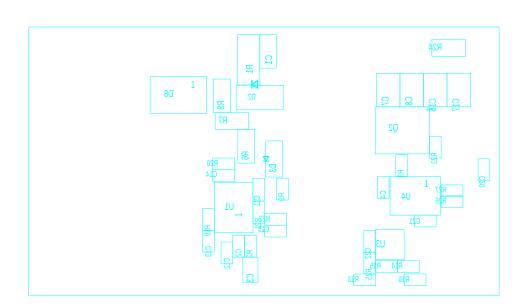
ſ	TEXAS INSTRUMENTS			Copp	Copper Layer Name		Silkscreen		S Mask		P Mask		Assembly		Fab Drawing
L	TEM CONTROLLENTO		Тор		Bot	Тор	Bot	Тор	Bot	Top	Bot	Top	Bot	T ab Drawing	
	Board No. PMP65	570	Rev. A	L1											
I	Date: {Start Date}	Filename: PMP6570 A	Engineer: Brian	King	PCB Ds	<sup>Osgnr:</sup> Brian King	Modif	ied Date:	(Modification	Date}				Software	PADs v9.2



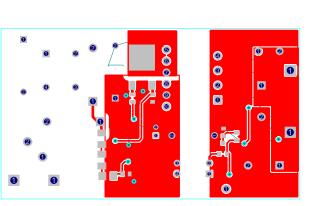
TEXAS INSTRUMENTS			Copper Layer Name			Silkscreen		S Mask		P Mask		Assembly		Fab Drawing
TEXAS INSTRUMENTS		Тор	Bot	T	ор	Bot	Top	Bot	Top	Bot	Тор	Bot	T ab brawing	
PMP6570 Rev.		Rev. A		L2	Τ									
Date: {Start Date} Filename: PMP6570 A Engineer: Brian Kin		ng	PCB Dsgnr: Brian King			Modified Date: {Modification Date}						Software	PADs v9.2	



TEYAS INISTRUM	TEXAS INSTRUMENTS			Silks	Silkscreen		S Mask		P Mask		mbly	Fab Drawing
TEXAS INSTROMENTS		Тор	Bot	Тор	Bot	Тор	Bot	Top	Bot	Тор	Bot	Tab Diawing
Board No. PMP6570	Rev.	L1								TA		
Date: {Start Date} Filename: PMP65	70 A Engine	<sup>neer:</sup> Brian King	PCB Dsgnr: Brian Kin	Mod	lified Date:	(Modification	Date}				Software	PADs v9.2



TEYAS	TEXAS INSTRUMENTS			er Layer Name	Silkscreen		S Mask		P Mask		Assembly		Fab Drawing
TEXAS INSTROMENTS		Тор	Bot	Top	Bot	Тор	Bot	Top	Bot	Top	Bot	T ab Drawing	
Board No.	570	Rev.		L2								ВА	
Date: {Start Date}	Filename: PMP6570 A	Engineer: Brian Ki	ng	PCB Dsgnr: Brian King	Modii	ied Date:	(Modification	Date}				Software	PADs v9.2



TEXAS INSTRUMENTS			Coppe	Copper Layer Name		Silkscreen		S Mask		P Mask		Assembly		Fab Drawing	
			Тор		Bot	Top	Bot	Тор	Bot	Тор	Bot	Тор	Bot	rab Diawing	
PMP6570		Rev.		L1											FB
Date: {Start Date} Filenam	ne: PMP6570 A	Е	Engineer: Brian Kin	g	PCB Ds	<sup>ignr:</sup> Brian King	Modif	ied Date: {	Modification	Date}				Software	PADs v9.2

FABRICATION CHART										
FINISHED THICKNESS	SILKSCREEN	SOLDERMA	SK	FINISHED COPPER WEIGHT						
0.031	LAYER 1	LAYER 1		☐ 1 OZ.						
0.062	LAYER 2	LAYER 2		■ 2 OZ.						
0.093	NONE	NONE		OTHER						
□ 0.125										
DESIGN	TRACE/GAP S	PACING	LAYER COUNT							
SMD	0.010/0.010			SINGLE SIDED						
☐ THRU-HOLE	0.008/0.007			2 LAYER						
MIX	0.006/0.006			4 LAYER						
				OTHER						

## NOTES: UNLESS OTHERWISE SPECIFIED

1. MATERIAL: ALL MATERIALS, INCLUDING BUT NOT LIMITED TO BASE LAMINATE, BONDING MATERIALS

AND SOLDERMASK COATINGS FORMING THE FINISHED PRINTED CIRCUIT BOARD SHALL MEET UL-796 REQUIREMENTS AND BE ROHS COMPLIANT AND HAVE A FLAMMABILITY OF UL94V-0.

PLASTIC SHEET, LAMINATED METAL CLAD, ONE OR TWO SIDES, BASE MATERIAL NEMA TYPE FR-4 OR

2. BASE LAMINATE:

EQUIVALENT, W/Tg =140 Deg C OR HIGHER. MINIMUM COMPOSITION TEMP (Td) OF 320 Deg c. GLASS EPOXY RESIN, COPPER-CLAD IN ACCORDANCE WITH 2 LAYER STACK-UP,

COMPLIANT WITH LEAD FREE PROCESS.

3. SOLDERMASK:

SOLDERMASK OVER BARE COPPER (SMOBC) USING LIQUID PHOTO-IMAGEABLE SOLDERMASK IN ACCORDANCE WITH IPC-SM-840. COLOR: GREEN. MINOR SOLDERMASK ADJUSTMENTS TO FACILITATE PCB FAB AND OR ASSEMBLY IS ALLOWED PROVIDED NO DEFECTS ARE CREATED TO FINAL ASSEMBLY

AS A RESULT.

4. TOLERANCES: UNLESS OTHERWISE SPECIFIED PCB TOLERANCES

SHALL BE +/- .005 INCHES, HOLE DIAMETERS SHALL BE +/- .003 INCHES.

5. PLATING: HOLES REQUIRING PLATING, SEE HOLE CHART, TO HAVE 1 OZ. (0.0014) MIN. THK MIN.

PLATE WITH ROHS COMPLIANT, IMMERSION SILVER PREFERRED, IMMERSION TIN OR Sn/Ag/Cu, 6. FINISH:

WITH RMA FLUX, 0.0003" to .0005" THICK ALL EXPOSED AREAS

AS COATED, NO ACTIVE FLUXES ARE ACCEPTABLE.

7. LEGEND: IF REQUIRED, SILKSCREEN LEGEND(S) WITH WHITE NON-CONDUCTIVE EPOXY INK.

8. MARKINGS: BOARD MUST BEAR VENDOR'S IDENTIFICATION CODE (ETCH OR WHITE NON-CONDUCTIVE INK).

LOCATION OPTIONAL.

9. WORKMANSHIP: BOARD IS TO BE MANUFACTURED PER IPC-A-600 CLASS 2 REQUIREMENTS OR BETTER.

10. DOCUMENTATION: PCB VENDOR IS REQUIRED TO RETURN ANY AND ALL DOCUMENTS SUPPLIED OR ULTIMATELY PURCHASED BY TEXAS

INSTRUMENTS UPON COMPLETION OF PURCHASE ORDER.

11. DRILL SIZES: HOLE DIAMETERS SHOWN ARE FINISHED SIZES AFTER PLATING UNLESS OTHERWISE NOTED.

ANY METAL IN BORDER AREA INCLUDING PART NUMBER, DATECODE AND/OR REVISION LETTERS 12. PANEL BORDER:

MUST BE COVERED WITH SOLDERMASK.

13. PROCESS CHANGES: NO DIMENSIONAL, MATERIAL, OR PROCESS CHANGES ARE ALLOWED WITHOUT PRIOR EXPLICIT WRITTEN PERMISSION

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