

CC, CV Control for Battery Charger & Adaptor

Description

The SL71063 is a voltage and current control IC which contains precision voltage reference. It is suitable for battery charger, second controller of switching regulator systems, and other battery systems.

Features

- Constant Voltage & Constant Current Control
- Wide input voltage range: 2.2V to 14V
- Internal Precision Voltage Reference : 1.240V ± 1%
- Photo Coupler (PC) Drive Current : 40mA Typ.
- Operating Current : 1mA Max.
- Easy compensation & Low external component count.
- Package Outline: SOT26.

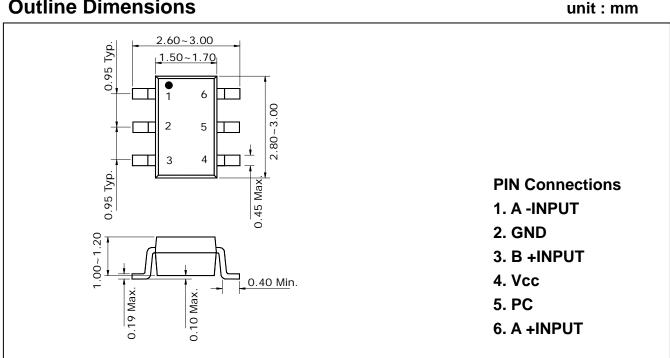
Applications

- Adapters
- Battery chargers

Ordering Information

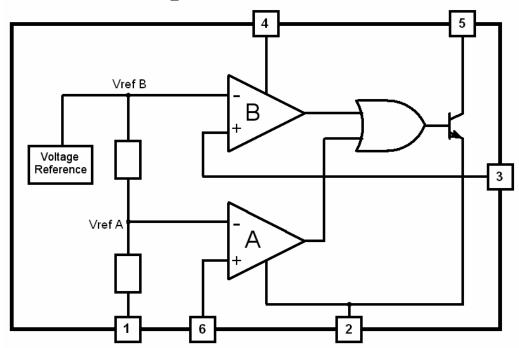
| Type NO. | Marking | Package Code | |
|----------|---------|--------------|--|
| SL71063 | 763 | SOT - 26 | |

Outline Dimensions

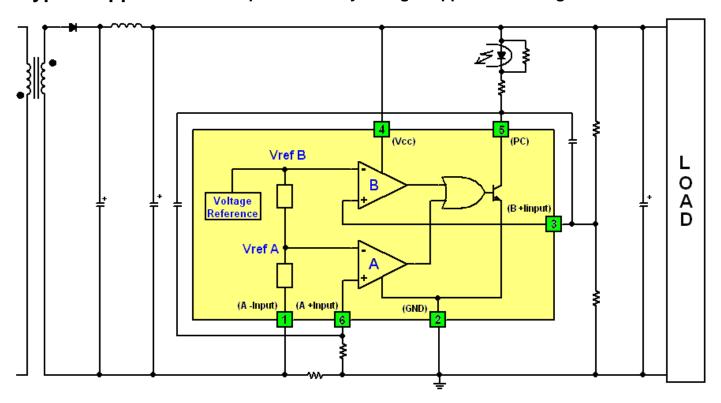


KSD-I5P008-000

Internal Block Diagram



Typical Application : Adapter or Battery Charger Application Using SL71063



In the above application schematic, the SL71063 is used on the secondary side of a flyback adapter (or battery charger) to provide an accurate control of voltage and current.

The above feedback loop is made with an optocoupler.

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Pin Description

| Name | Pin No. | Function | |
|----------|---------|--|--|
| A –INPUT | 1 | Input Pin of the Current Control Loop | |
| GND | 2 | Ground Line. 0V Reference For All Voltages | |
| B +INPUT | 3 | Input Pin of the Voltage Control Loop | |
| Vcc | 4 | DC Supply Line | |
| PC | 5 | Photo Coupler (P/C)Drive Pin. Sinking Current Only | |
| A +INPUT | 6 | Input Pin of the Current Control Loop | |

Absolute Maximum Ratings

| Characteristic | Symbol | Value | Unit |
|-------------------------------------|--------|------------|------------|
| DC Supply Voltage | Vcc | 16 | V |
| Power Dissipation | Pd * | 500 | mW |
| Operating Ambient Temperature Range | Topr | -40 to 85 | $^{\circ}$ |
| Storage Temperature Range | Tstg | -55 to 150 | $^{\circ}$ |

^{*} Mount on a glass epoxy circuit board of 30x30mm Pad dimension of 50mm²

Operating Conditions

| Characteristic | Symbol | Value | Unit |
|-------------------|-----------------|-----------|------|
| DC Supply Voltage | V _{CC} | 2.2 to 14 | V |

Electrical Characteristics

| Characteristic | Symbol | Test Condition | Min. | Тур. | Max. | Unit |
|---------------------|---------------------|--------------------------|------|-----------------------|------|------|
| Operating Current | I _{cc} | I _{PC} = off | - | 0.6 | 1.0 | mA |
| Leakage Current | I _{PCLEAK} | $V^+ = V_{PC} = 14V$ | - | - | 10 | μA |
| Saturation Voltage | V _{PCSAT} | $I_{PC} = 20 \text{ mA}$ | - | 500 | 700 | mV |
| PC Terminal Current | I _{PC} | - | 20 | 40 | 70 | mA |
| Output Voltage High | V _{OH} | R _L = 30 kΩ | ı | V _{CC} – 0.1 | ı | V |

Electrical Characteristics (Continued)

Ta = 25 $^{\circ}$ C and V_{CC} = 5V (unless otherwise specified)

| Characteristic | Symbol | Test Condition | Min. | Тур. | Max. | Unit |
|-------------------------------|----------------------|---------------------------|-------|-------|-------|-------|
| [A CH : Current Control Loop] | | | | | | |
| Reference Voltage 1) | V _{REF} (A) | $I_{PC} = 2.5 \text{ mA}$ | 69 | 72 | 75 | mV |
| Input Bias Current | I _B (A) | - | - | 20 | 50 | μA |
| Transconduction Gain 2) | G _M (A) | - | 1.5 | 8 | - | mA/mV |
| [B Ch : Voltage Control Loop] | | | | | | |
| Reference Voltage 3) | V _{REF} (B) | $I_{PC} = 1.5 \text{ mA}$ | 1.227 | 1.240 | 1.253 | V |
| Input Bias Current | I _B (B) | - | - | 50 | 500 | nA |
| Transconduction Gain 4) | G _M (B) | - | 1 | 3 | - | mA/mV |

- 1) The internal current sense threshold is set to -72mV. The current control loop precision takes into account the cumulative effects of the internal voltage reference deviation as well as the input offset voltage of the transconduction operational amplifier.
- 2) When the positive input at ICTRL is lower than -72mV, and the voltage is decreased by 1mV, the sinking current at the output OUT will be increased by 8mA.
- 3) The internal voltage reference is set at 1.240V (bandgap reference). The voltage control loop precision takes into account the cumulative effects of the internal voltage reference deviation as well as the input offset voltage of the trans-conductance operational amplifier.
- 4) If the voltage on B +Input (the positive input of the amplifier) is higher than the negative amplifier input (Vref(B) = 1.240V), and it is increased by 1mV, the sinking current at the output OUT will be increased by 3mA.

Fig.1 Supply current vs Ambient Temperature

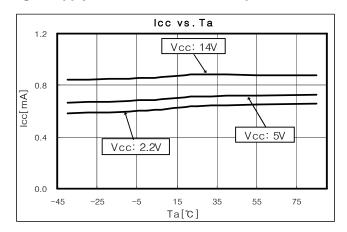


Fig.2 Vref(B) vs Ambient Temperature

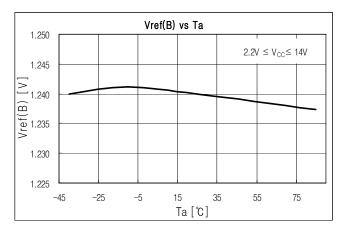


Fig.3 Vref(A) vs Ambient Temperature

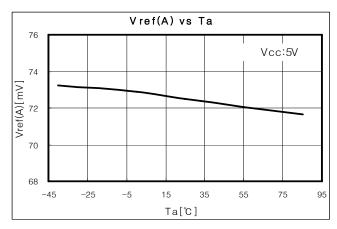


Fig.4 Saturation Voltage vs Ambient Temperature

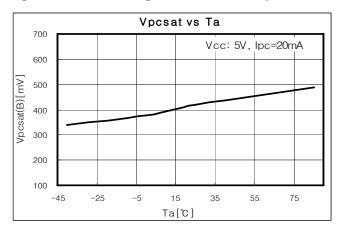
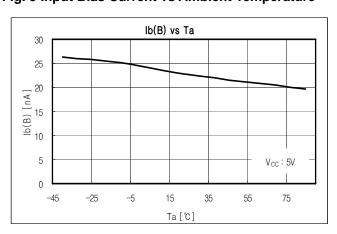


Fig. 5 Input Bias Current vs Ambient Temperature



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CC, CV Control for Battery Charger & Adaptor

Description

SL71051 is a highly integrated solution for SMPS applications requiring CV (constant voltage) and CC (constant current) mode. SL71051 integrates one voltage reference, two operational amplifiers (with ORed outputs-common collectors), and a current sensing circuit. The voltage reference combined with one operational amplifier makes it an ideal voltage controller, and the other low voltage reference combined with the other operational amplifier makes it an ideal current limiter for output low side current sensing, the current threshold is fixed, and precise, the only external components are:

- -. a resistor bridge to be connected to the output of the power supply (adapter, battery charger) to set the voltage regulation by dividing the desired output voltage to match the internal voltage reference value.
- -. a sense resistor having a value and allowable dissipation power which need to be chosen according to the internal voltage threshold.
- -. optional compensation components (R and C). SL71051, housed in one of the smallest package available, is ideal for space shrink applications such as adapters and battery chargers.

Features

- Constant voltage & constant current control
- Wide input voltage range (2.5V to 12V)
- Precision internal voltage reference (1.21V ±1.0%)
- Low external component count
- Current sink output stage
- Easy compensation
- Low ac mains voltage rejection

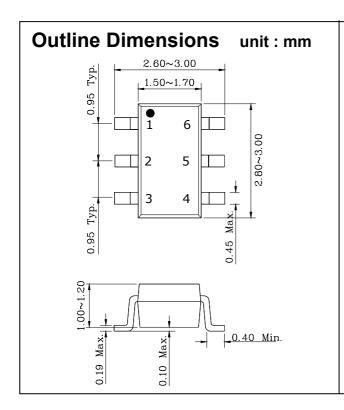
Applications

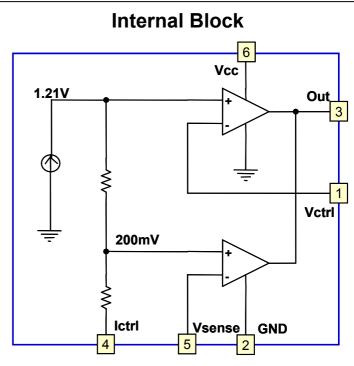
- Adapters
- · Battery chargers

Ordering Information

| Type NO. | Marking | Package Code | |
|----------|---------|--------------|--|
| SL71051 | 701 | SOT - 26 | |

KSD-I5P001-000





Pin Description

| Name | Pin No. | Туре | Function | |
|-----------------|---------|---|---------------------------------------|--|
| Vctrl | 1 | Analog Input | Input Pin of the Voltage Control Loop | |
| Gnd | 2 | Power Supply Ground Line. 0V Reference For All Voltages | | |
| Out | 3 | Current Sink Output | Output Pin. Sinking Current Only | |
| Ictrl | 4 | Analog Input | Input Pin of the Current Control Loop | |
| Vsense | 5 | Analog Input | Input Pin of the Current Control Loop | |
| V _{CC} | 6 | Power Supply | DC Supply Line | |

Absolute Maximum Ratings

| Characteristic | Symbol | Value | Unit |
|-------------------------------------|--------|------------|------------|
| DC Supply Voltage | Vcc | 14 | V |
| Power Dissipation | Pd * | 500 | mW |
| Operating Ambient Temperature Range | Topr | 0 to 85 | $^{\circ}$ |
| Storage Temperature Range | Tstg | -55 to 150 | $^{\circ}$ |

^{*} Mount on a glass epoxy circuit board of 30x30mm Pad dimension of 50mm²

Operating Conditions

| Characteristic | Symbol | Value | Unit |
|-------------------|----------|-----------|------|
| DC Supply Voltage | V_{CC} | 2.5 to 12 | V |

Electrical Characteristics

Ta = 25 $^{\circ}$ C and V_{CC} = 5V (unless otherwise specified)

| Characteristic | Symbol | Test Condition | Min. | Тур. | Max. | Unit |
|---|--------------------|--------------------------|-------|-------|-------|-------|
| [Total Current Consumption] | • | | | • | | |
| Total Supply Current – not taking | , | Ta = 25℃ | - | 1.2 | 2 | A |
| the output sinking current into account | I _{cc} | 0 < Ta < 85℃ | - | 1.5 | - | mA |
| [Voltage Control Loop] | • | | | • | • | |
| Transconduction Gain (Vctrl). | G _{mv} | Ta = 25℃ | 1 | 3.5 | - | mA/mV |
| Sink Current Only 1) | Gmv | 0 < Ta < 85℃ | - | 2.5 | - | ШДЛ |
| Voltage Control Loop Reference 2) | V_{ref} | Ta = 25 ℃ | 1.198 | 1.210 | 1.222 | V |
| voltage Control Loop Reference | v ret | 0 < Ta < 85℃ | 1.186 | 1.210 | 1.234 | V |
| Input Bias Current (Vctrl) | l | Ta = 25 ℃ | 1 | 50 | - | nA |
| input bias current (vetil) | l _{ibv} | 0 < Ta < 85℃ | - | 100 | - | HA |
| [Current Control Loop] | | | | | | |
| Transconduction Gain (lctrl). | G _{mi} | Ta = 25 ℃ | 1.5 | 7.0 | - | mA/mV |
| sink Current Only 3) | | 0 < Ta < 85℃ | - | - | - | |
| Current Control Loop Reference 4) | | lout = 2.5 mA, Ta = 25 ℃ | 196 | 200 | 204 | mV |
| Current Control Loop Reference | V _{sense} | 0 < Ta < 85℃ | 192 | - | 208 | IIIV |
| Compart and of his ICTRI at 200ms)/ | | Ta = 25℃ | - | 25 | - | |
| Current out of pin ICTRL at -200mV | l _{ibi} | 0 < Ta < 85℃ | - | 30 | - | μΑ |
| [Output Stage] | | | | | | |
| Low output Voltage at 10mA Sinking current | V _{ol} | - | - | 200 | - | mV |
| Output Short Circuit Current. Output to V _{CC} . Sink Current Only | l _{os} | - | - | 35 | 70 | mA |

- 1) If the voltage on VCTRL (the negative input of the amplifier) is higher than the positive amplifier input (Vref = 1.210V), and it is increased by 1mV, the sinking current at the output OUT will be increased by 3.5mA.
- 2) The internal voltage reference is set at 1.210V (bandgap reference). The voltage control loop precision takes into account the cumulative effects of the internal voltage reference deviation as well as the input offset voltage of the trans-conductance operational amplifier. The internal voltage reference is fixed by bandgap, and trimmed to 0.5% accuracy at room temperature.
- 3) When the positive input at ICTRL is lower than -200mV, and the voltage is decreased by 1mV, the sinking current at the output OUT will be increased by 7mA.
- 4) The internal current sense threshold is set to -200mV. The current control loop precision takes into account the cumulative effects of the internal voltage reference deviation as well as the input offset voltage of the transconduction operational amplifier.

SL71051

Fig.1 Supply current vs Ambient Temperature

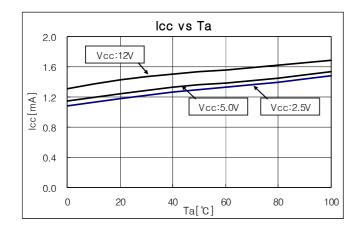


Fig.2 Vref vs Ambient Temperature

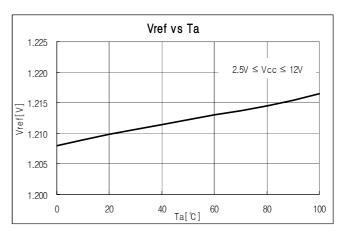


Fig.3 Vsense vs Ambient Temperature

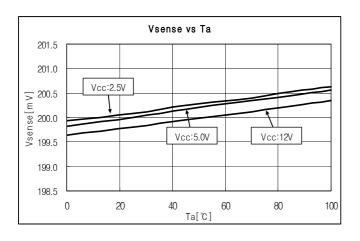


Fig.4 Vsense pin input bias current vs

Ambient Temperature

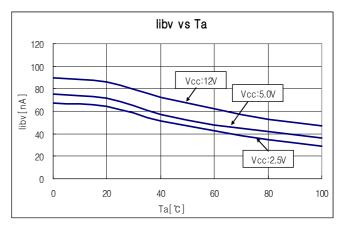


Fig.5 lctrl pin input bias current vs

Ambient Temperature

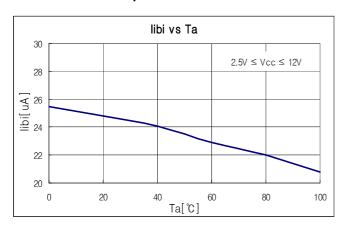
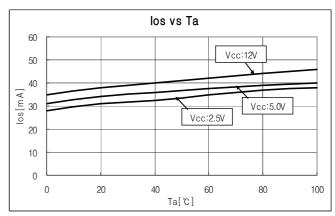


Fig.6 Output Short circuit current vs

Ambient Temperature



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CC, CV Control for Battery Charger & Adaptor

Description

SL71053 is a highly integrated solution for SMPS applications requiring CV (constant voltage) and CC (constant current) mode. SL71053 integrates one voltage reference, two operational amplifiers (with ORed outputs-common collectors), and a current sensing circuit. The voltage reference combined with one operational amplifier makes it an ideal voltage controller, and the other low voltage reference combined with the other operational amplifier makes it an ideal current limiter for output low side current sensing, the current threshold is fixed, and precise, the only external components are:

- -. a resistor bridge to be connected to the output of the power supply (adapter, battery charger) to set the voltage regulation by dividing the desired output voltage to match the internal voltage reference value.
- -. a sense resistor having a value and allowable dissipation power which need to be chosen according to the internal voltage threshold.
- -. optional compensation components. SL71053, housed in one of the smallest package available, is ideal for space shrink applications such as adapters and battery chargers.

Features

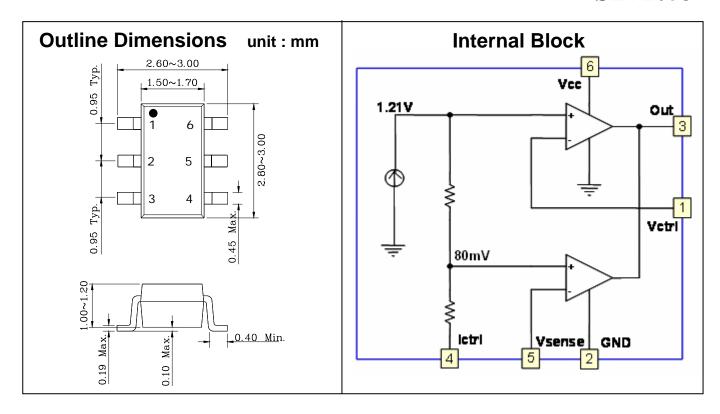
- Constant voltage & constant current control
- Wide operating voltage range (2.2V to 14V)
- Precision internal voltage reference (1.21V $\pm 1.0\%$)
- Precision internal CC AMP reference (80mV Typ.)
- Low external component count
- Current sink output stage
- Easy compensation

Applications

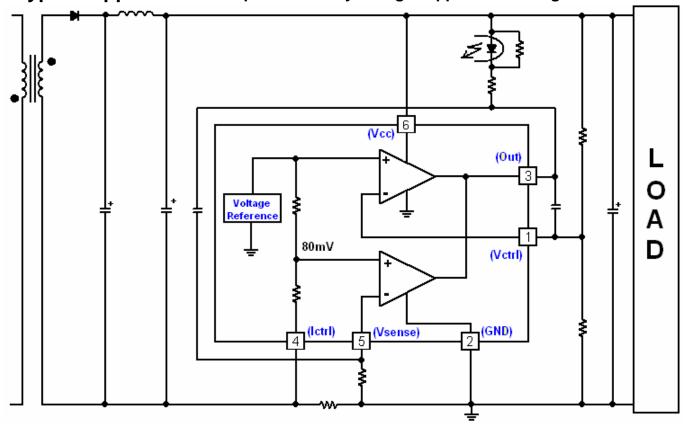
- Adapters
- Battery chargers

Ordering Information

| Type NO. | Marking | Package Code | |
|----------|---------|--------------|--|
| SL71053 | 703 | SOT - 26 | |



Typical Application : Adapter or Battery Charger Application Using SL71053



In the above application schematic, the SL71053 is used on the secondary side of a flyback adapter (or battery charger) to provide an accurate control of voltage and current. The above feedback loop is made with an optocoupler.

Pin Description

| Name | Pin No. | Туре | Function |
|-----------------|---------|---------------------|--|
| Vctrl | 1 | Analog Input | Input Pin of the Voltage Control Loop |
| Gnd | 2 | Power Supply | Ground Line. 0V Reference For All Voltages |
| Out | 3 | Current Sink Output | Output Pin. Sinking Current Only |
| lctrl | 4 | Analog Input | Input Pin of the Current Control Loop |
| Vsense | 5 | Analog Input | Input Pin of the Current Control Loop |
| V _{cc} | 6 | Power Supply | DC Supply Line |

Absolute Maximum Ratings

| Characteristic | Symbol | Value | Unit |
|-------------------------------------|--------|------------|---------------|
| DC Supply Voltage | Vcc | 14 | V |
| Power Dissipation | Pd * | 500 | mW |
| Operating Ambient Temperature Range | Topr | 0 to 85 | ${\mathbb C}$ |
| Storage Temperature Range | Tstg | -55 to 150 | ${\mathbb C}$ |

^{*} Mount on a glass epoxy circuit board of 30x30mm Pad dimension of 50mm²

Operating Conditions

| Characteristic | Symbol | Value | Unit |
|-------------------|-----------------|-----------|------|
| DC Supply Voltage | V _{cc} | 2.2 ~ 14V | V |

Electrical Characteristics

Ta = 25 $^{\circ}$ C and V_{CC} = 5V (unless otherwise specified)

| Characteristic | Symbol | Test Condition | Min. | Тур. | Max. | Unit |
|---|--------|----------------|------|------|------|------|
| [Total Current Consumption] | | | | | | |
| Total Supply Current – not taking | l | Ta = 25 ℃ | - | 1.2 | 2 | mΛ |
| the output sinking current into account | Icc | 0 < Ta < 85℃ | - | 1.5 | - | mA |

Electrical Characteristics

Ta = 25 $^{\circ}$ C and V_{CC} = 5V (unless otherwise specified)

| Characteristic | Symbol | Test Condition | Min. | Тур. | Max. | Unit |
|---|--------------------|--------------------------|-------|-------|-------|--------|
| [Voltage Control Loop] | | | | | | |
| Transconduction Gain (Vctrl). | | Ta = 25 ℃ | 1 | 3.5 | - | 1 /m\/ |
| Sink Current Only 1) | G _{mv} | 0 < Ta < 85℃ | - | 2.5 | - | mA/mV |
| Voltage Control Loop Reference 2) | V_{ref} | Ta = 25 ℃ | 1.198 | 1.210 | 1.222 | V |
| Voltage Control Loop Reference | v ref | 0 < Ta < 85 ℃ | 1.186 | 1.210 | 1.234 | V |
| Input Bias Current (Vctrl) | l _{ibv} | Ta = 25 ℃ | - | 50 | - | пΛ |
| | libv | 0 < Ta < 85 ℃ | - | 100 | - | nA nA |
| [Current Control Loop] | | | | | | |
| Transconduction Gain (lctrl). sink Current Only 3) | G _{mi} | Ta = 25℃ | 1.5 | 7.0 | - | mA/mV |
| | | 0 < Ta < 85℃ | - | 1 | - | |
| Current Control Loop Reference 4) | V | lout = 2.5 mA, Ta = 25 ℃ | 77 | 80 | 83 | mV |
| Current Control Loop Reference | V _{sense} | 0 < Ta < 85℃ | 75 | - | 85 | 1 "" |
| Current out of pin ICTRL at -200mV | | Ta = 25 ℃ | - | 25 | - | Λ |
| Current out of pill ICTRL at -200111V | l _{ibi} | 0 < Ta < 85℃ | - | 30 | - | μΑ |
| [Output Stage] | | | | | | |
| Low output Voltage at 10mA Sinking current | V _{ol} | - | - | 200 | - | mV |
| Output Short Circuit Current. Output to V _{CC} . Sink Current Only | l _{os} | - | - | 35 | 70 | mA |

- 1) If the voltage on VCTRL (the negative input of the amplifier) is higher than the positive amplifier input (Vref = 1.210V), and it is increased by 1mV, the sinking current at the output OUT will be increased by 3.5mA.
- 2) The internal voltage reference is set at 1.210V (bandgap reference). The voltage control loop precision takes into account the cumulative effects of the internal voltage reference deviation as well as the input offset voltage of the trans-conductance operational amplifier. The internal voltage reference is fixed by bandgap, and trimmed to 0.5% accuracy at room temperature.
- 3) When the positive input at ICTRL is lower than -80mV, and the voltage is decreased by 1mV, the sinking current at the output OUT will be increased by 7mA.
- 4) The internal current sense threshold is set to -80mV. The current control loop precision takes into account the cumulative effects of the internal voltage reference deviation as well as the input offset voltage of the transconduction operational amplifier.

Electrical Characteristic Curves

Fig.1 Supply current vs Ambient Temperature

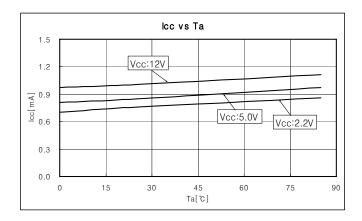


Fig.2 Vref vs Ambient Temperature

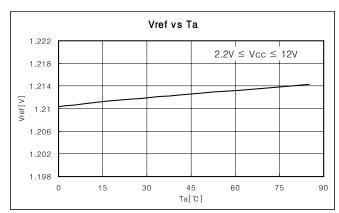


Fig.3 Vsense vs Ambient Temperature

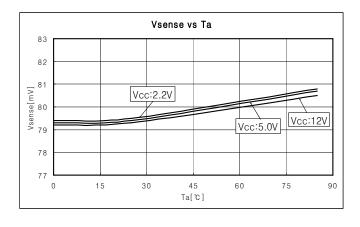


Fig.4 Vsense pin input bias current vs

Ambient Temperature

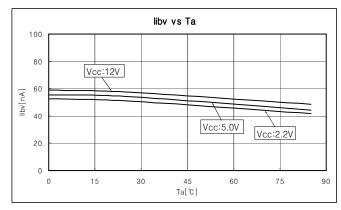


Fig.5 lctrl pin input bias current vs

Ambient Temperature

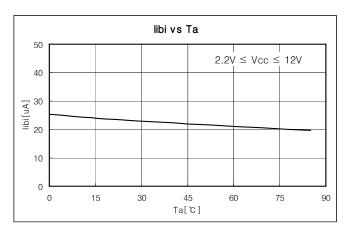
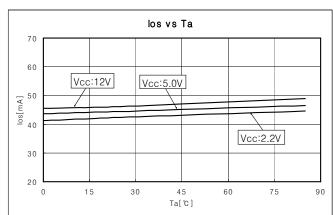


Fig.6 Output Short circuit current vs
Ambient Temperature



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Description

The SL71061 is a voltage and current control IC which contains precision voltage reference. It is suitable for battery charger, second controller of switching regulator systems, and other battery systems.

Features

- Constant Voltage & Constant Current Control
- Wide input voltage range: 2.2V to 14V
- Internal Precision Voltage Reference : 1.240V ± 1%
- Photo Coupler (PC) Drive Current : 40mA Typ.
- Operating Current : 1mA Max.
- Easy compensation & Low external component count.
- Package Outline: SOT26.

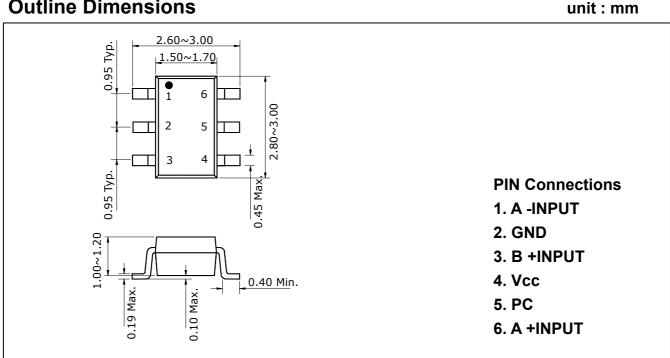
Applications

- Adapters
- Battery chargers

Ordering Information

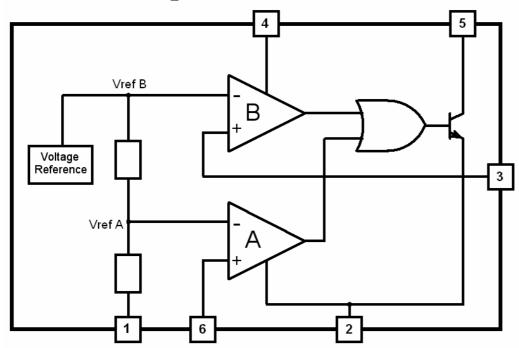
| Type NO. | Marking | Package Code | |
|----------|---------|--------------|--|
| SL71061 | 761 | SOT - 26 | |

Outline Dimensions

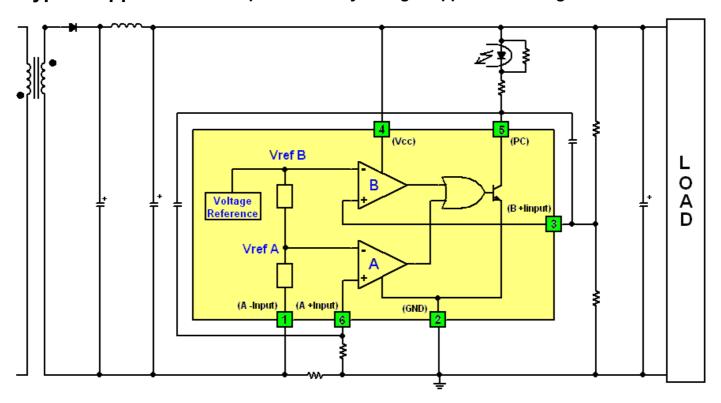


KSD-I5P004-001

Internal Block Diagram



Typical Application : Adapter or Battery Charger Application Using SL71061



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The above feedback loop is made with an optocoupler.

Pin Description

| Name | Pin No. | Function |
|----------|---------|--|
| A –INPUT | 1 | Input Pin of the Current Control Loop |
| GND | 2 | Ground Line. 0V Reference For All Voltages |
| B +INPUT | 3 | Input Pin of the Voltage Control Loop |
| Vcc | 4 | DC Supply Line |
| PC | 5 | Photo Coupler (P/C)Drive Pin. Sinking Current Only |
| A +INPUT | 6 | Input Pin of the Current Control Loop |

Absolute Maximum Ratings

| Characteristic | Symbol | Value | Unit |
|-------------------------------------|--------|------------|------|
| DC Supply Voltage | Vcc | 16 | V |
| Power Dissipation | Pd * | 500 | mW |
| Operating Ambient Temperature Range | Topr | -40 to 85 | °C |
| Storage Temperature Range | Tstg | -55 to 150 | °C |

^{*} Mount on a glass epoxy circuit board of 30x30mm Pad dimension of 50mm²

Operating Conditions

| Characteristic | Symbol | Value | Unit |
|-------------------|-----------------|-----------|------|
| DC Supply Voltage | V _{CC} | 2.2 to 14 | V |

Electrical Characteristics

| Characteristic | Symbol | Test Condition | Min. | Тур. | Max. | Unit |
|---------------------|---------------------|-------------------------|------|-----------------------|------|------|
| Operating Current | I _{cc} | I _{PC} = off | - | 0.6 | 1.0 | mA |
| Leakage Current | I _{PCLEAK} | $V^{+} = V_{PC} = 14V$ | - | - | 10 | μA |
| Saturation Voltage | V _{PCSAT} | I _{PC} = 20 mA | - | 500 | 700 | mV |
| PC Terminal Current | I _{PC} | - | 20 | 40 | 70 | mA |
| Output Voltage High | V _{OH} | R _L = 30 kΩ | 1 | V _{CC} – 0.1 | 1 | V |

Electrical Characteristics (Continued)

Ta = 25 $^{\circ}$ C and V_{CC} = 5V (unless otherwise specified)

| Characteristic | Symbol | Test Condition | Min. | Тур. | Max. | Unit | |
|-------------------------------|----------------------|--------------------------|-------|-------|-------|-------|--|
| [A CH : Current Control Loop] | | | | | | | |
| Reference Voltage 1) | V _{REF} (A) | I _{PC} = 2.5 mA | 145 | 151 | 157 | mV | |
| Input Bias Current | I _B (A) | - | - | 20 | 50 | μA | |
| Transconduction Gain 2) | G _M (A) | - | 1.5 | 8 | - | mA/mV | |
| [B Ch : Voltage Control Loop] | | | | | | | |
| Reference Voltage 3) | V _{REF} (B) | I _{PC} = 1.5 mA | 1.227 | 1.240 | 1.253 | V | |
| Input Bias Current | I _B (B) | - | - | 50 | 500 | nA | |
| Transconduction Gain 4) | G _M (B) | - | 1 | 3 | - | mA/mV | |

- 1) The internal current sense threshold is set to -151mV. The current control loop precision takes into account the cumulative effects of the internal voltage reference deviation as well as the input offset voltage of the transconduction operational amplifier.
- 2) When the positive input at ICTRL is lower than -151mV, and the voltage is decreased by 1mV, the sinking current at the output OUT will be increased by 8mA.
- 3) The internal voltage reference is set at 1.240V (bandgap reference). The voltage control loop precision takes into account the cumulative effects of the internal voltage reference deviation as well as the input offset voltage of the trans-conductance operational amplifier.
- 4) If the voltage on B +Input (the positive input of the amplifier) is higher than the negative amplifier input (Vref(B) = 1.240V), and it is increased by 1mV, the sinking current at the output OUT will be increased by 3mA.

Fig.1 Supply current vs Ambient Temperature

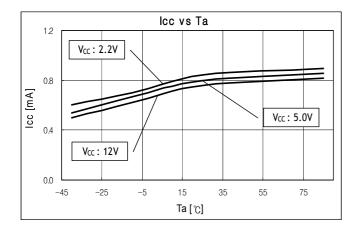


Fig.2 Vref(B) vs Ambient Temperature

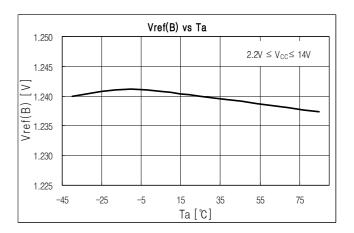


Fig.3 Vref(A) vs Ambient Temperature

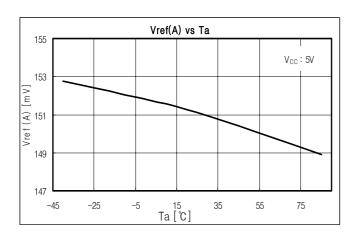


Fig.4 Saturation Voltage vs Ambient Temperature

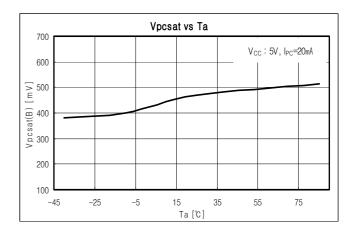
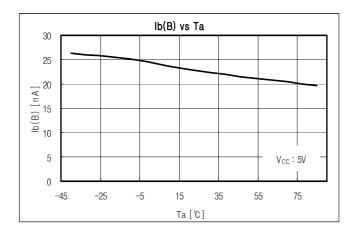


Fig. 5 Input Bias Current vs Ambient Temperature



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CC, CV Control for Battery Charger & Adaptor

Description

The SL71062 is a voltage and current control IC which contains precision voltage reference. It is suitable for battery charger, second controller of switching regulator systems, and other battery systems.

Features

- Constant Voltage & Constant Current Control
- Wide input voltage range: 2.2V to 14V
- Internal Precision Voltage Reference : 1.240V ± 1%
- Photo Coupler (PC) Drive Current : 40mA Typ.
- Operating Current : 1mA Max.
- Easy compensation & Low external component count.
- Package Outline: SOT26.

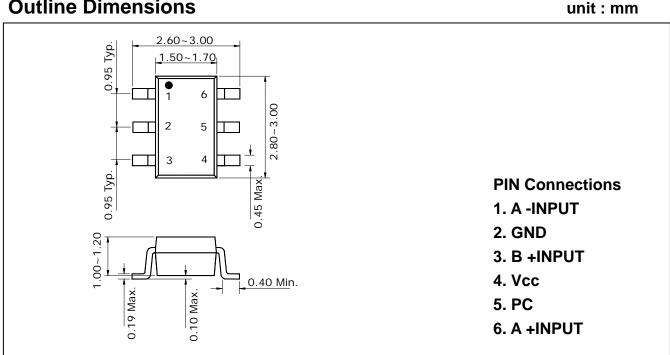
Applications

- Adapters
- Battery chargers

Ordering Information

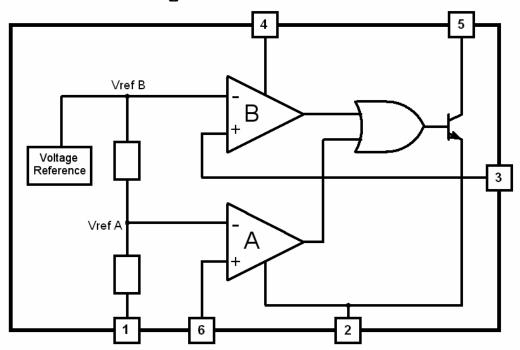
| Type NO. | Marking | Package Code | |
|----------|---------|--------------|--|
| SL71062 | 762 | SOT - 26 | |

Outline Dimensions

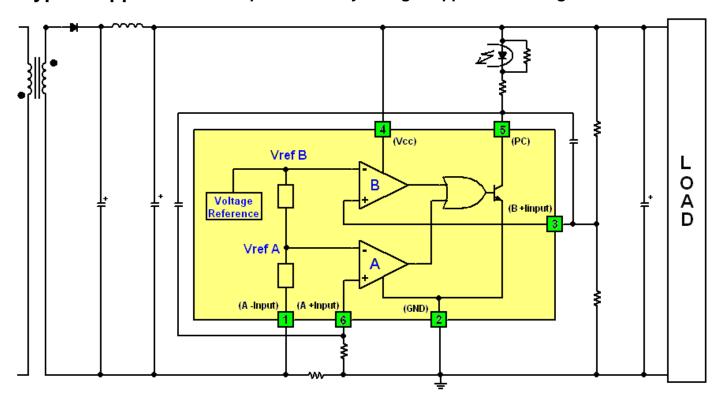


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Internal Block Diagram



Typical Application : Adapter or Battery Charger Application Using SL71062



In the above application schematic, the SL71062 is used on the secondary side of a flyback adapter (or battery charger) to provide an accurate control of voltage and current.

The above feedback loop is made with an optocoupler.

Pin Description

| Name | Pin No. | Function |
|----------|---------|--|
| A –INPUT | 1 | Input Pin of the Current Control Loop |
| GND | 2 | Ground Line. 0V Reference For All Voltages |
| B +INPUT | 3 | Input Pin of the Voltage Control Loop |
| Vcc | 4 | DC Supply Line |
| PC | 5 | Photo Coupler (P/C)Drive Pin. Sinking Current Only |
| A +INPUT | 6 | Input Pin of the Current Control Loop |

Absolute Maximum Ratings

| Characteristic | Symbol | Value | Unit |
|-------------------------------------|--------|------------|------------|
| DC Supply Voltage | Vcc | 16 | V |
| Power Dissipation | Pd * | 500 | mW |
| Operating Ambient Temperature Range | Topr | -40 to 85 | $^{\circ}$ |
| Storage Temperature Range | Tstg | -55 to 150 | $^{\circ}$ |

^{*} Mount on a glass epoxy circuit board of 30x30mm Pad dimension of 50mm²

Operating Conditions

| Characteristic | Symbol | Value | Unit | |
|-------------------|-----------------|-----------|------|--|
| DC Supply Voltage | V _{CC} | 2.2 to 14 | V | |

Electrical Characteristics

| Characteristic | Symbol | Test Condition | Min. | Тур. | Max. | Unit |
|---------------------|---------------------|--------------------------|------|-----------------------|------|------|
| Operating Current | I _{cc} | I _{PC} = off | - | 0.6 | 1.0 | mA |
| Leakage Current | I _{PCLEAK} | $V^+ = V_{PC} = 14V$ | - | - | 10 | μA |
| Saturation Voltage | V _{PCSAT} | $I_{PC} = 20 \text{ mA}$ | - | 500 | 700 | mV |
| PC Terminal Current | I _{PC} | - | 20 | 40 | 70 | mA |
| Output Voltage High | V _{OH} | R _L = 30 kΩ | - | V _{CC} – 0.1 | - | V |

Electrical Characteristics (Continued)

Ta = 25 $^{\circ}$ C and V_{CC} = 5V (unless otherwise specified)

| Characteristic | Symbol | Test Condition | Min. | Тур. | Max. | Unit | |
|-------------------------------|----------------------|---------------------------|-------|-------|-------|-------|--|
| [A CH : Current Control Loop] | | | | | | | |
| Reference Voltage 1) | V _{REF} (A) | $I_{PC} = 2.5 \text{ mA}$ | 105 | 109 | 113 | mV | |
| Input Bias Current | I _B (A) | - | - | 20 | 50 | μA | |
| Transconduction Gain 2) | G _M (A) | - | 1.5 | 8 | - | mA/mV | |
| [B Ch : Voltage Control Loop] | | | | | | | |
| Reference Voltage 3) | V _{REF} (B) | $I_{PC} = 1.5 \text{ mA}$ | 1.227 | 1.240 | 1.253 | V | |
| Input Bias Current | I _B (B) | - | - | 50 | 500 | nA | |
| Transconduction Gain 4) | G _M (B) | - | 1 | 3 | - | mA/mV | |

- 1) The internal current sense threshold is set to -109mV. The current control loop precision takes into account the cumulative effects of the internal voltage reference deviation as well as the input offset voltage of the transconduction operational amplifier.
- 2) When the positive input at ICTRL is lower than -109mV, and the voltage is decreased by 1mV, the sinking current at the output OUT will be increased by 8mA.
- 3) The internal voltage reference is set at 1.240V (bandgap reference). The voltage control loop precision takes into account the cumulative effects of the internal voltage reference deviation as well as the input offset voltage of the trans-conductance operational amplifier.
- 4) If the voltage on B +Input (the positive input of the amplifier) is higher than the negative amplifier input (Vref(B) = 1.240V), and it is increased by 1mV, the sinking current at the output OUT will be increased by 3mA.

Fig.1 Supply current vs Ambient Temperature

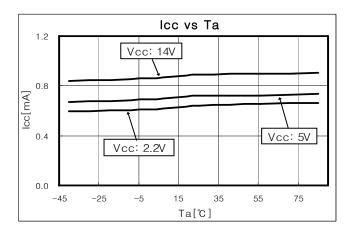


Fig.2 Vref(B) vs Ambient Temperature

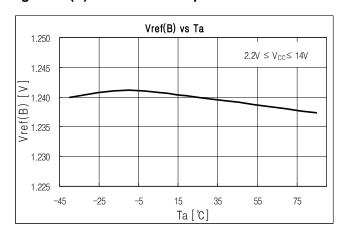


Fig.3 Vref(A) vs Ambient Temperature

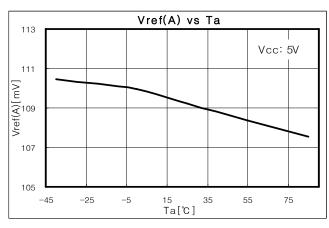


Fig.4 Saturation Voltage vs Ambient Temperature

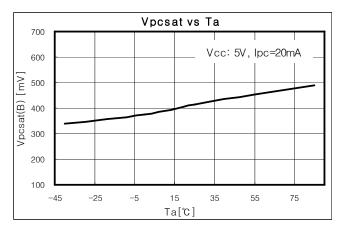
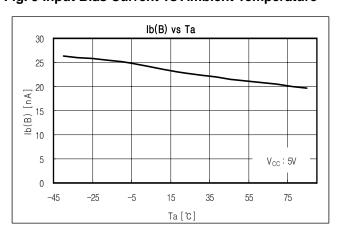


Fig. 5 Input Bias Current vs Ambient Temperature



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