

數能科技股份有限公司

150mA Single channel LED Driver

Features

- Up to 150mA single channel constant current regulator
- Current set by an external resistor
- 3V ~ 10V wide supply voltage range supports self power structure in lighting application
- Minimum 0.4V (80mA) dropout voltage
- Fast current rising and falling
- -40°C ~ 85°C operating temperature
- Less than ±3% Chip to Chip current skew
- Less than 2%/V load (or line) regulation
- 160 °C half power / 115 °C recovery thermal protect
- Cascade-able for higher voltage drop applications

Product Description

NU510 is a medium power linear current regulation component that can be easily used in various LED lighting applications. It is equipped the excellent feature of good load/line regulation capability, minimized chip current skew, stable output current in high power or load voltage fluctuating environment that can be used in wide area of LED lighting source to maintain the uniformity of light intensity . NU510 also can be used in the digital PWM controlled circuit to achieve more precise current adjusting in gray level applications.

A special cascade mode is also provided by NU510. In high power supply voltage and low LED load dropout voltage application, two or more NU510 can be connected in series to share redundant high voltage. With the exclusive voltage sharing technology of NUMEN tech., the extra redundant voltage that exceeds the preset threshold voltage (Viboost) can be shared by next NU510.

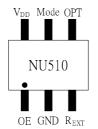
With the feature of wide power supply range design and ultra low I_{DD} consumption, the NU510 supports the self powered structure in LED lighting applications. In this structure, the NU510 no need to be provided a dedicate power circuit even the system power voltage is much higher than the maximum operation voltage of NU510. The V_{DD} power can be gotten from the proper position in LED series of system.

Applications

- General LED lighting
- Decoration lighting for architecture
- LED torch / flash light
- **RGB** lighting
- RGB display / indicator

Package Type

SOT 23-6 (pin out compatible with NU501)



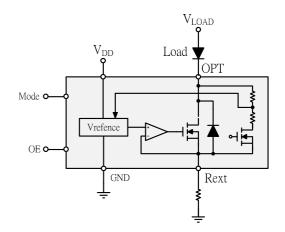
MSOP 8 / SOP 8



Terminal Description

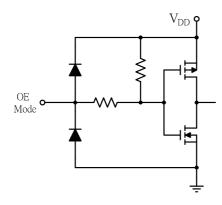
Pin name	Function			
V_{DD}	Power supply			
OPT	Current sink			
R _{EXT}	Current setting Resistor			
OE Output enable				
Mode Cascade / Normal mode selection				
GND	Ground			

Block Diagram



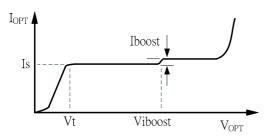
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Equivalent Circuits for Inputs

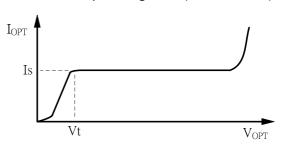


Ideal IV characteristic

Mode pin = Logic high (default, cascade mode)



Mode pin = Logic low (normal mode)



Maximum Ratings (T = 25°C)

Characteristic	Symbol		Rating	Unit	
Supply voltage	V	DD	0 ~ 12	V	
Output voltage	Vo	OPT	-0.2 [~] 20	V	
Output current	I,	PN	0 ~ 150	mA	
	PD	SOT 236	0.25	W	
Power Dissipation (Ta=25°C)		MSOP 8	1		
		SOP 8	1.25		
	$R_{TH(j\text{-a})}$	SOT 236	400		
Thermal Resistance (On PCB, Ta=25°C)		MSOP 8	125	°C /W	
		SOP 8	100		
Operating temperature	T _{OPR}		-40~+85	°C	
Storage temperature	T_{STG}		-55~+150	°C	

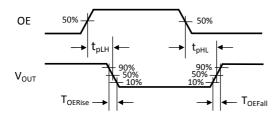
Electrical Characteristics and Recommended Operating Conditions

Characteristic	Symbol	Condition		Min.	Тур.	Max.	Unit
Supply voltage	V_{DD}	Room Temp. V _{OPT} = 1V		3	-	10	V
Supply current	I _{DD}		-	50	80	150	uA
			I _S <= 20mA	0.25	0.3	0.4	V
	.,		I _s <= 60mA	0.3	0.4	0.5	
Minimum dropout voltage	V_{OUT}	$V_{DD} >= 3V$	I _S <= 100mA	0.4	0.5	0.6	
			I _S <= 150mA	0.6	0.7	0.8	
Output current	I _{OPT}			-	-	150	mA
Leakage	$I_{Leakage}$	$V_{DD} > 3V, V_{OPT} = 10V$		1	-	5	uA
Line regulation	%/V _{DD}	12V > V _{DD} > 3V		-	-	±1	%/V
Load regulation	%/V _P	8V>V _{OUT} >0.4V, Mode = low		-	-	±1	%/V
Thermal regulation	%/10°C	$V_{DD} = V_P = 3V$		-	-	±0.5	%/10°C
Half power temperature	T_{half}	$Iout \cong \frac{Inormal}{2}$		-	160	-	$^{\circ}\! \mathbb{C}$
Half power recovery temperature	T_{recov}	<i>lout</i> recover to <i>Inormal</i>		-	115	-	
Current boost voltage	V_{iboost}	Mode = high		7	8	9	V
Current boost	l _{boost}	Mode = high		5	6	9	% * I _{OPT}
Chip current skew	I _{Skew}	V _{DD} =	V _P = 3V	-	2.5	4	%

Switching Characteristics (T = 25°C)

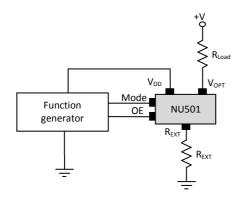
Characteristic	Symbol	ol Condition		Тур.	Max.	Unit
Propagation Delay Time (OE from "L" to "H")	t _{pLH}	V_{DD} =4V, V_{OUT} =1V, I_{OUT} =120mA, OE= 0V \rightarrow 4V	140	200	260	nS
Output current rising time (OE from "L" to "H")	t _{OERise}	V_{DD} =4V, V_{OUT} =1V, I_{OUT} =120mA, OE= 0V \rightarrow 4V	30	40	60	nS
Propagation Delay Time (OE from "H" to "L")	t _{pHL}	V_{DD} =4V, V_{OUT} =1V, I_{OUT} =120mA, OE= 4V \rightarrow 0V	260	320	380	nS
Output current falling time (OE from "H" to "L")	t _{OEFall}	V_{DD} =4V, V_{OUT} =1V, I_{OUT} =120mA, OE= 4V \rightarrow 0V	30	50	80	nS

Timing Waveform



OE timing diagram

Test Circuit



Output Current Setting

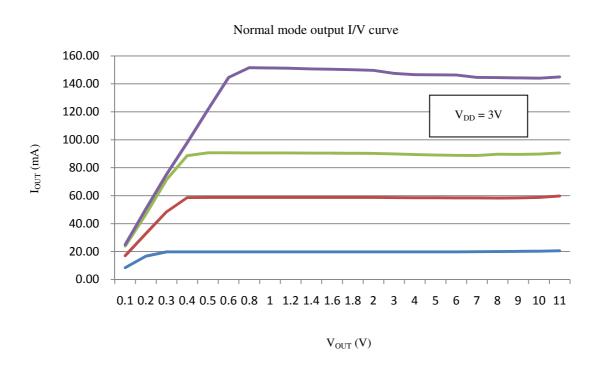
The output current of NU510 is set by an external resistor (R_{EXT}). The output current can be figured out by following equation.

$$Iout \cong \frac{0.2V}{R_{EXT}}$$

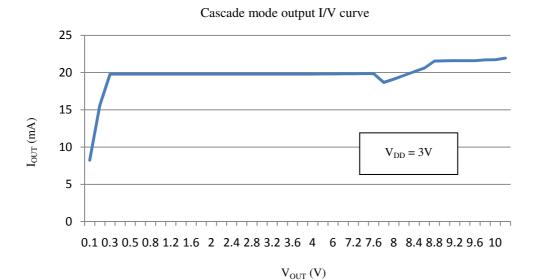
Thermal protection

When junction temperature is more than half power temperature (~160°C), the output current of NU510 will decrease about 50% to lower down the power dissipation on chip. When the half power occurs, there are two conditions can recover the output current to normal. One is the junction temperature lower than recovery temperature (~115°C). The other one is to reset the OE signal from low to high.

I/V Curve

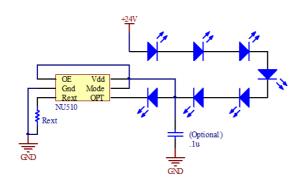


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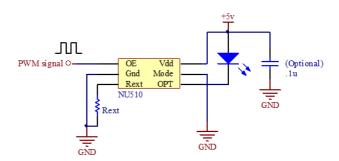


Typical Application Circuit

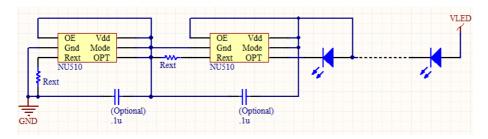
24v General lighting



PWM grey level application



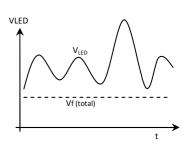
Cascade application



Two or more NU510 in series can absorb higher voltage variation in lighting system. Each NU510 can share 8 volts redundant. The total voltage variation range that system can work is calculated by following equation.

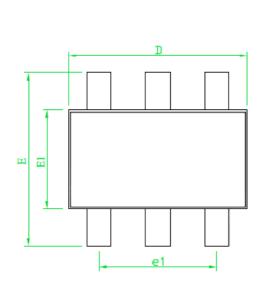
$$VLED_{(max)} \dot{=} 8 * N_{(NU510)} - Vf_{(total)}$$

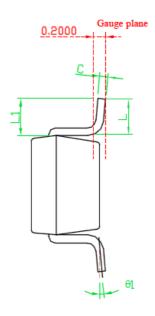
Where $VLED_{(max)}$ is the system power voltage, $N_{(NU510)}$ is the number of NU510 and $Vf_{(total)}$ is the total forward voltage of all LEDs.

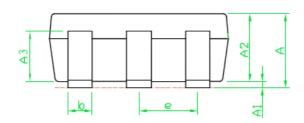


Package Dimensions

● SOT23-6

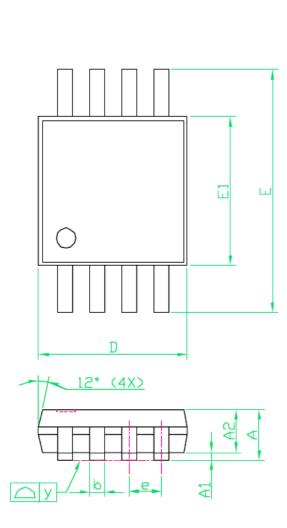


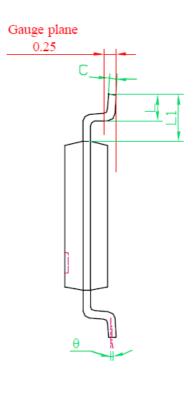




SYMBOLS	DIMENSIONS IN MILLIMETERS			
STWIDOLS	MIN	NOM	MAX	
A	1.00	1.10	1.40	
A1	0.00		0.10	
A2	1.00	1.10	1.30	
A3	0.70	0.80	0.90	
b	0.35	0.40	0.50	
C	0.10	0.15	0.25	
D	2.70	2.90	3.10	
E1	1.40	1.60	1.80	
e1		1.90(TYP)		
E	2.60	2.80	3.00	
L	0.37			
θ1	1°	5°	9°	
e		0.95(TYP)		
L1	0.5	0.6	0.7	

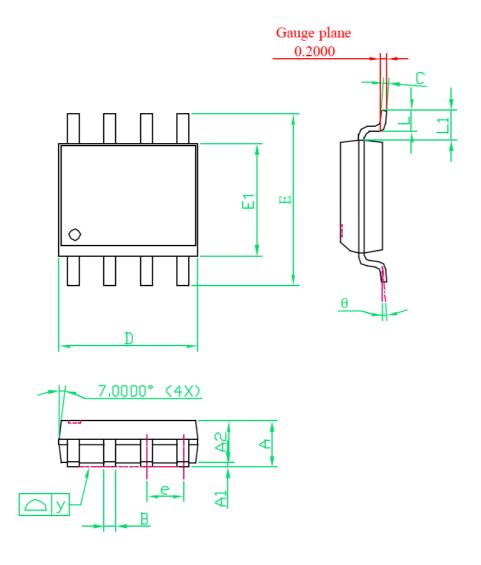
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SYMBOLS	DIMENSIONS IN MILLIMETER				
STWIDOLS	MIN NOM		MAX		
A			1.10		
A1	0.00		0.10		
A2	0.75	0.85	0.95		
b	0.22	0.30	0.38		
C	0.13	0.15	0.23		
D		3.00BSC			
E		4.90BSC			
E1		3.00BSC			
e		0.65BSC			
L	0.40	0.53	0.66		
у			0.10		
θ	0°		6°		
L1	0.85	0.95	1.05		

● SOP-8



SYMBOLS	DIMENSIONS IN MILLIMETER			DIMENSIONS IN INCH			
SIMBOLS	MIN	NOM	MAX	MIN	NOM	MAX	
A	1.35	1.60	1.75	0.053	0.063	0.069	
A1	0.10		0.25	0.004		0.010	
A2		1.45			0.057		
В	0.33		0.51	0.013		0.020	
С	0.19		0.25	0.007		0.010	
D	4.80		5.00	0.189		0.197	
E 1	3.80	3.90	4.00	0.150	0.153	0.157	
e		1.27			0.050		
Е	5.80	6.00	6.20	0.228	0.236	0.244	
L	0.40		1.27	0.016		0.050	
y			0.10			0.004	
θ	0°		8°	0°		8°	
L1	0.95	1.05	1.15	0.037	0.041	0.045	

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