Offline Isolated Flyback LED Controller with Primary Side Control

Features

- Primary side regulation control, no opto-isolator required for regulation
- Primary side supply chip power, simplify transformer design
- 8VAC～265VAC wide input voltage
- Simple application design
- Accurate regulated LED current
- Chip PWM dimming control
- High line & load regulation
- LED short & open protection
- Primary side OCP, supply UVLO protection
- ZCD short protection
- CS sense resistor open protection
- OTP protection
- Cycle-by-Cycle current control and protection
- Patent pending sense architecture
- 8 pin SOP package

Applications

- AC/DC or DC/DC LED driver applications
- LED housekeeping power supplies
- Other LED constant current application

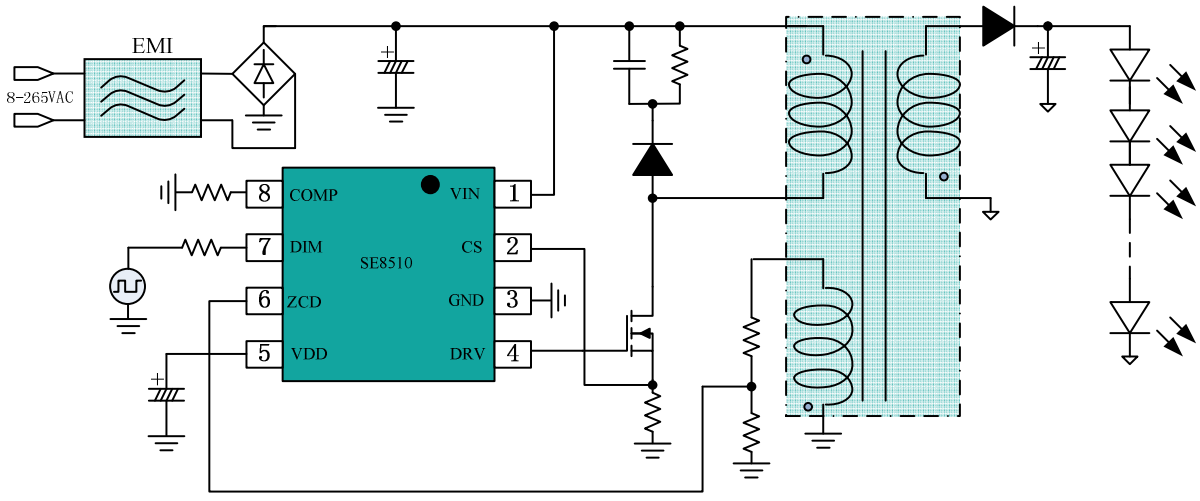
Description

SE8510 is an offline isolated flyback led controller with primary side control IC. Boundary mode operation provides a small magnetic solution with excellent load regulation. Using a patent pending current sensing scheme, the controller is able to deliver a well regulated current to the secondary side without using an opto-coupler.

SE8510 uses a rugged high voltage junction isolated process that can withstand an input voltage surge of up to 450V. Then SE8510 can be powered directly by main line input voltage source, this simplify the design of transformer for wide output voltage. Also the total application design becomes simpler than before.

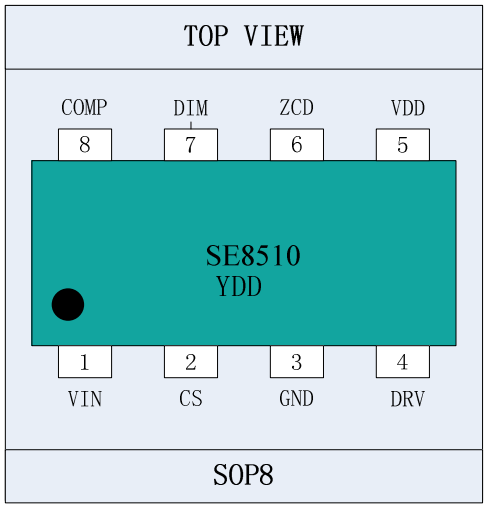
SE8510 has PWM dimming control feature. Additional features include thermal shutdown, current limit, open & short LED load and VDD under-voltage lockout.

Typical Application



Typical SE8510 LED Driver Application Circuit

Pin Configuration



Pin Descriptions

Name	SOP8	Description
VIN	1	Input voltage 8V to 400V DC
CS	2	Senses primary current
GND	3	Device ground
DRV	4	Drives the gate of the external MOSFET
VDD	5	Internally regulated supply voltage
ZCD	6	feedback terminal to detect zero current
DIM	7	PWM dimming pin
COMP	8	power compensation

Order Information

Type	Package	Logo	Temp	Tape and Reel
SE8510	SOP8	SE8510 YDD	-40℃ to 85℃	3K

SE8510, SE- Company Logo，8510 - Chip Type，Y - Year，DD - Production Code

Absolute Maximum Ratings

VIN to GND	-0.3V to +450V
CS，VID, DRV, ZCD, LD, DIM, COMP to GND	-0.3V to (VDD+0.3V)
Junction Temperature	+150℃
Storage Temperature Range	-65℃ to +150℃

Note: Absolute maximum ratings are limits beyond which damage to the device may occur. Operating Ratings are conditions for which the device is intended to be functional, but device parameter specifications may not be guaranteed.

Electrical Characteristics

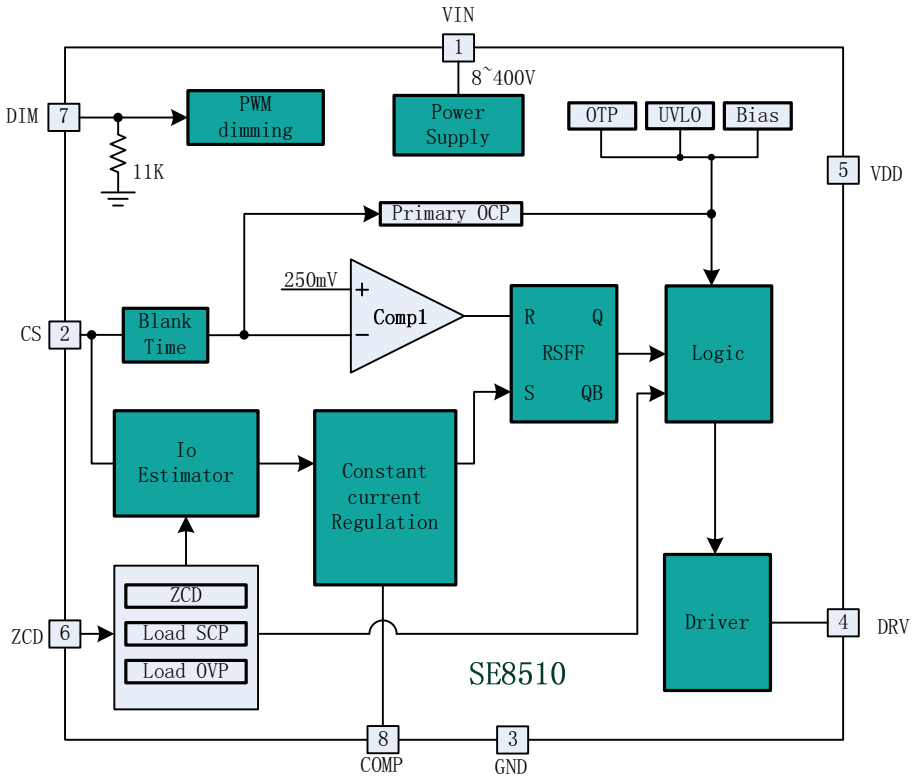
（TA = 25℃, Vdd = 7.5V, Vin=12V unless noted otherwi se）

Symbol	Parameter	Min	Typical	Max	Unit	Condition
V_{IN}	Input DC supply voltage range [1]	8		400	V	DC input voltage
I_{IN}	supply current	-	2	3	mA	
V_{DD}	Internally regulated voltage	7.0	7.5	8.0	V	V_{IN}= 8-400V, pin DRV open
I_{DD}	VDD current for external circuitry	-	-	0.5	mA	V_{IN} = 8 - 100V
UVLO	VDD under voltage lockout threshold	4.5	5.0	6.5	V	V_{IN} rise
ΔUVLO	VDD under voltage lockout hysteresis	-	1.5	-	V	V_{IN} fall
V_{CS}	Current sense threshold voltage	225	250	275	mV	Temp=-40℃ to +85℃
V_{DRV}(hi)	DRV high output voltage	V_{DD}-0.3	-	V_{DD}	V	I_{OUT} = 5mA
V_{DRV}(lo)	DRV low output voltage	0	-	0.3	V	I_{OUT} = -5mA
R_{DIM}	Pin DIM pull-down resistance		11.0		KΩ	For SE8510
V_{DIM}	Pin DIM voltage range [2]	0	-	1	V	TEMP ≤ 85℃, for SE8510
V_{DIM-th}	Pin DIM threshold voltage range	0.20	0.25	0.30	V	TEMP ≤ 85℃, for SE8510
T_{BLANK}	Current sense blanking interval	300	450	600	ns	V_{CS} ≥ 300mV,V_{LD}=V_{DD}
T_{RISE}	DRV rise time	-	30	50	ns	C_{GATE} = 500pF
T_{FALL}	DRV fall time	-	30	50	ns	C_{GATE} = 500pF
V_{ZCD}	ZCD feedback comparator threshold		350		mV	
V_{OVP}	ZCD over voltage threshold		1.2		V	
I_{OCP}	primary side over current threshold		4.8*I_{PK}		A	I_{PK}=primary peak current
T_{SD}	Thermal shutdown temperature		160		℃	
T_{hy}	Thermal shutdown hysteresis		70		℃	

1. Also limited by package power dissipation limit, whichever is lower.

2. Due to one internal 11K resistor If the voltage of pin DIM is too large there will be a large current.

SIMPLIFIED INTERNAL BLOCK DIAGRAM



Detailed Description

SE8510 is an offline isolated flyback LED controller with primary side control IC. It combines a high performance low voltage PWM controller chip and an input high voltage MOSFET that can withstand an input voltage surge of up to 450V. Using a patent pending current sensing scheme, the controller is able to deliver a well regulated current to the secondary side without using an opto-coupler. Moreover it guarantees a safe operation when short circuit of LEDs occurs, or open load circuit.

Supply Power

As shown in block diagram, chip current is internally generated in SE8510 without using bulky startup resistors typically required in the offline applications. Moreover, in many applications the SE8510 can be continuously powered using its internal linear regulator that provides a regulated voltage of 7.5V for all internal circuits.

Its supply is from the main line source. By this way the design of application system becomes simpler than before.

Discontinuous Conduction Mode

Critical conduction mode is a variable frequency switching scheme that always returns the secondary current to zero with every cycle. The SE8510 relies on boundary mode or discontinuous mode to calculate the critical current because the sensing scheme assumes the secondary current returns to zero with every cycle. The ZCD pin uses a patented schematic to determine the zero current point.

Primary-Side Current Control

When the voltage at CS pin exceeds a peak current sense voltage threshold, the gate drive signal terminates, and the power MOSFET turns off. The threshold is internally set to 250mV. The primary peak current can be calculated as:

$$I_{pk1} = \frac{250mV}{R_{cs}} \quad \text{.....(1)}$$

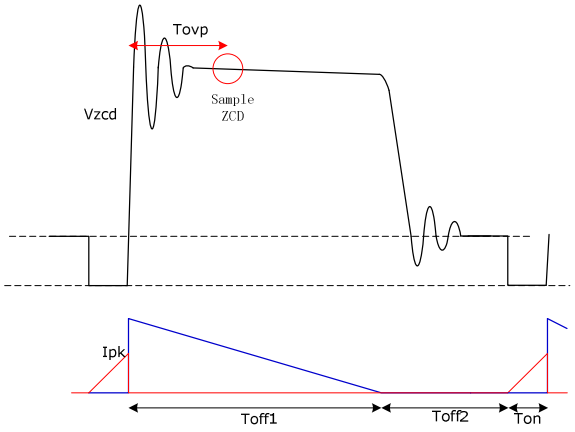


Figure 1. Discontinuous Conduction Mode

Secondary-Side(load) Current Control

SE8510 controls the secondary side LED current from the primary side information. The output LED mean current can be calculated approximately as:

$$I_{out} = \frac{62.5m \times N}{R_{cs}} \quad \text{.....(2)}$$

or,

$$I_{out} = \frac{I_{pk1} \times N}{4} \quad \text{.....(3)}$$

N — Turn ratio of primary side to secondary side

R_{CS} — primary side current sense resistor

I_{PK1} — primary side peak current

Switching Frequency

The Switching Frequency of SE8510 can be calculate as:

$$F_{osc} = \frac{V_{out}}{2NL_{sec}I_{pk1}} \quad \text{.....(4)}$$

or,

$$F_{osc} = \frac{V_{out}}{8L_{sec}I_{out}} \quad \text{.....(5)}$$

V_{OUT} — output load voltage

N — Turn ratio of primary side to secondary side

L_{SEC} — secondary side inductance

I_{PK1} — primary side peak current

It is better to set the frequency lower than 70KHz. The recommend value is 45KHz.

PWM dimming Feature (pin DIM)

PWM dimming can be achieved by driving the DIM pin with a low frequency square wave signal. When the PWM signal is zero, the GATE driver is turned off and when the PWMD signal is high(>300mV,please refer to the note 2), the GATE driver is enabled. The LED current is proportional to the duty cycle of PWM wave.

Output Over Voltage & Open Load Protection (OVP)

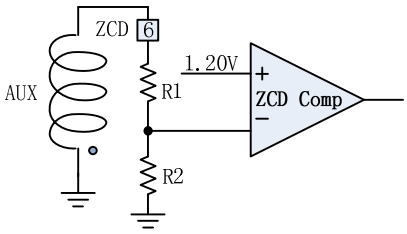


Figure 2. Over Voltage Protection

After the MOSFET turns off, the ZCD comparator detects the level of pin ZCD. Once its level is larger than 1.2v the gate driver will be turned off. To avoid mis-trigger OVP the ZCD OVP function does not work until the blank time Tzcd (fig.1), the typical interval is 1us.

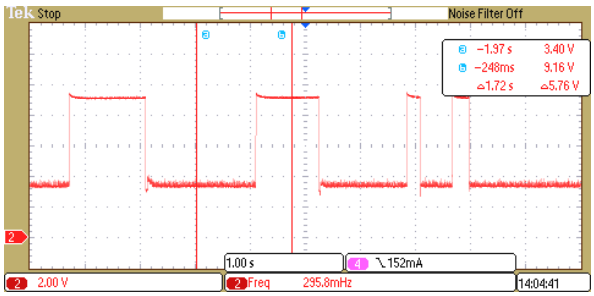
The OVP threshold is:

$$V_{ovp} = 1.2 \times \left(1 + \frac{R_1}{R_2}\right)\frac{N_{sec}}{N_{aux}} \quad \text{.....(6)}$$

N_{SEC}—The secondary winding turns

N_{AUX}—The auxiliary winding turns

The below wave records four OVP events.



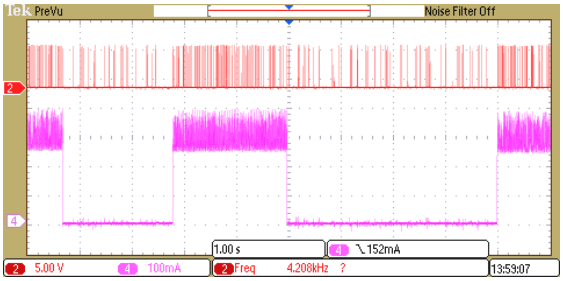
Primary-Side Current Limit

When the primary current exceeds a limit, the chip will shut down, and then wait for a moment to restart. The limit is 4.8*I_{PK}.

Output short Circuit Protection (OSCP)

Chip can control the output current, but when OSCP the better way is to disable PWM signal. SE8510 will trigger OSCP after short LED series. After a fixed time the chip will restart to try whether the OSCP is still here. If so, the chip will re-disable PWM signal.

The below wave records two output short events.



Hysteretic Thermal Shutdown

The thermal shutdown circuitry senses the controller die temperature. The threshold is set at 160 ℃ typical with a 70 ℃ hysteresis.

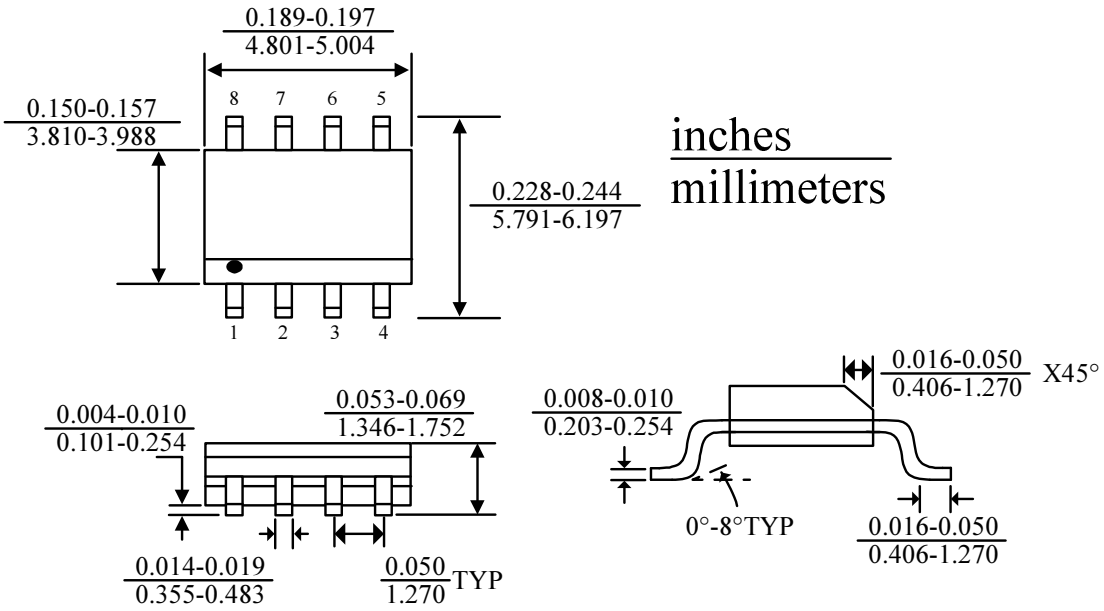
Application Example

Please refer to document AN26 which has one detailed design example, or contact someone of the company.

The final application is shown in figure 3.

Package Information

SOP8



Version

Version	Date	Note
V1.0	2013.05.23	the first version

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Application Schematic

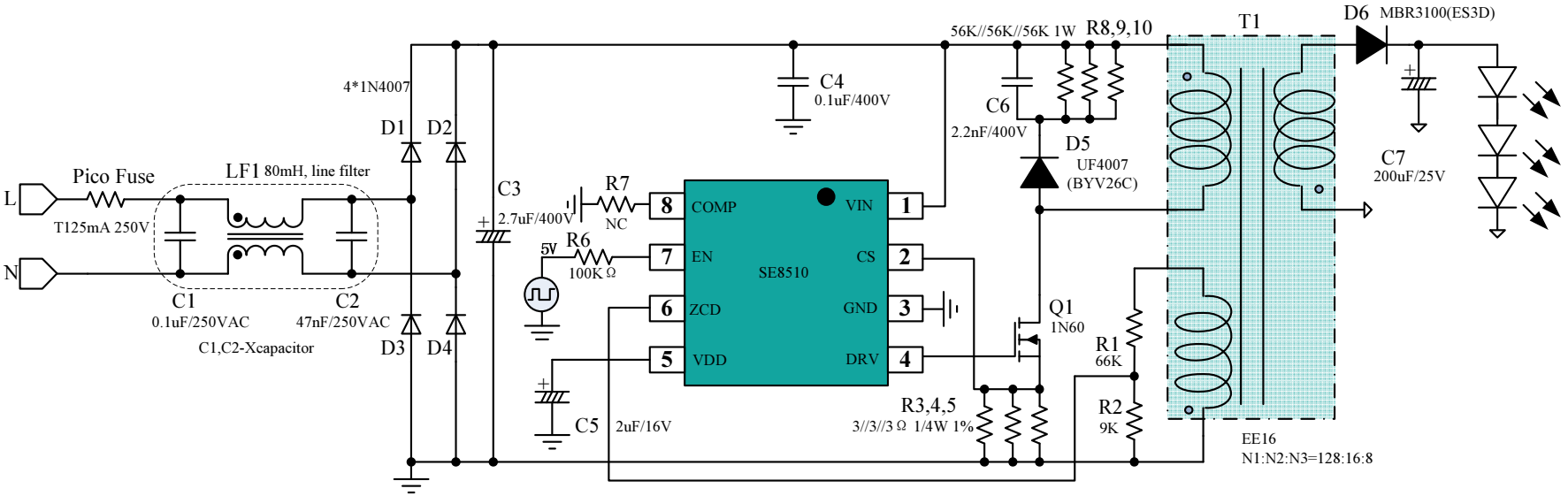

Figure 3. Application Schematic 9.6V/500mA LED driver